

# FMV06N60ES

#### **FUJI POWER MOSFET**

# Super FAP-E<sup>3S</sup> series

### **N-CHANNEL SILICON POWER MOSFET**

#### Features

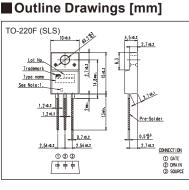
Maintains both low power loss and low noise Lower R<sub>DS</sub>(on) characteristic More controllable switching dv/dt by gate resistance Smaller V<sub>GS</sub> ringing waveform during switching Narrow band of the gate threshold voltage (3.7±0.5V) High avalanche durability

#### Applications

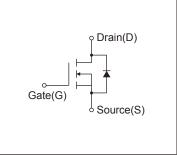
Switching regulators UPS (Uninterruptible Power Supply) **DC-DC** converters

#### Maximum Ratings and Characteristics

#### • Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)



Equivalent circuit schematic



Description	Symbol	Characteristics	Unit	Remarks
Drain Source Veltere	VDS	600	V	
Drain-Source Voltage	VDSX	600	V	V <sub>GS</sub> = -30V
Continuous Drain Current	lo	±6	А	
Pulsed Drain Current	IDP	±24	А	
Gate-Source Voltage	Vgs	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	lar	6	А	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	313.7	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	3.7	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	3.8	kV/µs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Maximum Bawar Dissinction	PD	2.16	W	Ta=25°C
Maximum Power Dissipation		37	VV	Tc=25°C
On anothing and Otamora Tananatana anana	Tch	150	°C	
Operating and Storage Temperature range	Tstg	-55 to +150	°C	
Isolation Voltage	VISO	2	kVrms	t = 60sec, f = 60Hz

#### • Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit	
Drain-Source Breakdown Voltage	BVDSS	I <sub>D</sub> =250µA, V <sub>GS</sub> =0V		600	-	-	V	
Gate Threshold Voltage	Vgs (th)	ID=250µA, VDS=VGS		3.2	3.7	4.2	V	
Zero Gate Voltage Drain Current		V <sub>DS</sub> =600V, V <sub>GS</sub> =0V	Tch=25°C	-	-	25		
	IDSS	V <sub>DS</sub> =480V, V <sub>GS</sub> =0V	Tch=125°C	-	-	250	μA	
Gate-Source Leakage Current	Igss	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V		10	100	nA	
Drain-Source On-State Resistance	RDS (ON)	ID=3A, VGS=10V	ID=3A, VGS=10V		1.03	1.20	Ω	
Forward Transconductance	<b>g</b> fs	ID=3.0A, VDS=25V	ID=3.0A, VDS=25V		5	-	S	
Input Capacitance	Ciss	V <sub>DS</sub> =25V V <sub>GS</sub> =0V f=1MHz		-	950	1425	pF	
Output Capacitance	Coss			-	100	150		
Reverse Transfer Capacitance	Crss			-	7.5	11		
Turn-On Time	td(on)	V <sub>cc</sub> =300V V <sub>GS</sub> =10V I <sub>D</sub> =3.0A R <sub>G</sub> =27Ω		-	29	43.5	ns	
	tr			-	15	22.5		
Turn-Off Time	td(off)			-	75	113		
	tf			-	16	24		
Total Gate Charge	QG	V <sub>cc</sub> =300V I <sub>D</sub> =6A V <sub>GS</sub> =10V		-	31	46.5	nC	
Gate-Source Charge	QGS			-	10.5	15.8		
Gate-Drain Charge	QGD			-	8	12		
Gate-Drain Crossover Charge	Qsw			-	4.5	6.75		
Avalanche Capability	lav	L=6.39mH, T <sub>ch</sub> =25°C		6	-	-	A	
Diode Forward On-Voltage	Vsd	IF=6A, VGS=0V, Tch=25°C		-	0.90	1.35	V	
Reverse Recovery Time	trr	IF=6A, VGS=0V		-	0.4	-	μS	
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	3.3	-	μC	

#### Thermal Characteristics

Description Symbol		Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to case			3.38	°C/W
	Rth (ch-a)	Channel to ambient			58.0	°C/W

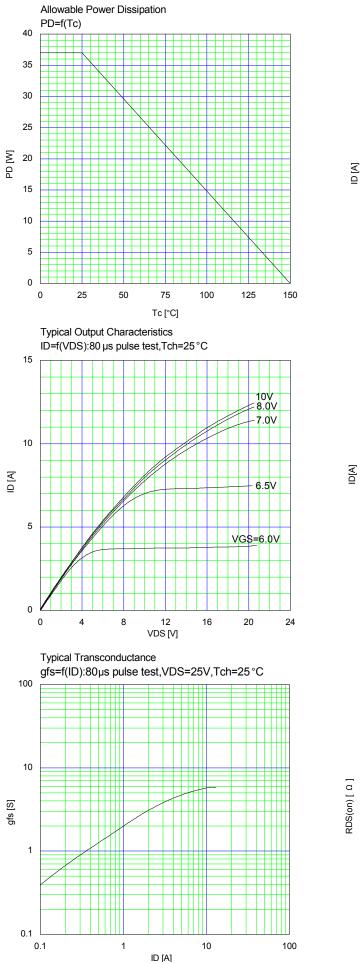
Note \*1 : Tch≤150°C

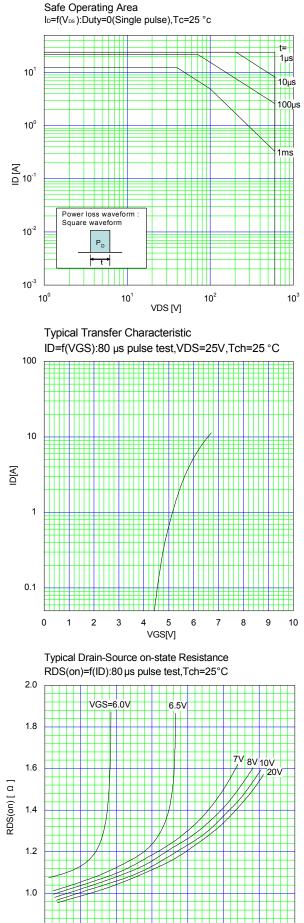
Note 1 : Italia 50 °C, IAs=2 4A, L=99.8mH, Vcc=60V, Rg=50Ω EAs limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph.

Note \*3 : Repetitive rating : Pulse width limited by maximum channel temperature

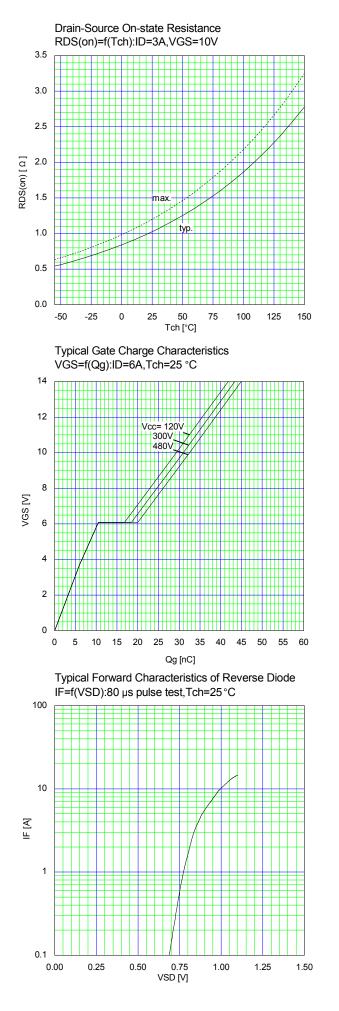
See to the 'Transient Themal impeadance' graph.

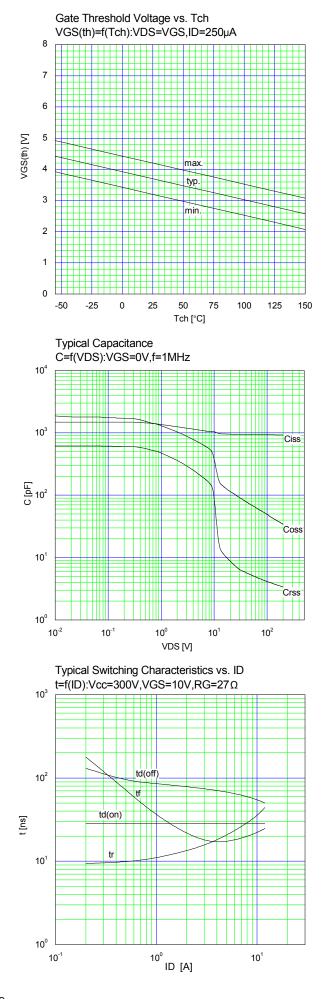
Note \*4 : Ir<-ID, -di/dt=100A/µs, Vcc≤BVDss, Tch≤150°C. Note \*5 : Ir<-ID, dv/dt=3.8kV/µs, Vcc≤BVDss, Tch≤150°C.

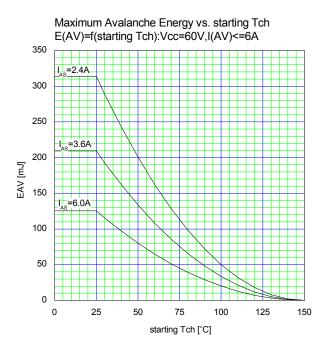




0.8 0

ID [A] 





Maximum Transient Thermal Impedance Zth(ch-c)=f(t):D=0

10<sup>-3</sup>

t [sec]

10<sup>-2</sup>

10<sup>-1</sup>

10<sup>0</sup>

Zth(ch-c) [°C/W]

10<sup>-2</sup>

10<sup>-3</sup>

10<sup>-6</sup>

10-5

10-4

4

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		WARNING		
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