

FMV13N60E

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

■ Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (3.0±0.5V) High avalanche durability

Applications

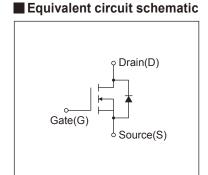
Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

TO-220F(SLS)	
10 m.5 M.7	\$5.50.2 2.7.0.2 Pre-Solder 0.513 2.7.0.2 OMECTION © DATE © DATE © DATE © DATE

■ Outline Drawings [mm]



Description	Symbol	Characteristics	Unit	Remarks
Proin Source Voltage	V _{DS}	600	V	
Drain-Source Voltage	V _{DSX}	600	V	V _{GS} = -30V
Continuous Drain Current	ID	±13	A	
Pulsed Drain Current	IDP	±52	Α	
Gate-Source Voltage	V _G s	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	Iar	13	А	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	471.5	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	8	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	5.2	kV/μs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Maximum Bawar Biosination	PD	2.16	W	Ta=25°C
Maximum Power Dissipation		80	VV	Tc=25°C
Operating and Storage Temperature range	Tch	150	°C	
	T _{stg}	-55 to + 150	°C	
Isolation Voltage	Viso	2	kVrms	t=60sec, f=60Hz

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit	
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V		600	-	-	V	
Gate Threshold Voltage	V _{GS} (th)	In=250µA, Vns=Vgs	I _D =250µA, V _{DS} =V _{GS}		3.0	3.5	V	
Zero Gate Voltage Drain Current		V _{DS} =600V, V _{GS} =0V	T _{ch} =25°C	-	-	25	μΑ	
	IDSS	V _{DS} =480V, V _{GS} =0V	T _{ch} =125°C	-	-	250		
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V	V _{GS} =±30V, V _{DS} =0V		10	100	nA	
Drain-Source On-State Resistance	Ros (on)	I _D =6.5A, V _{GS} =10V	I _D =6.5A, V _{GS} =10V		0.50	0.58	Ω	
Forward Transconductance	g _{fs}	I _D =6.5A, V _{DS} =25V		7.5	15	-	S	
Input Capacitance	Ciss	V _{DS} =25V		-	2150	3225		
Output Capacitance	Coss	V _{GS} =0V		-	190	285	pF	
Reverse Transfer Capacitance	Crss	f=1MHz		-	14	21	1	
Turn-On Time Turn-Off Time	td(on)	Vcc=300V		-	21	31.5		
	tr	V _{GS} =10V		-	8	12	1	
	td(off)	ID=6.5A		-	100	150	ns	
	tf	R _{GS} =10Ω		-	15	22.5	i	
Total Gate Charge	Q _G	V _{cc} =300V I _D =13A V _{GS} =10V		-	60	90		
Gate-Source Charge	Qgs			-	17	25.5	nC	
Gate-Drain Charge	Q _{GD}			-	18	27	1	
Avalanche Capability	lav	L=2.36mH, Tch=25°C	L=2.36mH, Tch=25°C		-	-	А	
Diode Forward On-Voltage	V _{SD}	I _F =13A, V _{GS} =0V, T _{ch} =25°	I _F =13A, V _{GS} =0V, T _{ch} =25°C		0.90	1.08	V	
Reverse Recovery Time	trr	I _F =13A, V _{GS} =0V	I _F =13A, V _{GS} =0V		0.7	-	μs	
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25	-di/dt=100A/µs, Tch=25°C		8	-	μC	

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to case			1.560	°C/W
	Rth (ch-a)	Channel to ambient			58.0	°C/W

Note *1 : Tch≤150°C

Note 12: Stating Tch=25°C, Ias=6A, L=24.0mH, Vcc=60V, R_G=50Ω

Eas limited by maximum channel temperature and avalanche current.

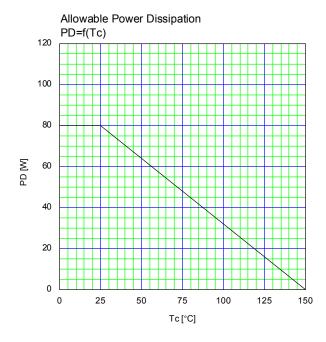
See to 'Avalanche Energy' graph.

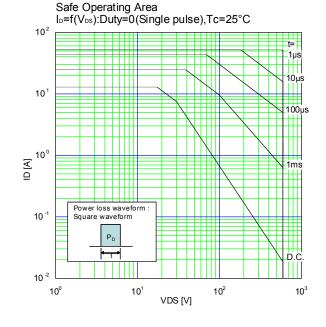
Note $^{\star}3$: Repetitive rating : Pulse width limited by maximum channel temperature

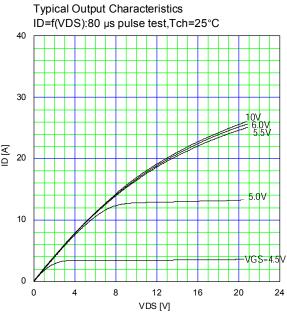
See to the 'Transient Themal impeadance' graph.

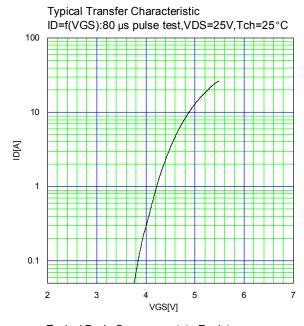
Note *4 : I₅≤-I₀, -di/dt=100A/μ₅, Vcc≤BVbss, Tch≤150°C.

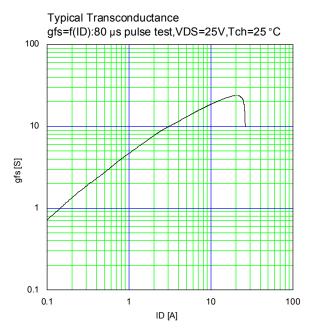
Note *5 : I₅≤-I₀, dv/dt=5.2kV/μ₅, Vcc≤BVbss, Tch≤150°C.

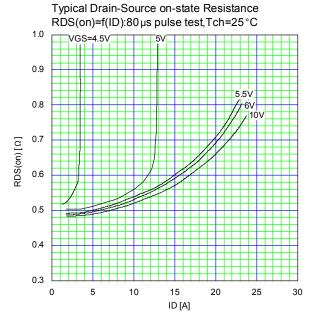


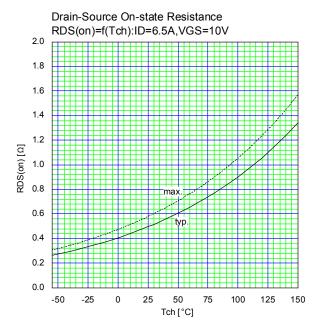


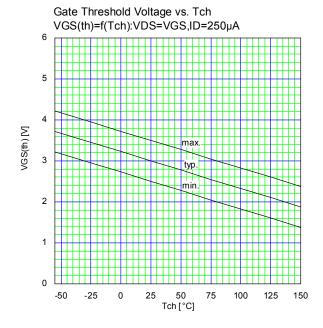


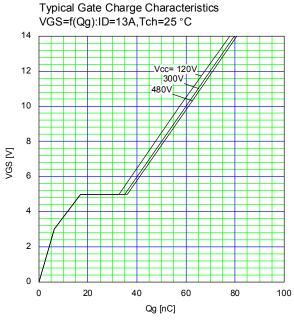


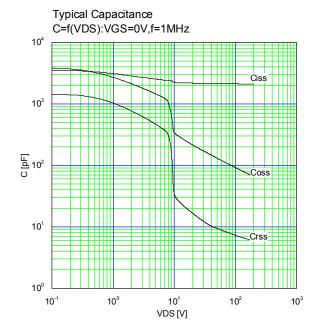


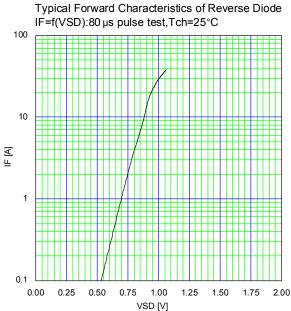


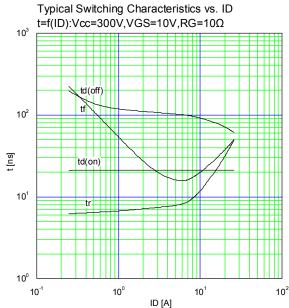


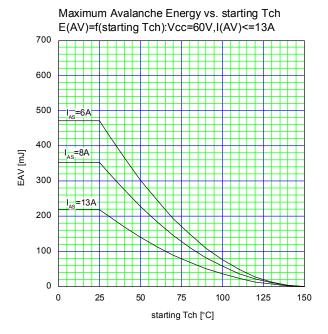


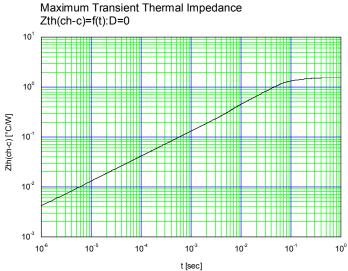












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