

FNK06N02C N-Channel Enhancement Mode Power MOSFET

DESCRIPTION

The FNK06N02C uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V.

$$I_D = 50A$$

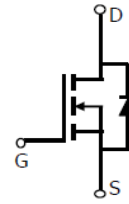
$$R_{DS(ON)} < 7.5m\Omega @ V_{GS}=4.5V$$

$$R_{DS(ON)} < 10.0m\Omega @ V_{GS}=2.5V$$

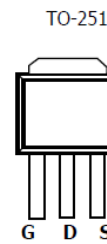
- High Power and current handing capability
- Lead free product is acquired

Application

- Battery Switch
- Load switch
- Power management



Schematic diagram



Top View
Drain Connected
to Tab

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
06N02C	FNK06N02C	TO-251			

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current ($T_J = 150^\circ C$)	I_D	$T_C = 25$	50
		$T_C = 70^\circ C$	20
		$T_A = 25^\circ C$	20
		$T_A = 70^\circ C$	15
Drain Current-Pulsed (Note 1)	I_{DM}	80	A
Maximum Power Dissipation	P_D	50	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	40	$^\circ C/W$
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Electrical Characteristics ($T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	20		-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20V, V_{GS}=0V$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	0.5	0.75	1	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=4.5V, I_D=8A$	-	7	7.5	m Ω
		$V_{GS}=2.5V, I_D=6.5A$	-	9	10	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=15V, I_D=8A$	10	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C_{iss}	$V_{DS}=15V, V_{GS}=0V,$ $F=1.0MHz$	-	1600	-	PF
Output Capacitance	C_{oss}		-	350	-	PF
Reverse Transfer Capacitance	C_{rss}		-	300	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-15V, I_D=1A,$ $V_{GS}=10V, R_{GEN}=6\Omega$	-	10	-	nS
Turn-on Rise Time	t_r		-	15	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	110	-	nS
Turn-Off Fall Time	t_f			70	-	nS
Total Gate Charge	Q_g	$V_{DS}=15V, I_D=8A$ $V_{GS}=10V$	-	30	-	nC
Gate-Source Charge	Q_{gs}		-	5.5	-	nC
Gate-Drain Charge	Q_{gd}		-	8	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_S=2.1A$	-	-	1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.