

FNK N-Channel Power MOSFET

Description

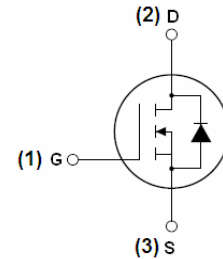
The FNK1404 uses advanced trench technology and design to provide excellent $R_{ds(on)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

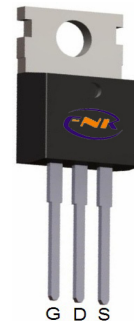
- $V_{DS} = 45V$, $I_D = 205A$
 $R_{ds(on)} < 4m\Omega$ @ $V_{GS} = 10V$ (Typ: $2.8m\Omega$)
- High density cell design for ultra low $R_{ds(on)}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high EAS
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- E-Tools
- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



To-220 Top View

Package Marking and Ordering Information

Device Marking	Device Package	Form	Minimum Quantity
FNK1404	TO-220	Tube	1000

Absolute Maximum Ratings

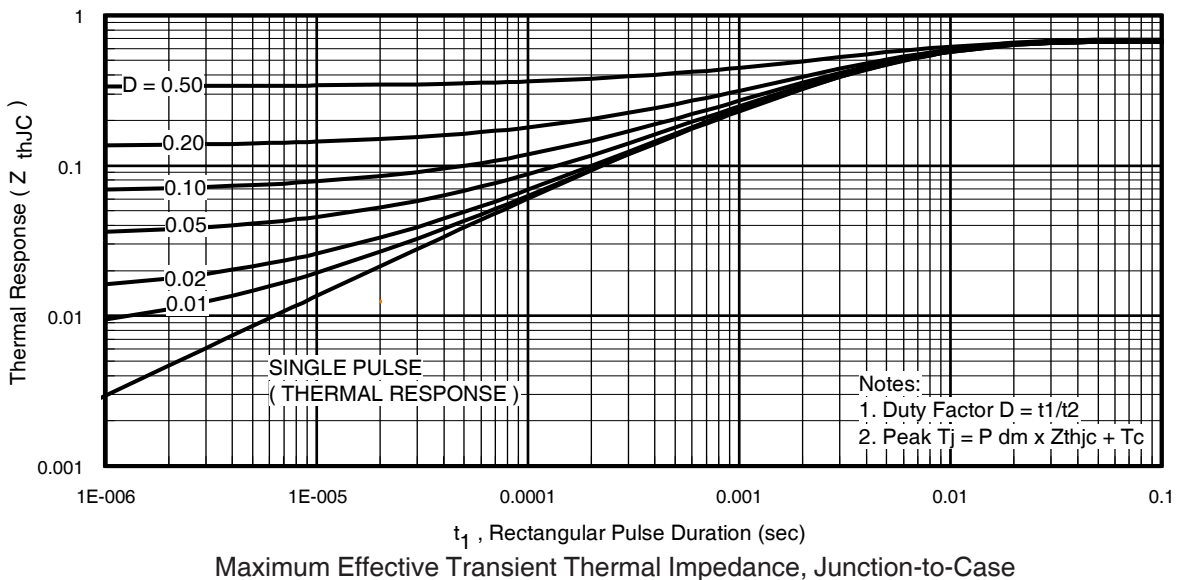
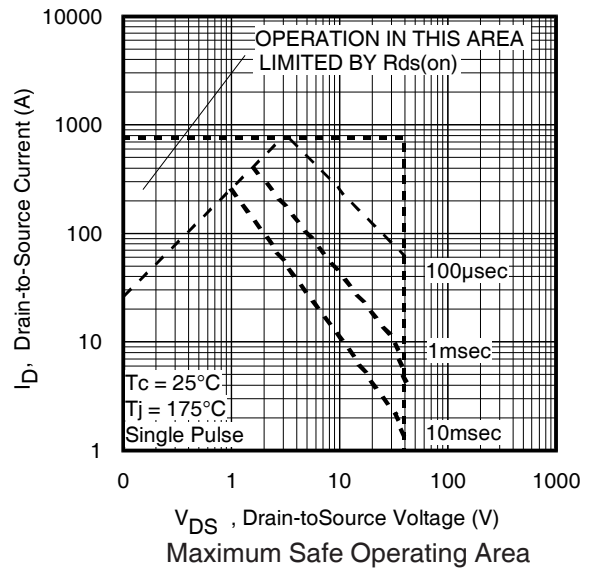
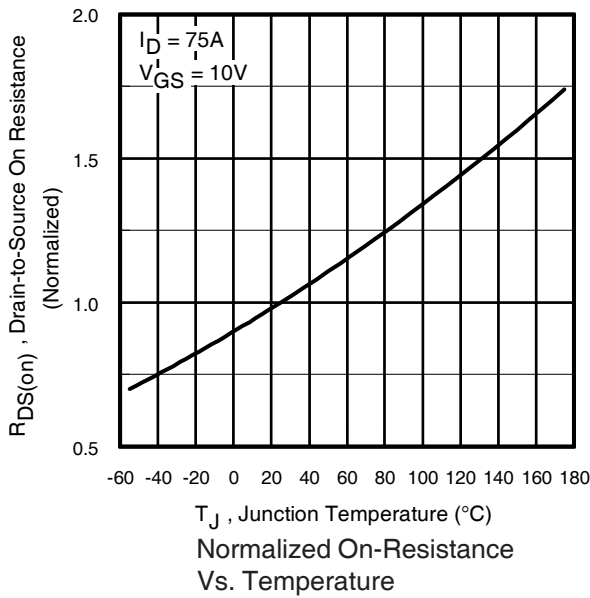
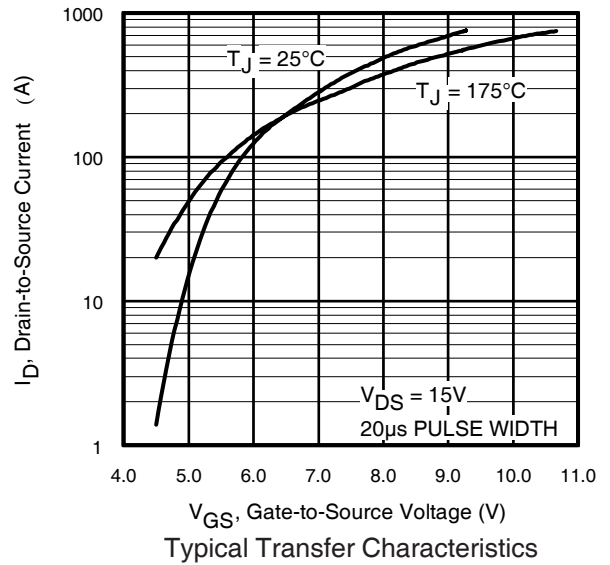
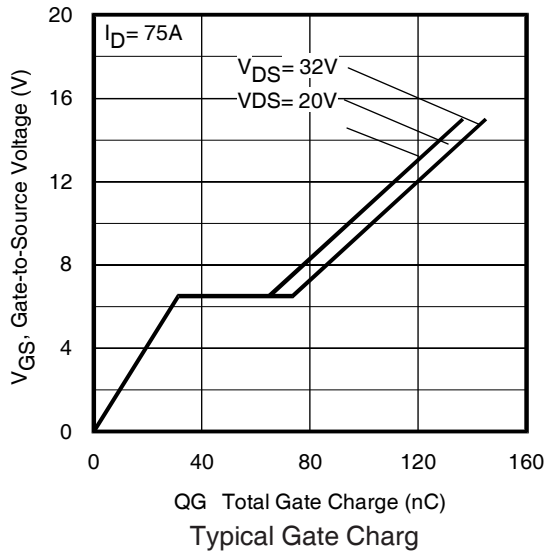
Symbol	Parameter	Max.	Unit
V_{ds}	Drain-Source Voltage	45	V
V_{gs}	Gate-Source Voltage	± 20	V
I_D (25°C)	Drain Current-Continuous ($T_c = 25^\circ C$)	205	A
I_D (100°C)	Drain Current-Continuous ($T_c = 100^\circ C$)	140	A
I_{dm}	Pulsed Drain Current (Note 1,3)	820	A
P_d	Maximum Power Dissipation	300	W
EAS	Single pulse avalanche energy (Note 5)	1010	mJ
T_j	Operating Junction and Storage Temperature Range	-55—175	°C
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case (Note 2)	0.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	65	°C/W

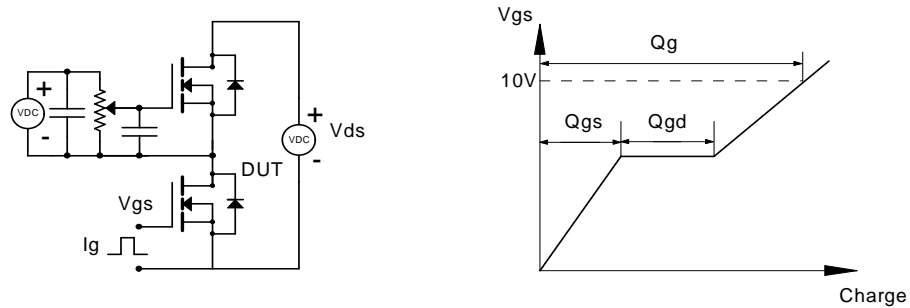
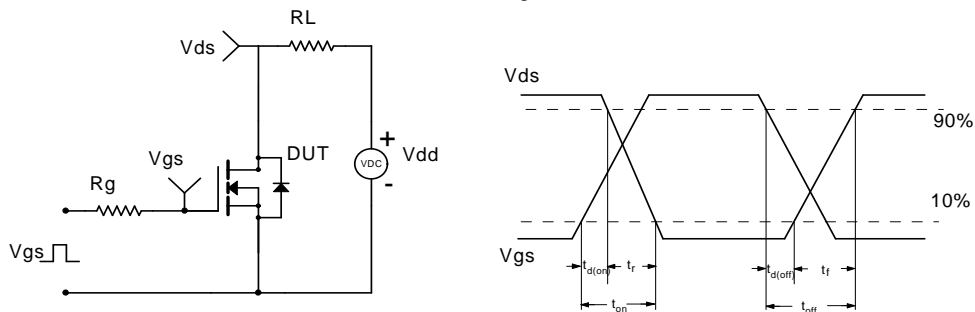
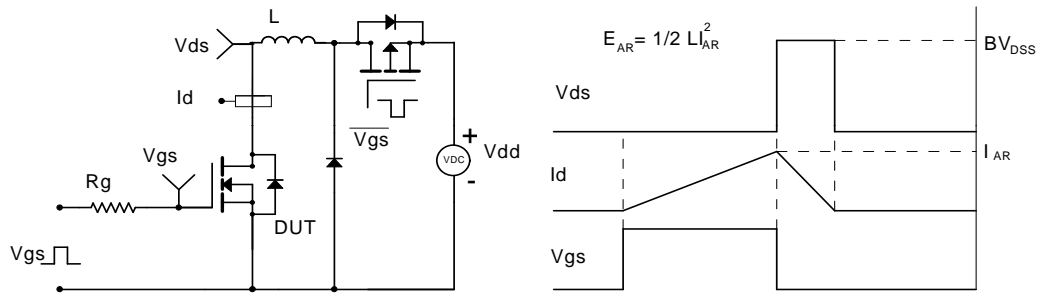
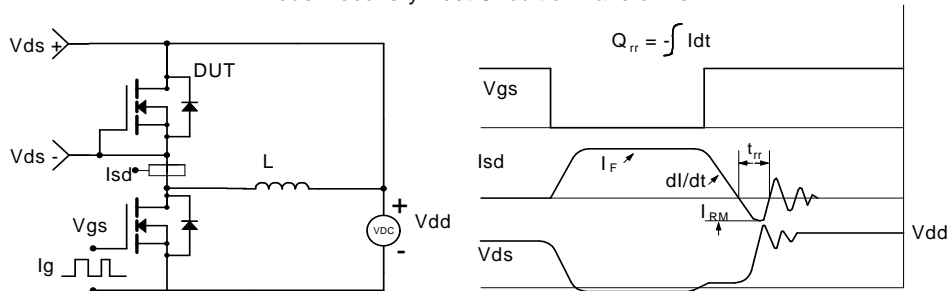
I _{DSS}	Zero Gate Voltage Drain Current	—	—	5	μA	V _{GS} =0V, V _{DS} =V _{DS}
I _{GSS}	Gate-Body Leakage Current	—	—	±100	nA	V _{GS} =±20V, V _{DS} =0V
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	2.5	3.0	3.5	V	V _{DS} =V _{GS} , I _D =250μA
R _{DS(on)} @10	Drain-Source On-State Resistance	—	2.8	4.2	mΩ	V _{GS} =10V, I _D =I _D /4
g _{fs}	Forward Transconductance	—	78	—	S	V _{DS} =V _{DS} /2, I _D =I _D /4
Dynamic Characteristics (Note4)						
C _{iss}	Input Capacitance	—	8120	—	pF	V _{DS} =V _{DS} /2 V _{GS} =0V F=1.0MHz
C _{oss}	Output Capacitance	—	1370	—	pF	
C _{rss}	Reverse Transfer Capacitance	—	910	—	pF	
Switching Characteristics (Note 4)						
T _{d(on)}	Turn-on Delay Time	—	19	—	nS	V _{DS} =V _{DS} *0.75 I _D =I _D /2 R _G =2.5Ω V _{GS} =10V
T _r	Turn-on Rise Time	—	206	—	nS	
T _{d(off)}	Turn-Off Delay Time	—	53	—	nS	
T _f	Turn-Off Fall Time	—	38	—	nS	
Q _g	Total Gate Charge	—	135	200	nC	I _D =I _D /2 V _{DS} =V _{DS} *0.75 V _{GS} =10V
Q _{gs}	Gate-Source Charge	—	40	65	nC	
Q _{gd}	Gate-Drain Charge	—	42	71	nC	
R _g	Gate-Drain Charge	2.1	2.8	3.5	Ω	F=1MHz, open drain
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage (Note 3)	—	0.65	1	V	I _S =I _D /2, V _{GS} =0V
T _{rr}	Reverse Recovery Time	—	85	125	nS	I _F =I _D /2, V _R =V _{DS} *0.75 di/dt = 100A/μs
Q _{rr}	Reverse Recovery Charge	—	173	240	nC	
T _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

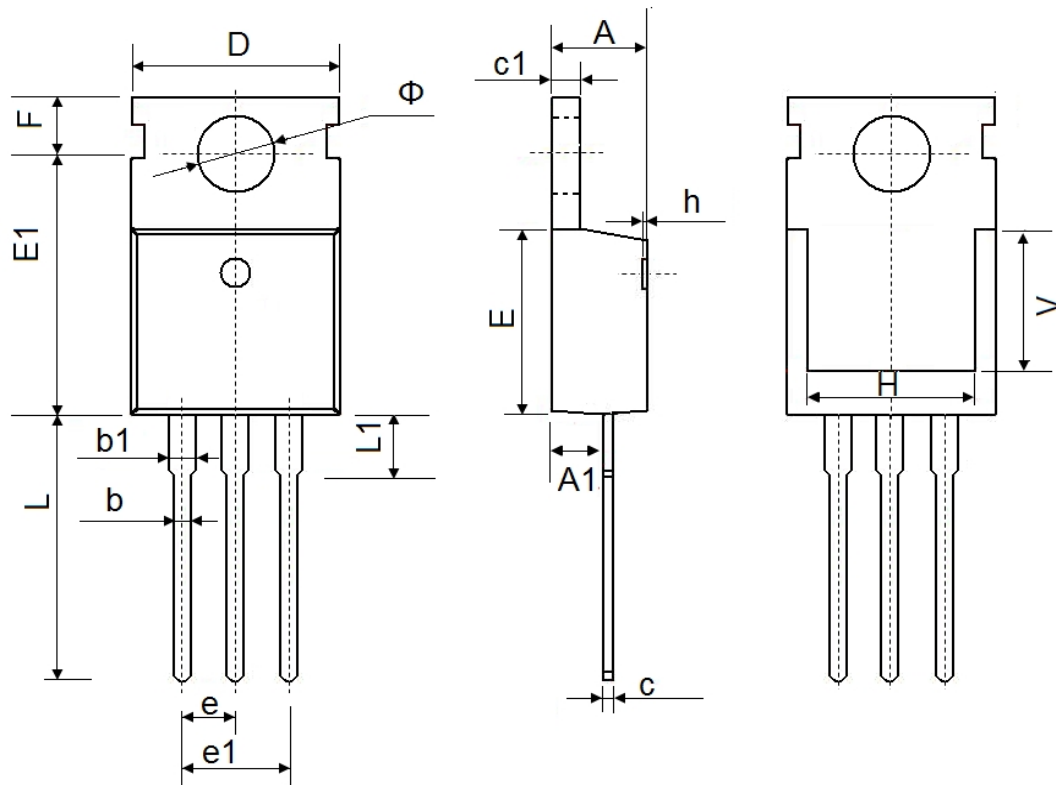
Notes:

- ①.Repetitive Rating: Pulse width limited by maximum junction temperature.
- ②.Surface Mounted on FR4 Board, t ≤ 10 sec.
- ③.Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
- ④. Guaranteed by design, not subject to production
- ⑤. EAS condition : T_j=25°C, V_{DD}=40V, V_G=10V, L=0.5mH, R_g=25Ω

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



TEST CIRCUIT
Gate Charge Test Circuit & Waveform

Resistive Switching Test Circuit & Waveforms

Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

Diode Recovery Test Circuit & Waveforms


Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.9500	9.750	0.352	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	7.500 REF.		0.295 REF.	
Φ	3.400	3.800	0.134	0.150

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