



# FOD050L, FOD250L: Single Channel FOD053L: Dual Channel LVTTTL/LVCMOS 3.3V High Speed Transistor Optocouplers

## Features

- Low power consumption
- High speed
- Available in single channel 8-pin DIP (FOD250L), 8-pin SOIC (FOD050L) or dual channel 8-pin SOIC (FOD053L)
- Superior CMR –  $CM_H = 50kV/\mu s$  (typical) and  $CM_L = 35kV/\mu s$  (typical)
- Guaranteed performance over temperature: 0°C to 70°C
- U.L. recognized (File # E90700)
- VDE pending

## Applications

- Line receivers
- Pulse transformer replacement
- High speed logic ground isolation: LVTTTL/LVCMOS
- Wide bandwidth analog coupling

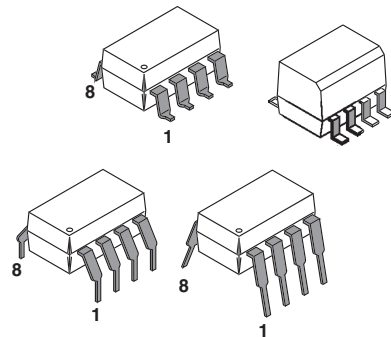
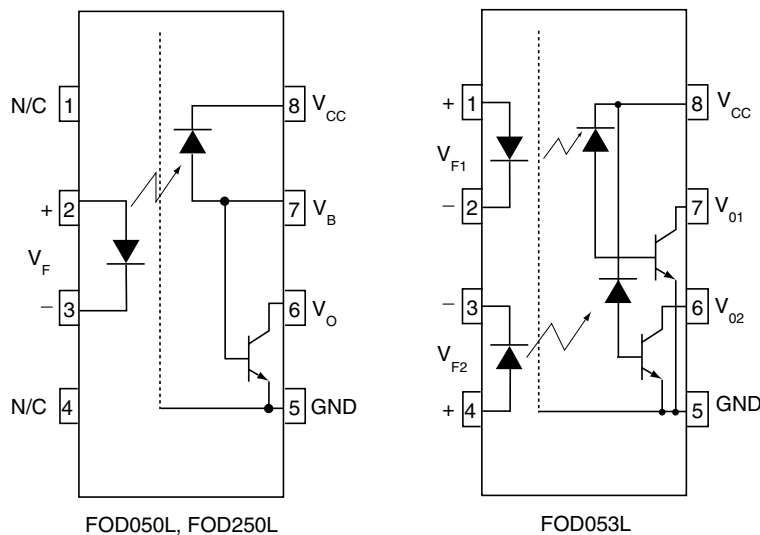
## Description

The FOD250L, FOD050L and FOD053L optocouplers consist of an AlGaAs LED optically coupled to a high speed photodetector transistor. These devices are specified for operation at a 3.3V supply voltage.

A separate connection for the bias of the photodiode improves the speed by several orders of magnitude over conventional phototransistor optocouplers by reducing the base-collector capacitance of the input transistor.

An internal noise shield provides superior common mode rejection of  $CM_H = 50kV/ms$  (typical) and  $CM_L = 35kV/ms$  (typical).

## Schematic



### Truth Table

LED	V <sub>O</sub>
On	LOW
Off	HIGH

**Absolute Maximum Ratings** ( $T_A = 25^\circ\text{C}$  Unless otherwise specified.)

Symbol	Parameter		Value	Units
$T_{STG}$	Storage Temperature		-40 to +125	$^\circ\text{C}$
$T_{OPR}$	Operating Temperature		-40 to +85	$^\circ\text{C}$
$T_{SOL}$	Lead Solder Temperature (Wave solder only)		260 for 10 sec	$^\circ\text{C}$
<b>EMITTER</b>				
$I_F$ (avg)	DC/Average Forward Input Current	Each Channel	25	mA
$I_F$ (pk)	Peak Forward Input Current (50% duty cycle, 1ms P.W.)	Each Channel	50	mA
$I_F$ (trans)	Peak Transient Input Current ( $\leq 1 \mu\text{s}$ P.W., 300pps)	Each Channel	1.0	A
$V_R$	Reverse Input Voltage	Each Channel	5	V
$P_D$	Input Power Dissipation (No derating required up to $85^\circ\text{C}$ )	Each Channel	45	mW
<b>DETECTOR</b>				
$I_O$ (avg)	Average Output Current	Each Channel	8	mA
$I_O$ (pk)	Peak Output Current	Each Channel	16	mA
$V_{EBR}$	Emitter-Base Reverse Voltage	FOD050L, FOD250L only	5	V
$V_{CC}$	Supply Voltage		-0.5 to 7	V
$V_O$	Output Voltage		-0.5 to 7	V
$I_B$	Base Current	FOD050L, FOD250L only	5	mA
$P_D$	Output Power Dissipation (No derating required up to $85^\circ\text{C}$ )	Each Channel	100	mW

## Electrical Characteristics (T<sub>A</sub> = 0 to 70°C Unless otherwise specified.)

### Individual Component Characteristics

Symbol	Parameter	Test Conditions	Device	Min.	Typ.**	Max.	Unit
<b>EMITTER</b>							
V <sub>F</sub>	Input Forward Voltage	I <sub>F</sub> = 16mA, T <sub>A</sub> = 25°C	All		1.45	1.7	V
		I <sub>F</sub> = 16mA				1.8	
B <sub>VR</sub>	Input Reverse Breakdown Voltage	I <sub>R</sub> = 10μA	All	5.0			V
<b>DETECTOR</b>							
I <sub>OH</sub>	Logic High Output Current	I <sub>F</sub> = 0mA, V <sub>O</sub> = V <sub>CC</sub> = 3.3V, T <sub>A</sub> = 25°C	All		0.001	1	μA
I <sub>CCL</sub>	Logic Low Supply Current	I <sub>F</sub> = 16mA, V <sub>O</sub> = Open, V <sub>CC</sub> = 3.3V	FOD050L FOD250L			200	μA
		I <sub>F1</sub> = I <sub>F2</sub> = 16mA, V <sub>O</sub> = Open, V <sub>CC</sub> = 3.3V	FOD053L			400	
I <sub>CCH</sub>	Logic High Supply Current	I <sub>F</sub> = 0mA, V <sub>O</sub> = Open, V <sub>CC</sub> = 3.3V, T <sub>A</sub> = 25°C	FOD050L FOD250L			0.3	μA
		I <sub>F</sub> = 0mA, V <sub>O</sub> = Open, V <sub>CC</sub> = 3.3V	FOD053L			10	

\*\*All Typicals at T<sub>A</sub> = 25°C

### Transfer Characteristics

Symbol	Parameter	Test Conditions	Device	Min.	Typ.**	Max.	Unit
<b>COUPLED</b>							
CTR	Current Transfer Ratio <sup>(1)</sup>	I <sub>F</sub> = 16mA, V <sub>O</sub> = 0.4 V, V <sub>CC</sub> = 3.3V, T <sub>A</sub> = 25°C	All	15		50	%
V <sub>OL</sub>	Logic Low Output Voltage Output Voltage	I <sub>F</sub> = 16mA, I <sub>O</sub> = 3mA, V <sub>CC</sub> = 3.3V, T <sub>A</sub> = 25°C	All			0.3	V

\*\*All Typicals at T<sub>A</sub> = 25°C

#### Note:

1. Current Transfer Ratio is defined as a ratio of output collector current, I<sub>O</sub>, to the forward LED input current, I<sub>F</sub>, times 100%.

### Switching Characteristics (T<sub>A</sub> = 0 to 70°C Unless otherwise specified, V<sub>CC</sub> = 3.3V.)

Symbol	Parameter	Test Conditions	Device	Min.	Typ.**	Max.	Unit
T <sub>PHL</sub>	Propagation Delay Time to Logic Low	R <sub>L</sub> = 1.9kΩ, I <sub>F</sub> = 16mA <sup>(2)</sup> (Fig. 10)	25°C All			1.0 2.0	μs
T <sub>PLH</sub>	Propagation Delay Time to Logic High	R <sub>L</sub> = 1.9kΩ, I <sub>F</sub> = 16mA <sup>(2)</sup> (Fig. 10)	25°C All			1.0 2.0	μs
ICM <sub>H</sub> I	Common Mode Transient Immunity at Logic High	I <sub>F</sub> = 0mA, V <sub>CM</sub> = 1,000V <sub>P-P</sub> , R <sub>L</sub> = 4.1kΩ, T <sub>A</sub> = 25°C <sup>(3,4)</sup> (Fig. 11)	All	5,000	50,000		V/μs
		I <sub>F</sub> = 0mA, V <sub>CM</sub> = 1,000V <sub>P-P</sub> , T <sub>A</sub> = 25°C, R <sub>L</sub> = 1.9kΩ <sup>(2,4)</sup> (Fig. 11)		5,000	50,000		V/μs
ICM <sub>L</sub> I	Common Mode Transient Immunity at Logic Low	I <sub>F</sub> = 16mA, V <sub>CM</sub> = 1,000V <sub>P-P</sub> , R <sub>L</sub> = 4.1kΩ, T <sub>A</sub> = 25°C <sup>(3,4)</sup> (Fig. 11)	All	5,000	35,000		V/μs
		I <sub>F</sub> = 16mA, V <sub>CM</sub> = 1,000V <sub>P-P</sub> , R <sub>L</sub> = 1.9kΩ <sup>(2,4)</sup> (Fig. 11)		5,000	35,000		V/μs

\*\* All Typical at T<sub>A</sub> = 25°C

### Isolation Characteristics (T<sub>A</sub> = 0 to 70°C Unless otherwise specified.)

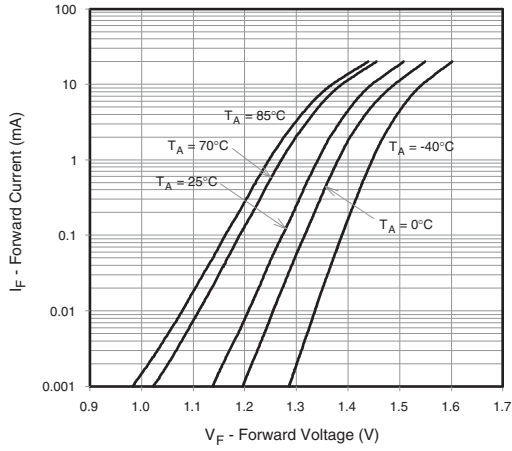
Symbol	Characteristics	Test Conditions	Device	Min.	Typ.**	Max.	Unit
I <sub>I-O</sub>	Input-Output Insulation Leakage Current	Relative humidity = 45%, T <sub>A</sub> = 25°C, t = 5s, V <sub>I-O</sub> = 3000VDC <sup>(5)</sup>	All			1.0	μA
V <sub>ISO</sub>	Withstand Insulation Test Voltage	f = 60Hz, T <sub>A</sub> = 25°C, t = 1 min. <sup>(5)</sup>	FOD050L FOD053L	2500			V <sub>RMS</sub>
			FOD250L	5000			
R <sub>I-O</sub>	Resistance (input to output)	V <sub>I-O</sub> = 500VDC <sup>(5)</sup>	All	10 <sup>11</sup>	10 <sup>12</sup>		Ω
C <sub>I-O</sub>	Capacitance (input to output)	f = 1MHz <sup>(5)</sup>	All		0.2		pF

#### Notes:

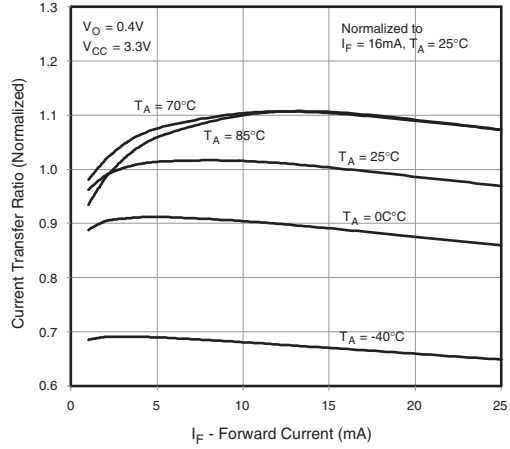
- The 1.9kΩ load represents 1 TTL unit load of 1.6mA and 5.6kΩ pull-up resistor.
- The 4.1kΩ load represents 1 LSTTL unit load of 0.36mA and 6.1kΩ pull-up resistor.
- Common mode transient immunity in logic high level is the maximum tolerable (positive) dV<sub>cm</sub>/dt on the leading edge of the common mode pulse signal V<sub>CM</sub>, to assure that the output will remain in a logic high state (i.e., V<sub>O</sub> > 2.0V). Common mode transient immunity in logic low level is the maximum tolerable (negative) dV<sub>cm</sub>/dt on the trailing edge of the common mode pulse signal, V<sub>CM</sub>, to assure that the output will remain in a logic low state (i.e., V<sub>O</sub> < 0.8V).
- Device is considered a two terminal device: Pins 1, 2, 3 and 4 are shorted together and Pins 5, 6, 7 and 8 are shorted together.

## Typical Performance Curves

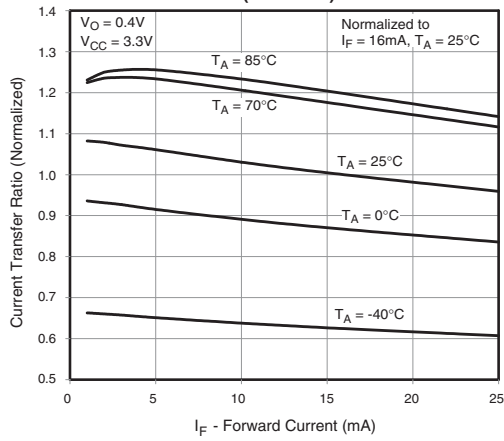
**Fig. 1 LED Forward Current vs. Forward Voltage**



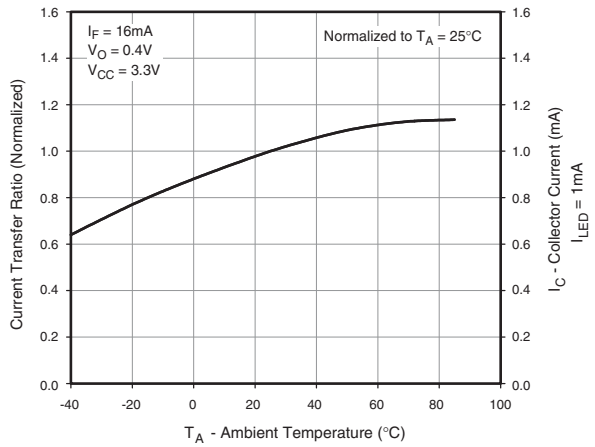
**Fig. 2 Current Transfer Ratio vs. Forward Current (FOD050L, FOD053L)**



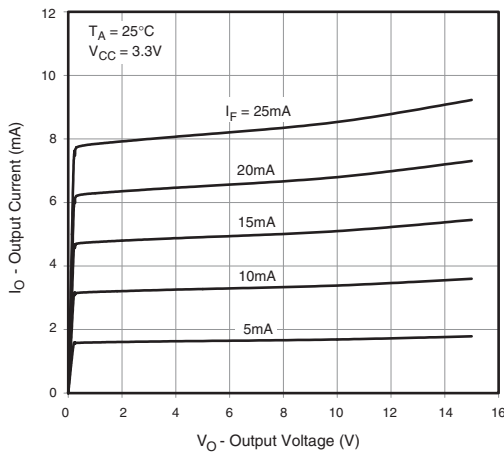
**Fig. 3 Current Transfer Ratio vs. Input Forward Current (FOD250L)**



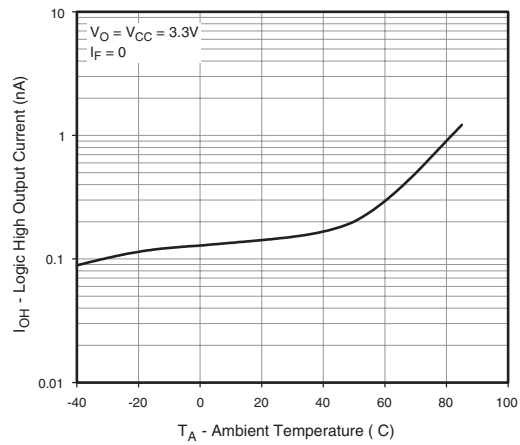
**Fig. 4 Current Transfer Ratio vs. Ambient Temperature**



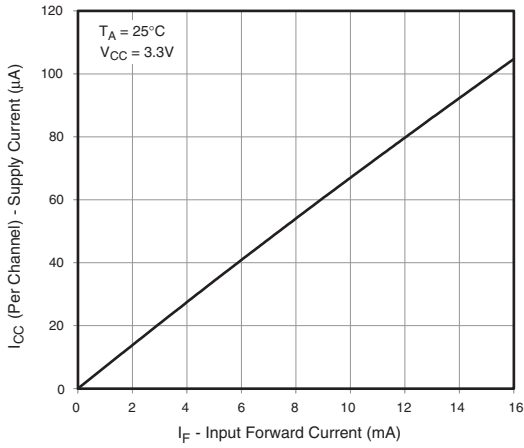
**Fig. 5 Output Current vs. Output Voltage**



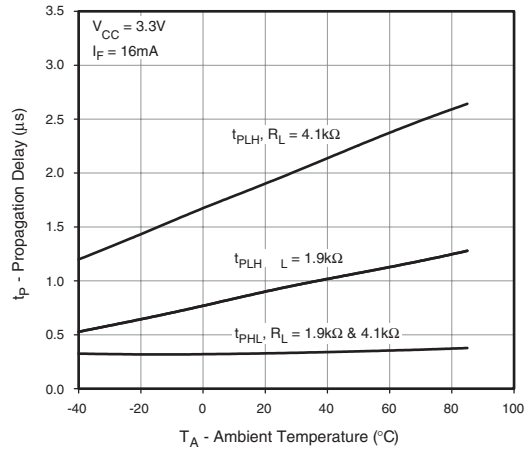
**Fig. 6 Logic High Output Current vs. Ambient Temperature**



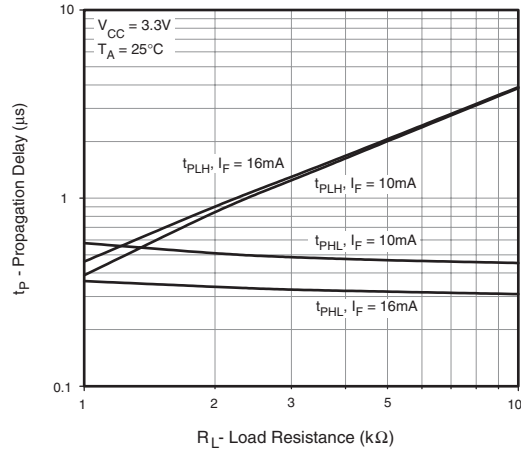
**Fig. 7 Supply Current vs. Input Forward Current**



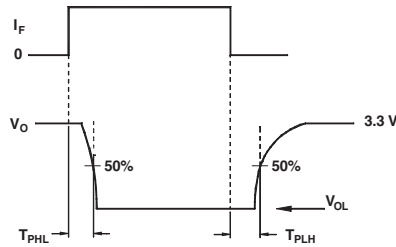
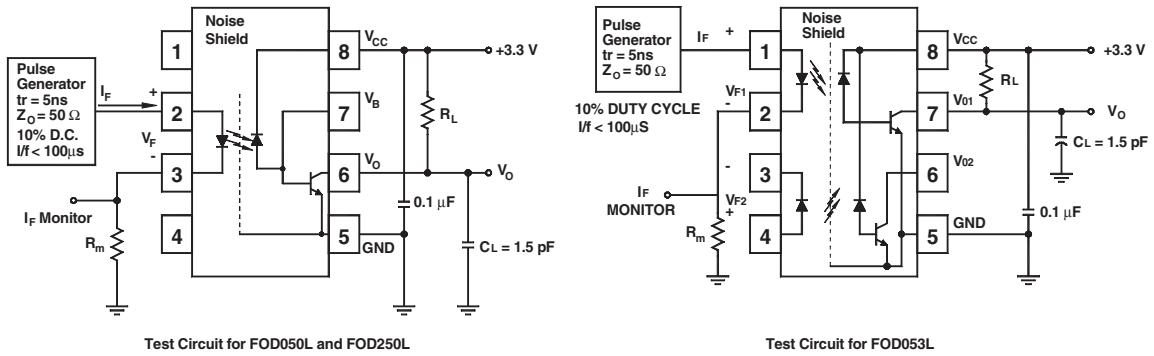
**Fig. 8 Propagation Delay vs. Ambient Temperature**



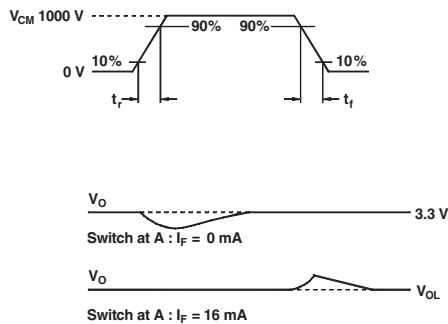
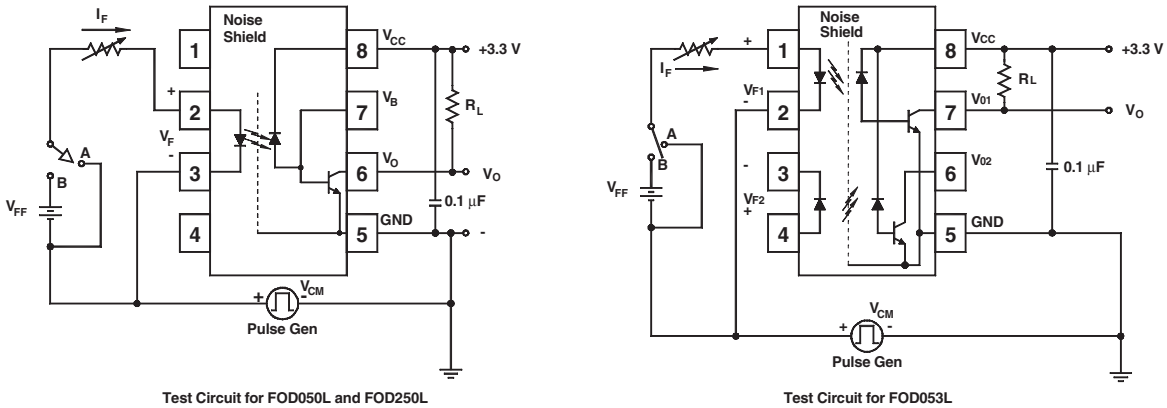
**Fig. 9 Propagation Delay vs Load Resistance**



**Fig. 10 Switching Time Test Circuit**



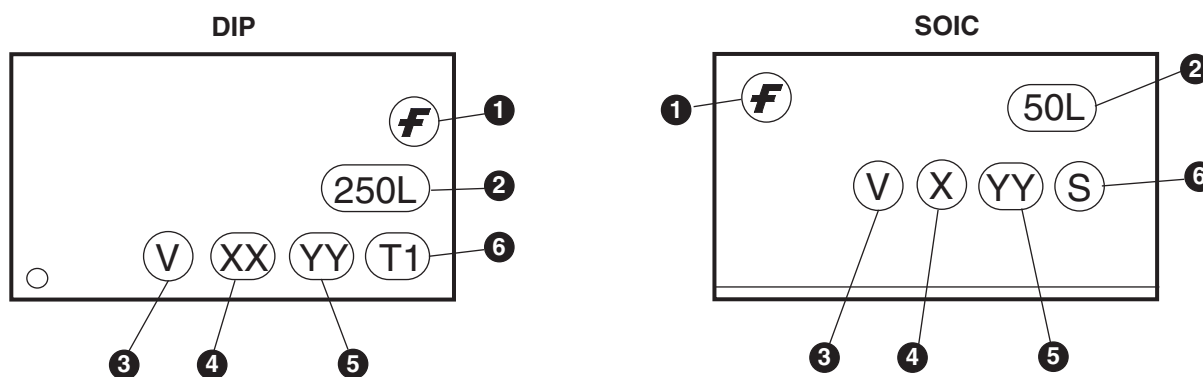
**Fig. 11 Common Mode Immunity Test Circuit**



## Ordering Information

Option	Order Entry Identifier	Description
No Suffix	FOD250L	Through Hole (DIP package only)
	FOD050L	Surface Mount Lead Form (SOIC-8 package only)
S	FOD250LS	Surface Mount Lead Bend (DIP package only)
SD	FOD250LSD	Surface Mount; Tape and reel (DIP package only)
SV	Pending Approval	Surface Mount; VDE0884 (DIP package only)
SDV	Pending Approval	Surface Mount; Tape and reel, VDE0884 (1000 units per reel) (DIP package only)
T	FOD250LT	0.4" Lead Spacing (DIP package only)
TV	Pending Approval	0.4" Lead Spacing, VDE0884 (DIP package only)
R1	FOD050LR1	Tape and Reel (500 units per reel) (SOIC-8 package only)
R1V	Pending Approval	VDE, Tape and Reel (500 units per reel) (SOIC-8 package only)
R2	FOD050LR2	Tape and Reel (2500 units per reel) (SOIC-8 package only)
R2V	Pending Approval	VDE, Tape and Reel (2500 units per reel) (SOIC-8 package only)
V	Pending Approval	VDE (SOIC-8 package only)

## Marking Information

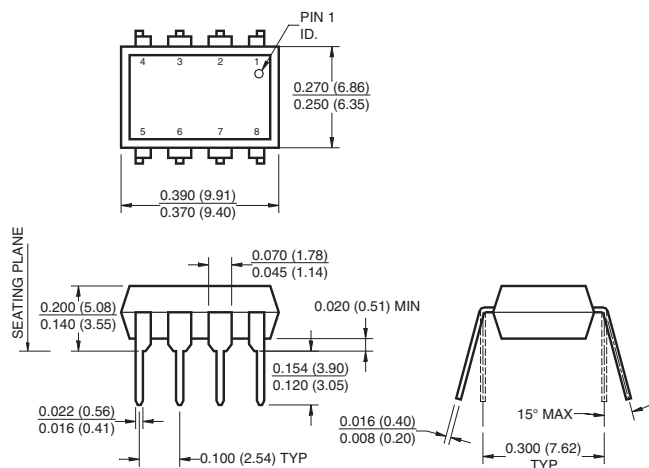


Definitions	
1	Fairchild logo
2	Device number
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)
4 (DIP)	Two digit year code, e.g., '03'
4 (SOIC)	One digit year code, e.g., '3'
5	Two digit work week ranging from '01' to '53'
6	Assembly package code

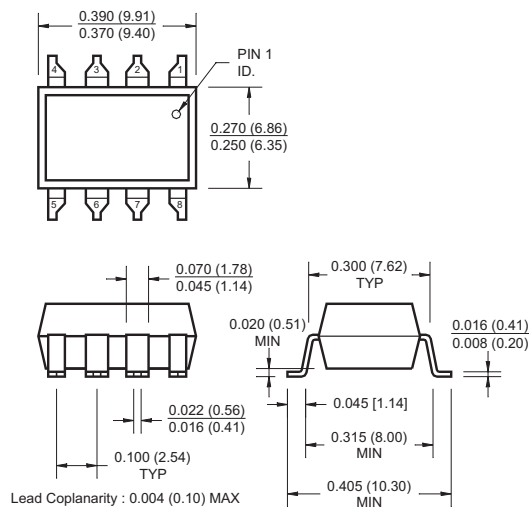


## Package Dimensions (8-Pin DIP)

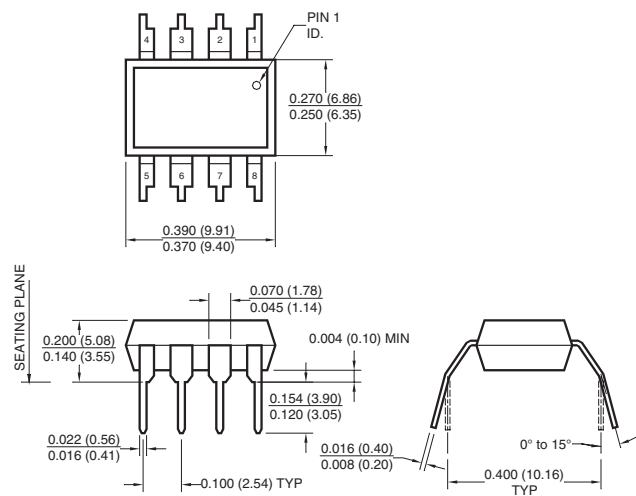
### Through Hole



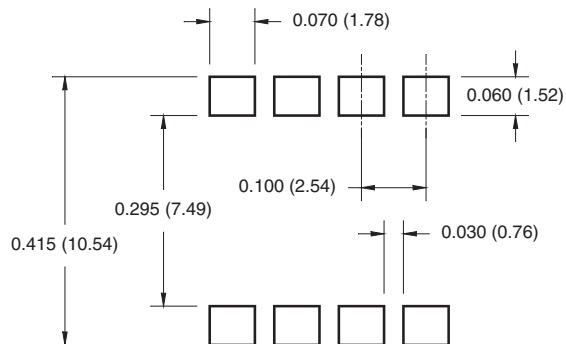
### Surface Mount



### 0.4" Lead Spacing



### 8-Pin DIP

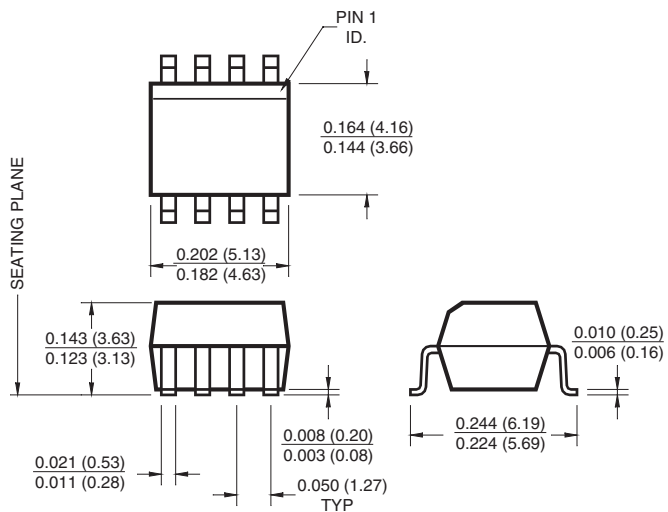


### Note:

All dimensions are in inches (millimeters)

## Package Dimensions (8-Pin SOIC)

### Surface Mount

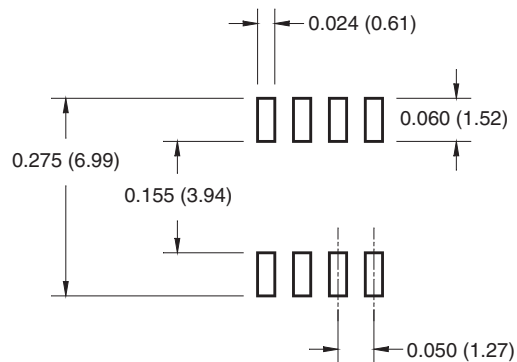


Lead Coplanarity : 0.004 (0.10) MAX

### Note:

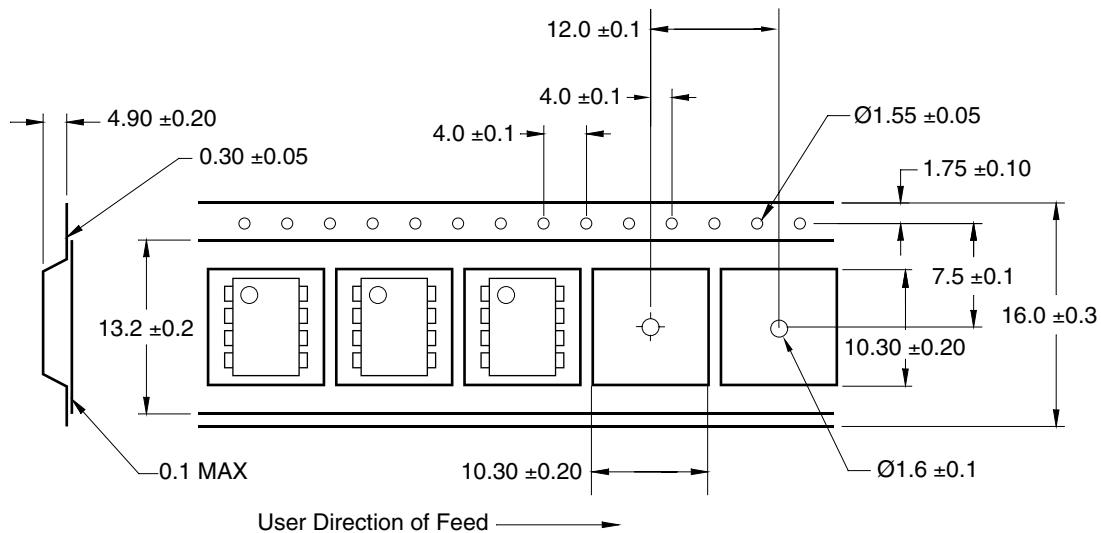
All dimensions are in inches (millimeters)

### 8-Pin Small Outline



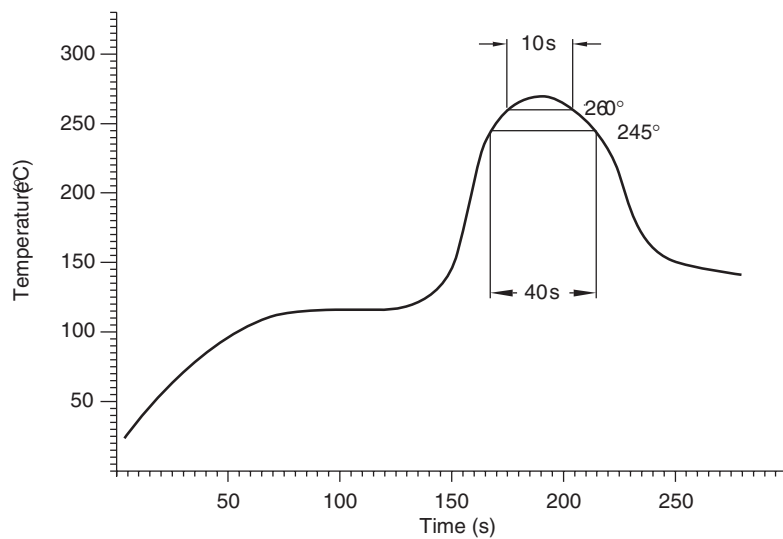
### 8-Pin DIP (FOD250L)

#### Carrier Tape Specification



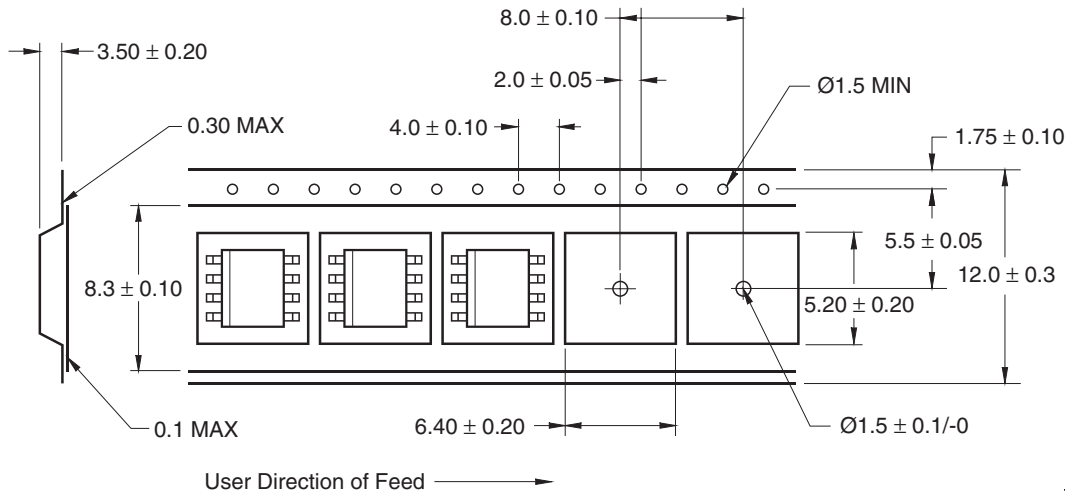
#### Reflow Profile

- Peak reflow temperature 260°C (package surface temperature)
- Time of temperature higher than 245°C 40 seconds or less
- Number of reflows Three

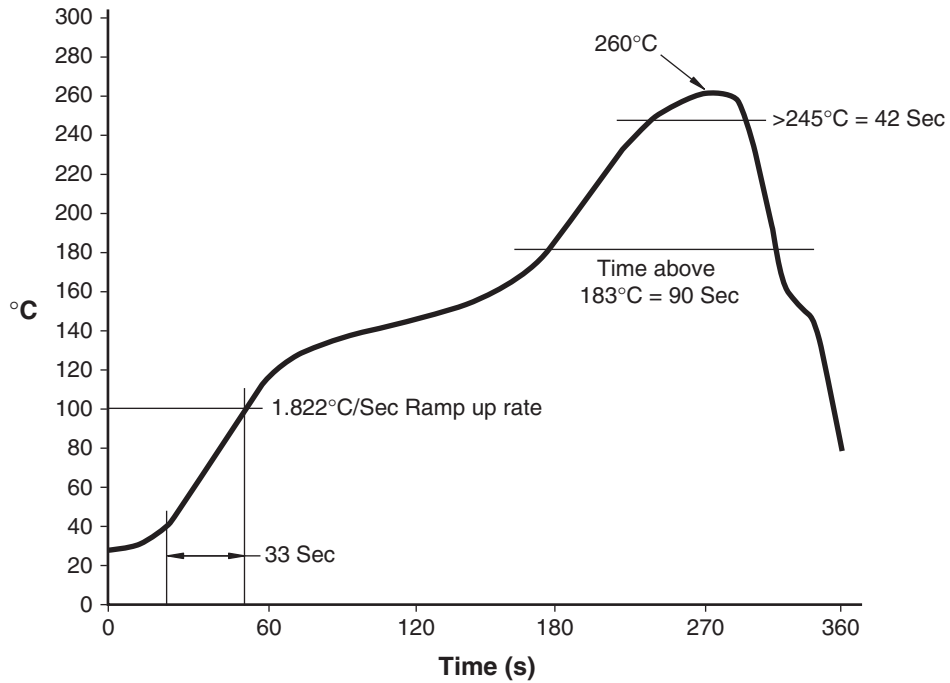


### 8-Pin SOIC (FOD050L, FOD053L)

#### Carrier Tape Specification



#### Reflow Profile



## TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	FACT Quiet Series™	OCX™	SILENT SWITCHER®	UniFET™
ActiveArray™	GlobalOptoisolator™	OCXPro™	SMART START™	UltraFET®
Bottomless™	GTO™	OPTOLOGIC®	SPM™	VCX™
Build it Now™	HiSeC™	OPTOPLANAR™	Stealth™	Wire™
CoolFET™	I <sup>2</sup> C™	PACMAN™	SuperFET™	
CROSSVOLT™	i-Lo™	POP™	SuperSOT™-3	
DOE™	ImpliedDisconnect™	Power247™	SuperSOT™-6	
EcoSPARK™	IntelliMAX™	PowerEdge™	SuperSOT™-8	
E <sup>2</sup> CMOS™	ISOPLANAR™	PowerSaver™	SyncFET™	
EnSigna™	LittleFET™	PowerTrench®	TCM™	
FACT™	MICROCOUPLER™	QFET®	TinyBoost™	
FAST®	MicroFET™	QS™	TinyBuck™	
FASTr™	MicroPak™	QT Optoelectronics™	TinyPWM™	
FPS™	MICROWIRE™	Quiet Series™	TinyPower™	
FRFET™	MSX™	RapidConfigure™	TinyLogic®	
	MSXPro™	RapidConnect™	TINYOPTO™	
Across the board. Around the world.™		μSerDes™	TruTranslation™	
The Power Franchise®		ScalarPump™	UHC™	
Programmable Active Droop™				

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. I20