

MOSFET – P-Channel, QFET®

-60 V, -47 A, 26 mΩ

FQP47P06

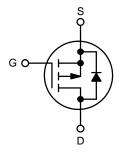
Description

This P-Channel enhancement mode power MOSFET is produced using **onsemi**'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on–state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

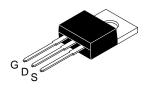
Features

- -47 A, -60 V, $R_{DS(on)} = 26 \text{ m}\Omega$ (Max.) @ $V_{GS} = -10 \text{ V}$, $I_D = -23.5 \text{ A}$
- Low Gate Charge (Typ. 84 nC)
- Low Crss (Typ. 320 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating

V _{DSS}	R _{DS(on)} MAX I _D MAX	
-60 V	26 mΩ @ –10 V	-47 A

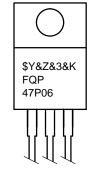


P-Channel MOSFET



TO-220-3LD CASE 340AT

MARKING DIAGRAM



\$Y = Logo

&Z = Assembly Plant Code &3 = 3-Digit Plant Code

&K = 2-Digits Lot Run Traceability Code

FQP47P06 = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping
FQP47P06	TO-220-3LD	1000 Units / Tube

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise specified)

Symbol	Paramet	FQP47P06	Unit	
V_{DSS}	Drain-Source Voltage		-60	V
I _D	Drain Current	– Continuous (T _C = 25°C)	-47	Α
		- Continuous (T _C = 100°C)	-33.2	Α
I _{DM}	Drain Current (Note 1)	- Pulsed	-188	Α
V_{GSS}	Gate-Source Voltage		±25	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		820	mJ
I _{AR}	Avalanche Current (Note 1)		-47	Α
E _{AR}	Repetitive Avalanche Energy (Note 1)		16	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		-7.0	V/ns
P_{D}	Power Dissipation (T _C = 25°C)		160	W
		– Derate above 25°C	1.06	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temperature for Soldering Purposes, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. L = 0.43 mH, $I_{AS} = -47$ A, $V_{DD} = -25$ V, $R_G = 25$ Ω , Starting $T_J = 25$ °C

3. $I_{SD} \le -47$ A, di/dt ≤ 300 A/ μ s, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25$ °C

THERMAL CHARACTERISTICS

Symbol	Characteristic	FQP47P06	Unit
$R_{ heta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.94	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
OFF CHAR	ACTERISTICS					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V, } I_D = -250 \mu\text{A}$	-60	-	_	V
$\Delta BV_{DSS} / \Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I_D = -250 μ A, Referenced to 25°C	-	-0.06	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	-1	μΑ
		$V_{DS} = -48 \text{ V}, T_{C} = 150^{\circ}\text{C}$	-	-	-10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = -25 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	-100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = 25 V, V _{DS} = 0 V	-	-	100	nA
ON CHARA	ACTERISTICS					
V _{GS(th})	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-2.0	-	-4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	$V_{GS} = -10 \text{ V}, I_D = -23.5 \text{ A}$	-	0.021	0.026	Ω
9FS	Forward Transconductance	$V_{DS} = -30 \text{ V}, I_D = -23.5 \text{ A (Note 4)}$	-	21	-	S
DYNAMIC	CHARACTERISTICS	•				
C _{iss}	Input Capacitance	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz}$	-	2800	3600	pF
C _{oss}	Output Capacitance		-	1300	1700	pF
C _{rss}	Reverse Transfer Capacitance	7	-	320	420	pF
SWITCHIN	G CHARACTERISTICS	•			-	
t _{d(on)}	Turn-On Delay Time	$V_{DD} = -30 \text{ V}, I_D = -23.5 \text{ A}, R_G = 25 \Omega$	-	50	110	ns
t _r	Turn-On Rise Time	(Note 4, 5)	-	450	910	ns
t _{d(off)}	Turn-Off Delay Time		-	100	210	ns
t _f	Turn-Off Fall Time	7	-	195	400	ns
Qg	Total Gate Charge	$V_{DS} = -48 \text{ V}, I_D = -47 \text{ A}, V_{GS} = -10 \text{ V}$	-	84	110	nC
Q _{gs}	Gate-Source Charge	(Note 4, 5)	-	18	-	nC
Q _{gd}	Gate-Drain Charge	7	-	44	-	nC
DRAIN-SO	URCE DIODE CHARACTERISTICS AND MAX	IMUM RATING				
I _S	Maximum Continuous Drain-Source Diode Forward Current		-	-	-47	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		_	-	-188	Α
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V, } I_{S} = -47 \text{ A}$	-	-	-4.0	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V, } I_{S} = -47 \text{ A,}$	_	130	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100 \text{ A/}\mu\text{s (Note 4)}$	_	0.55	_	μС

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 4. Pulse Test: Pulse width $\leq 300~\mu s$, Duty cycle $\leq 2\%$ 5. Essentially independent of operating temperature

TYPICAL CHARACTERISTICS

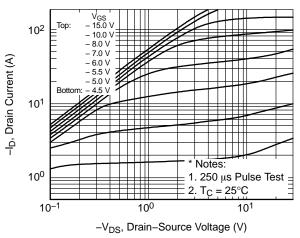


Figure 1. On-Region Characteristics

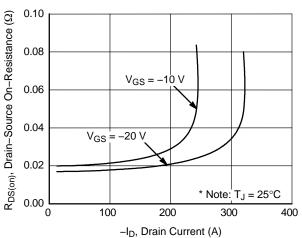


Figure 3. On–Resistance Variation vs. Drain Current and Gate Voltage

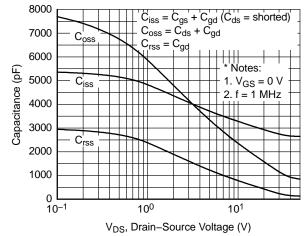


Figure 5. Capacitance Characteristics

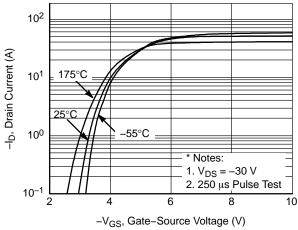


Figure 2. Transfer Characteristics

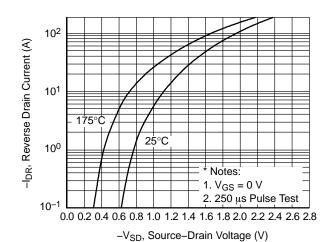


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

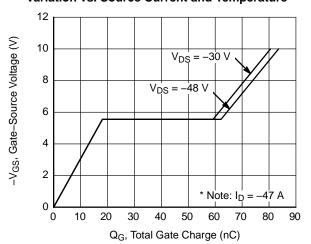


Figure 6. Gate Charge Characteristics

TYPICAL CHARACTERISTICS (Continued)

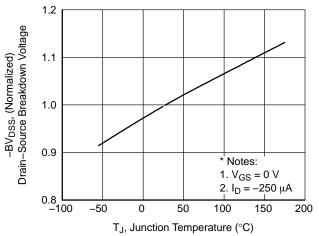


Figure 7. Breakdown Voltage Variation vs. Temperature

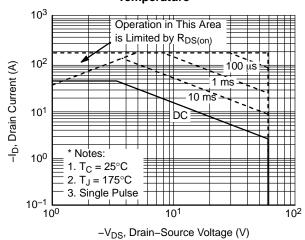
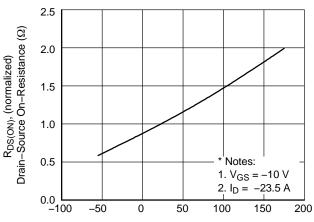


Figure 9. Maximum Safe Operating Area



T_J, Junction Temperature (°C)

Figure 8. On–Resistance Variation vs. Temperature

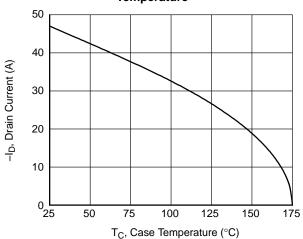


Figure 10. Maximum Drain Current vs. Case Temperature

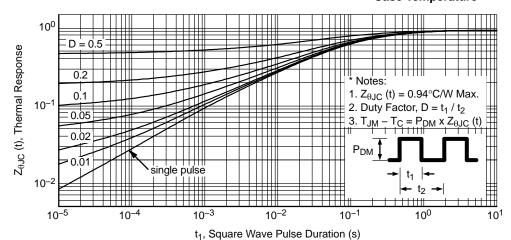


Figure 11. Transient Thermal Response Curve

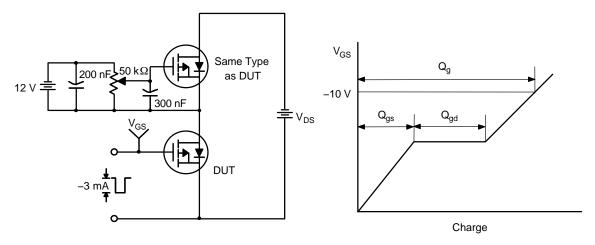


Figure 12. Gate Charge Test Circuit & Waveform

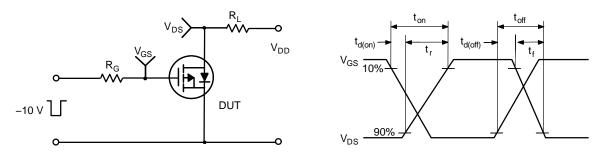


Figure 13. Resistive Switching Test Circuit & Waveforms

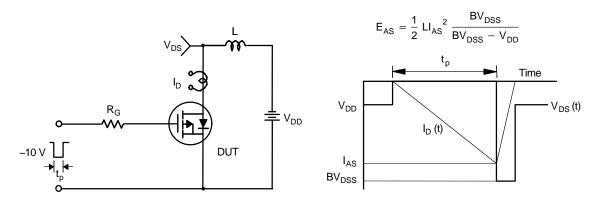
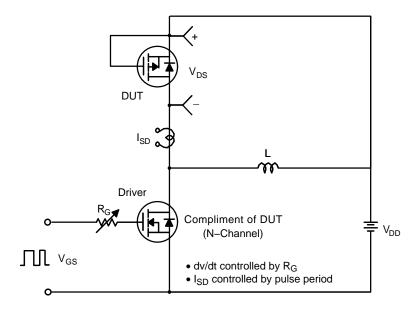
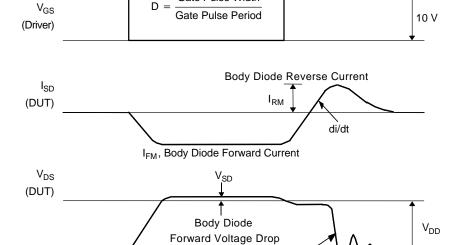


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



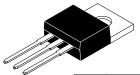


Gate Pulse Width

Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

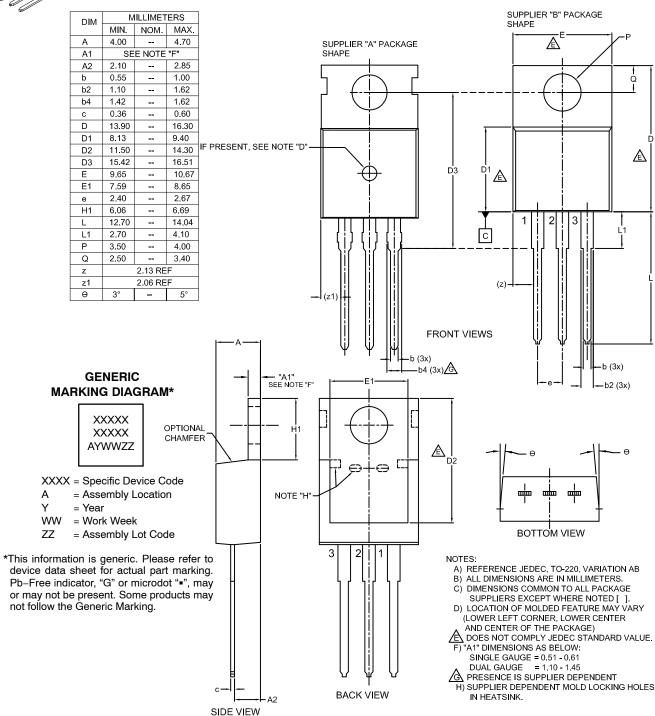
Body Diode Recoverydv/dt





TO-220-3LD CASE 340AT ISSUE B

DATE 08 AUG 2022



DOCUMENT NUMBER:	98AON13818G	Electronic versions are uncontrolled except when accessed directly from the Document Repository Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	TO-220-3LD		PAGE 1 OF 1	

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, ONSEMI., and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems. or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales