

November 2013

FQPF8N60C

N-Channel QFET® MOSFET

600 V, 7.5 A, 1.2 Ω

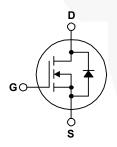
Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology. This advanced technology has been especially tailored to minimize onstate resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology.

Features

- 7.5 A, 600 V, $R_{DS(on)}$ = 1.2 Ω (Max.) @ V_{GS} = 10 V, I_D = 3.75 A
- Low Gate Charge (Typ. 28 nC)
- Low Crss (Typ. 12 pF)
- 100% Avalanche Tested





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQPF8N60C	Unit
V_{DSS}	Drain-Source Voltage		600	V
I _D	Drain Current - Continuous (T _C = 25	°C)	7.5 *	Α
	- Continuous (T _C = 10	0°C)	4.6 *	Α
I _{DM}	Drain Current - Pulsed	(Note 1)	30 *	Α
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	230	mJ
I _{AR}	Avalanche Current	(Note 1)	7.5	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	14.7	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5	V/ns
P_{D}	Power Dissipation (T _C = 25°C)		48	W
	- Derate above 25°C		0.38	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C

^{*} Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FQPF8N60C	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	2.6	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQPF8N60C	FQPF8N60C	TO-220F	Tube	N/A	N/A	50 units

Electrical Characteristics T_c = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Off Cha	aracteristics					
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	600			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C		0.7		V/°C
I _{DSS}	Zana Cata Valtana Duain Courset	V _{DS} = 600 V, V _{GS} = 0 V			1	μΑ
	Zero Gate Voltage Drain Current	V _{DS} = 480 V, T _C = 125°C			10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 3.75 A		1.0	1.2	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 3.75 A	\	8.7		S
Dynam	ic Characteristics					
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,		965	1255	pF
Coss	Output Capacitance	f = 1.0 MHz		105	135	pF
C _{rss}	Reverse Transfer Capacitance			12	16	pF
Switchi	ing Characteristics					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 300 \text{ V}, I_D = 7.5 \text{ A},$		16.5	45	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		60.5	130	ns
t _{d(off)}	Turn-Off Delay Time			81	170	ns
t _f	Turn-Off Fall Time	(Note 4)		64.5	140	ns
Qg	Total Gate Charge	V _{DS} = 480 V, I _D = 7.5 A,		28	36	nC
Q_{gs}	Gate-Source Charge	V _{GS} = 10 V		4.5		nC
Q_{gd}	Gate-Drain Charge	(Note 4)		12		nC
Drain-S	Source Diode Characteristics a	nd Maximum Ratings				
I _S	Maximum Continuous Drain-Source Diode Forward Current				7.5	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				30	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 7.5 A			1.4	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, I_{S} = 7.5 \text{ A},$		365		ns
Q _{rr}	Reverse Recovery Charge	dl _F / dt = 100 A/μs		3.4	//	μС

- 1. Repetitive rating : pulse-width limited by maximum junction temperature.
 2. L = 7.3 mH, I_{AS} = 7.5 A, V_{DD} = 50 V, R_{G} = 25 Ω , starting T_{J} = 25°C.
 3. I_{SD} ≤ 7.5 A, di/dt ≤ 200 A/ μ s, V_{DD} ≤ BV $_{DSS}$, starting T_{J} = 25°C.
 4. Essentially independent of operating temperature.

Typical Characteristics

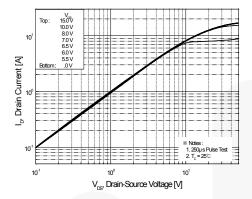


Figure 1. On-Region Characteristics

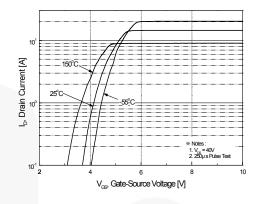


Figure 2. Transfer Characteristics

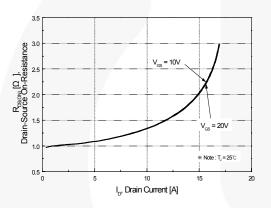


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

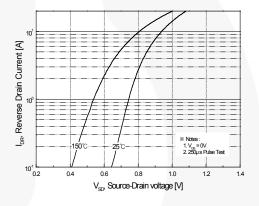


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

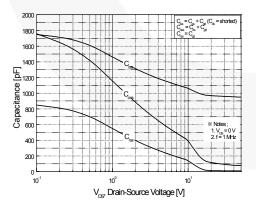


Figure 5. Capacitance Characteristics

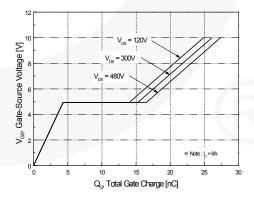


Figure 6. Gate Charge Characteristics

Typical Characteristics (continued)

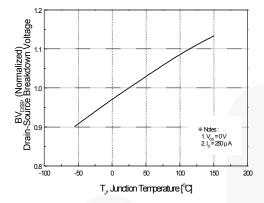


Figure 7. Breakdown Voltage Variation vs Temperature

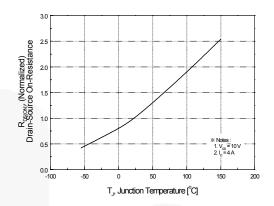


Figure 8. On-Resistance Variation vs Temperature

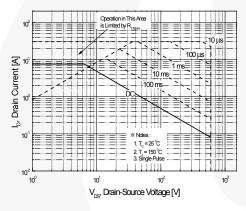


Figure 9. Maximum Safe Operating Area

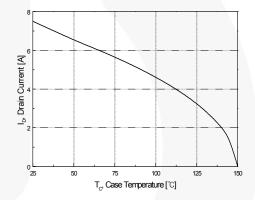


Figure 10. Maximum Drain Current vs Case Temperature

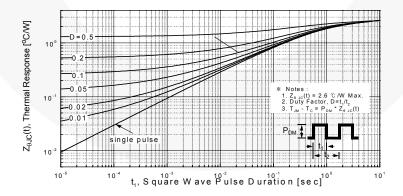


Figure 11. Transient Thermal Response Curve

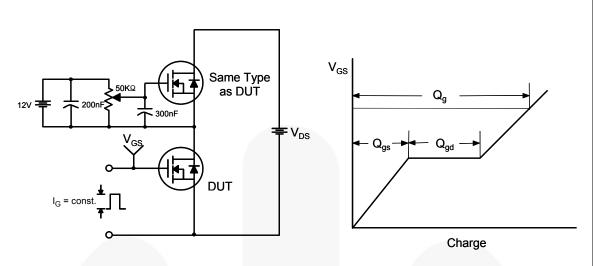


Figure 12. Gate Charge Test Circuit & Waveform

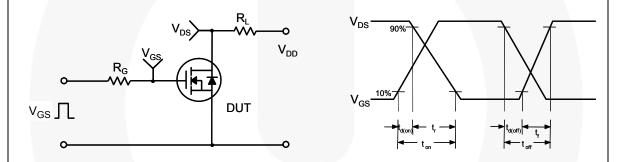


Figure 13. Resistive Switching Test Circuit & Waveforms

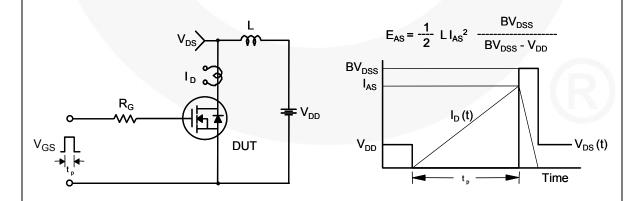
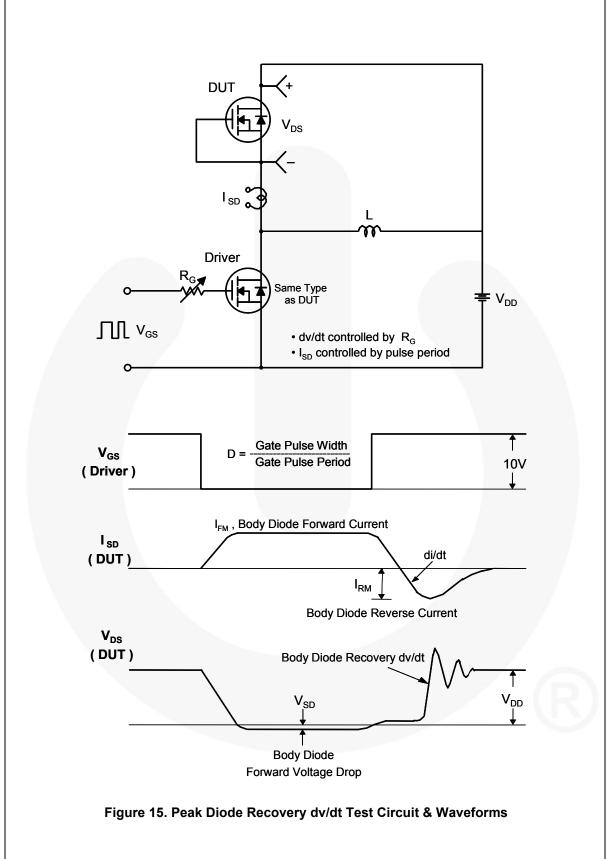


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions

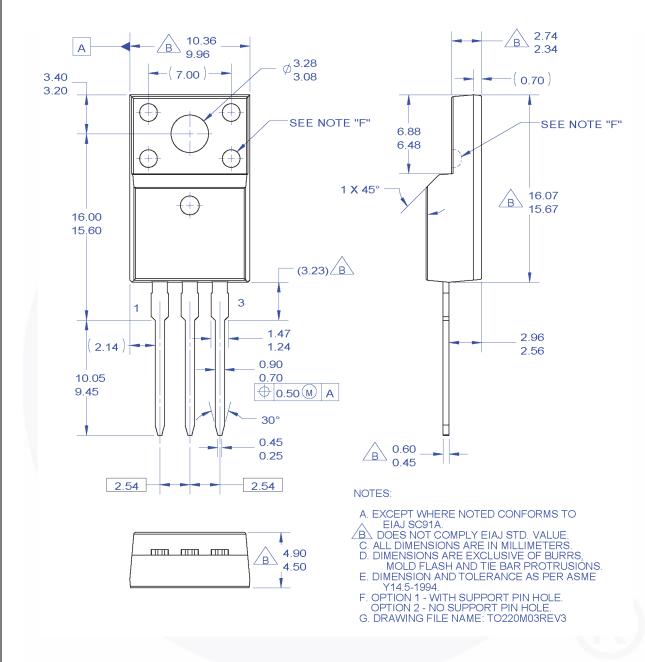


Figure 16. TO220, Molded, 3-Lead, Full Pack, EIAJ SC91, Straight Lead

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