FAIRCHILD SEMICONDUCTOR®

January 2007

FSA201 USB2.0 Full-Speed and Audio Switches with Negative Signal Capability

Features

- 3Ω Typical ON Resistance
- -3db Bandwidth: > 250MHz
- Low Power Consumption
- Packaged in Pb-free 10-Lead MicroPak™ (1.6 x 2.1mm), 10-pin MSOP (Preliminary)
- Power-off Protection on Common D+/R, D-/L Ports
- Automatically Detects V_{bus} for Switch Path Selection

Applications

- Cell Phone, PDA, Digital Camera, and Notebook
- LCD Monitor, TV, and Set-top Box

Description

The FSA201 is a Double-Pole, Double Throw (DPDT) multiplexer that combines a low-distortion audio and a USB2.0 Full-Speed (FS) switch path. This configuration enables audio and USB data to share a common connector port. The architecture is designed to allow audio signals to swing below ground. This means a common USB and headphone jack can be used for personal media players and similar portable peripheral devices.

Since USB2.0 is an industry standard for shared data-path in portable devices, the FSA201 also incorporates a V_{bus} detection capability. The FSA201 includes a power-off feature to minimize current consumption when V_{bus} is not present. This power-off circuitry is available for the common D+/R, D-/L ports only. Typical applications involve switching in portables and consumer applications, such as cell phones, digital cameras, and notebooks with hubs or controllers.

Ordering Information

Part Number	Package Number	Pb-Free	Packing Description
FSA201L10X	MAC010A	Yes	10-Lead MicroPak, JEDEC MO-255, 1.6 x 2.1mm
FSA201MUX (Preliminary)	MUA10A	Yes	10-Lead MSOP JEDEC MO-187, 3.0 mm Wide

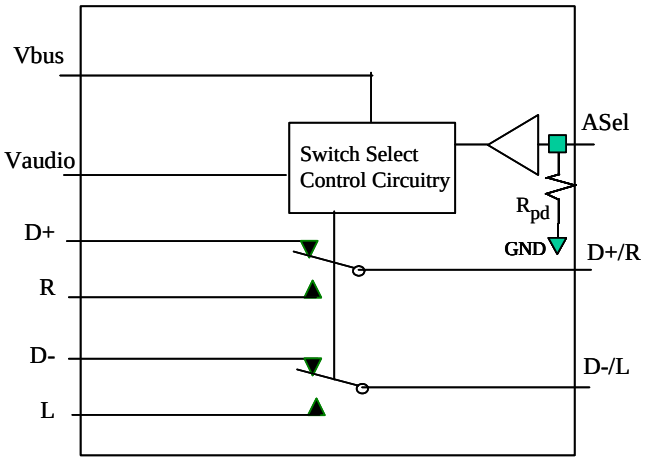


Figure 1. FSA201 Analog Symbol

Pin Assignments

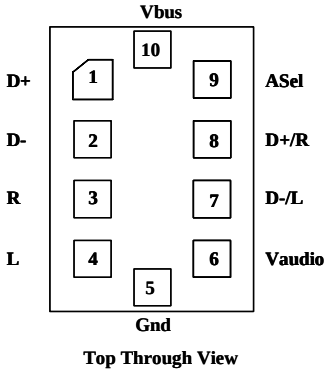


Figure 2. MicroPak 10-Pin Assignment

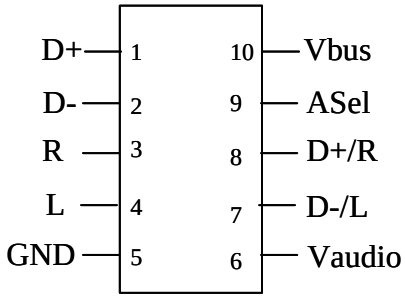


Figure 3. MSOP 10-Pin Assignment

Pin Descriptions

Pin #	Name	Description
1, 2	D+, D-	USB data bus input sources
6	V_{audio}	Power supply (audio)
3, 4	R, L	Audio right and left input sources
9	A_{Sel}	Audio select to override auto USB detect when V_{AUDIO} supply is present
10	V_{bus}	Power supply (USB) and auto USB switch-path select
8, 7	D+/R, D-/L	USB and audio common connector ports

Truth Table

A_{Sel} (1)	V_{audio}	V_{bus}	L, R	D+, D-
L	L	L	OFF	OFF
L	L	H (2)	OFF	ON
L	H (2)	L	ON	OFF
L	H (2)	H (2)	OFF	ON
H	L	L	OFF	OFF
H	L	H (2)	OFF	ON
H	H (2)	L	ON	OFF
H	H (2)	H (2)	ON	OFF

Notes:

1. A_{Sel} - Internal resistor to GND provides auto-V_{bus} detect if there is no external connection. Forcing A_{Sel} HIGH when V_{AUDIO} is present overrides the USB path even if V_{bus} is present.
2. H - Value is the threshold as defined to meet USB2.0 V_{bus} requirements and audio supply threshold in a system (see DC Tables).

Functional Description

The FSA201 is a combined USB and audio switch that enables sharing the D+/D- lines of a USB connector with stereo audio CODEC outputs. The switch is optimized for full-speed USB signals and includes an automatic V_{bus}-detection circuit. When a USB connector, rather than a headphone, is connected to the ultra-portable device the switch is automatically configured for full-speed USB data transfer. If no V_{bus} is detected, and yet V_{AUDIO} is present, the switch is configured for the low-distortion audio switch path. The audio switch path also handles negative signals (down to -2V), which eliminates the need for large coupling capacitors.

For those applications where the V_{bus} is generated as a self-powered device or where V_{bus} is not removed, the A_{Sel} pin provides the ability to switch, under software control, to the audio path. The A_{Sel} pin is internally terminated by a resistor to GND (typical value 3MΩ) and requires no connection for the standard ultra-portable (cell-phone, MP3, or Portable Media Player). In an application where the supply to the FSA201 V_{bus} pin is not guaranteed to be removed, a GPIO pin can be used to switch out of full-speed USB mode into audio mode, using the A_{Sel} pin.

The FSA201 V_{bus} pin must be connected directly to V_{bus} or a supply > 3.8V, not an LDO regulated down to 3.6V or a V_{bat}-generated supply that may fall below 3.8V in normal operation (see the Application Diagram).

Application Diagram

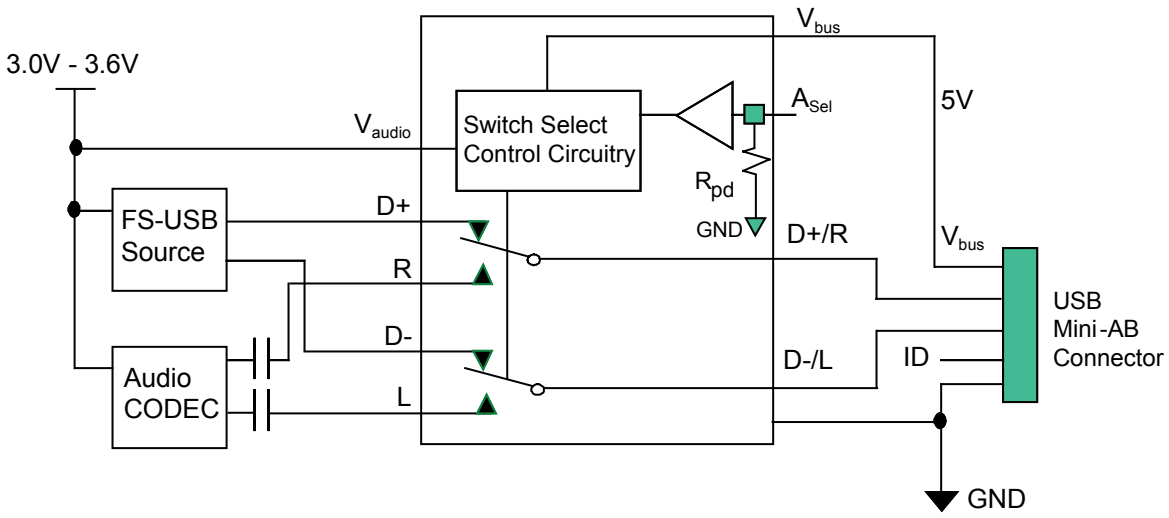


Figure 4. Application Diagram

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter		Conditions
V_{Audio}	Supply Voltage		-0.5V to 6.0V
V_{bus}	Supply Voltage		-0.5V to 6.0V
V_{SW}	Switch I/O Voltage[3]	R, L Pins	(V_{audio}- 7.0V) to (V_{audio}+ 0.3V)
		D+, D-, D+/R, D-/L Pins	(V_{bus}- 7.0V) to (V_{bus}+ 0.3V)
A_{Sel}	Control Input Voltage[3]		-0.5V to + 6.0V
I_{IK}	Input Clamp Diode Current		- 50mA
I_{SW}	Switch I/O Current (Continuous)	USB	50mA
		Audio	250mA
I_{SWPEAK}	Peak Switch Current (Pulsed at 1ms Duration, <10% Duty Cycle)	USB	100mA
		Audio	500mA
T_{STG}	Storage Temperature Range		-65°C to +150°C
T_J	Maximum Junction Temperature		+150°C
T_L	Lead Temperature (Soldering, 10 seconds)		+260°C
ESD	Human Body Model (JEDEC: JESD22-A114)	I/O to GND	10kV
		All Other Pins	8kV
	Charge Discharge Model (JEDEC-JESD-C101)		2kV

Notes:

3. The input and output negative ratings may be exceeded if the input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter		Minimum	Maximum
V_{Audio}	Supply Voltage		3.0V	3.6V
V_{bus}	Supply Voltage		4.25V	5.5V
A_{Sel}	Control Input Voltage		0V	V_{Audio}
V_{SW}	Switch I/O Voltage		V_{Audio}– 6.5V	V_{Audio}– 0.3V
T_A	Operating Temperature		-40ºC	85ºC
θ_{JA}	Thermal Resistance (free air)	MicroPak 10L package		330ºC / W (estimated)

DC Electrical Characteristics

All typical values are at 25ºC unless otherwise specified.

Symbol	Parameter	V_{Audio} (V)	Conditions	T_A = - 40ºC to +85ºC			Unit
				Min.	Typ.	Max.	
Common Pins							
V_{IK}	Clamp Diode Voltage	2.7	I_{IK} = -18mA			-1.2	V
V_{IH}	Control Input Voltage HIGH	2.7 to 3.6		1.3			
V_{IL}	Control Input Voltage LOW	2.7 to 3.6				0.5	
I_{IN}	A_{Sel} Input HIGH Current	3.6	V_{CNTRL} = 0V to 3.6V	-3		3	µA
I_{OFF}	Power Off Leakage Current (Common Port Only D+/R, D-/L)	V_{audio} = V_{bus} = 0V	Common Port (D+/R, D-/L) V_{SW} = 0V to 5.5V			1	µA
$I_{NO(0FF)}$	Off-LeakageCurrent of Port D+, D-, R, L	3.6	V_{bus} = 0V, 5. 5V D+/R, D-/L = 0.3V, V_{AUDIO} – 0.3V D+, D-, R, L = 0.3V, V_{AUDIO} –0.3V or Floating See Figure 14	-50	10	50	nA
$I_{NC(0N)}$	On-LeakageCurrent of Port D+/R or D-/L	3.6	V_{bus} = 0V, 5.5V D+/R, D-/L = 0.3V, V_{AUDIO} – 0.3V D+, D-, R, L = Floating See Figure 15	-100	50	100	nA
USB Switch Path		**V_{bus} (V)**					
	USB Analog Signal Range			0		3.6	V
R_{ONUSB}	FS Switch On Resistance(4)	4.25	$V_{D+/D-}$ = 0V, 3.0V, I_{ON} = -8mA See Figures 6, 13		3	6	Ω
ΔR_{ONUSB}	FS Delta R_{ON}(4,6)	4.25	$V_{D+/D-}$ = 3V, I_{ON} = -8mA		0.35		Ω
Audio Switch Path		**V_{Audio} (V)**					
	Audio Analog Signal Range			V_{audio} – 6.5		V_{audio}	V
$R_{ONAudio}$	Audio Switch On Resistance(7)	2.7	$V_{L/R}$ = -2V, 0V, 0.7V, V_{AUDIO}-0.7V, V_{AUDIO} I_{ON} = -100mA, V_{bus} = 0V See Figures 5, 13		0.5	1.0	Ω
$\Delta R_{ONAudio}$	Audio Delta R_{ON}(4)	2.7	$V_{L/R}$ = 0.7V I_{ON} = -100mA		0.01	0.1	Ω
$R_{FLAT(Audio)}$	Audio R_{ON} Flatness(5)	2.7	$V_{L/R}$ = -2V, 0V, 0.7V, 2V, 2.7V I_{ON} = -100mA			0.35	Ω

Notes:

4. $\Delta R_{ON}=R_{ON\ max} – R_{ON\ min}$ measured at identical V_{CC}, temperature, and voltage. Worst-case signal path, audio or USB channel, is characterized.
5. Flatness is defined as the difference between the maximum and minimum values of on resistance over the specified range of conditions.
6. Guaranteed by characterization, not production tested.
7. On resistance is determined by the voltage drop between the A and B pins at the indicated current through the switch.

DC Electrical Characteristics (Continued)

All typical values are at 25ºC unless otherwise specified.

Symbol	Parameter	V_{Audio} (V)	Conditions	T_A = - 40ºC to +85ºC			Unit
				Min.	Typ.	Max.	
Power Supply							
V_{busth}	V_{bus} Threshold Voltage			3.2		3.8	V
$V_{audioth}$	V_{audio} Threshold			0.5		1.5	V
$I_{CC(Audio)}$	Quiescent Supply Current (Audio)	3.0	V_{ASel} = 0 to V_{audio}, I_{OUT} = 0			10	µA
$I_{CC(Vbus)}$	Quiescent Supply Current (V_{bus})		V_{ASel} = 0 to V_{audio}, I_{OUT} = 0 V_{bus} = 5.5V			20	µA
I_{CCT}	Increase in I_{CC} current per control voltage and V_{CC}	3.0	V_{ASel} = 2.6V, V_{bus} = floating			15	µA
			V_{ASel} = 1.8V, V_{bus} = floating			18	

AC Electrical Characteristics

All typical value are for V_{AUDIO} = 3.3V and V_{bus} = 5.0 at 25ºC unless otherwise specified.

Symbol	Parameter	V_{audio}/V_{bus} (V)	Conditions	T_A = - 40°C to +85°C			Unit
				Min.	Typ.	Max.	
$T_{ONAUDIO1}$	Turn-On Time V_{AUDIO}↑ to Output	V_{bus} = 0V	$V_{D+/R, D-/L}$ = 1.0V R_L = 50Ω, C_L = 50pF See Figures 16, 18			10	µs
$T_{OFFAUDIO1}$	Turn-Off Time V_{Bus}↑ to Output	V_{AUDIO} = 2.7 for V_{bus} ↑	$V_{D+/R, D-/L}$ = 1.0V R_L = 50Ω, C_L = 50pF See Figures 16, 18			10	µs
$T_{ONAUDIO2}$	Turn-On Time A_{Sel} to Output	V_{bus} = 4.25V V_{AUDIO} = 2.7	$V_{D+/R, D-/L}$ = 1.0V R_L = 50Ω, C_L = 50pF See Figures 16, 17			1	µs
$T_{OFFAUDIO2}$	Turn-Off Time A_{Sel} to Output	V_{bus} = 4.25V V_{AUDIO} = 2.7	$V_{D+/R, D-/L}$ = 1.0V R_L = 50Ω, C_L = 50pF See Figures 16, 18			1	µs
$T_{ONAUDIO3}$	Turn-On Time V_{Bus}↓ to Output	V_{AUDIO} = 2.7	$V_{D+/R, D-/L}$ = 1.0V R_L=50Ω, C_L= 50pF See Figures 16, 17			10	µs
T_{ONUSB}	Turn-On Time V_{USB}↑ to Output	V_{AUDIO} = 2.7	$V_{D+/R, D-/L}$ = 1.0V R_L = 50Ω, C_L = 50pF See Figures 16, 18			10	µs
T_{OFFUSB}	Turn-Off Time V_{USB}↓ to Output	V_{AUDIO} = 2.7	$V_{D+/R, D-/L}$ = 1.0V R_L = 50Ω, C_L = 50pF See Figures 16, 18			10	µs
T_{PDUSB}	USB Switch Propagation Delay[8]	V_{AUDIO} = 2.7 V_{bus} = 4.25V	R_L = 50Ω, C_L = 50pF See Figure 19		0.25		ns
O_{IRRUSB}	Off-Isolation - USB	V_{AUDIO} = 2.7 V_{bus} = 4.25V	f = 6MHz, R_T = 50Ω, C_L = 0pF See Figures 8, 23		-55		dB
O_{IRRA}	Off-Isolation - Audio	V_{AUDIO} = 2.7 V_{bus} = 4.25V	f = 6MHz, R_T = 50Ω, C_L = 0pF See Figures 7, 23		-37		dB
$Xtalk_{USB}$	Non-Adjacent Channel Crosstalk - USB	V_{AUDIO} = 2.7 V_{bus} = 4.25V	f = 6MHz, R_T = 50Ω, C_L = 0pF See Figures 10, 24		-49		dB
$Xtalk_A$	Non-Adjacent Channel Crosstalk - Audio	V_{AUDIO} = 2.7 V_{bus} = 4.25V	f = 6MHz, R_T = 50Ω, C_L = 0pF See Figures 9, 24		-39		dB
BW	-3db Bandwidth	V_{AUDIO} = 2.7 V_{bus} = 4.25V	R_T = 50Ω, C_L = 0pF, Signal 0dBm See Figures 11, 12, 22		400		MHz
THD	Total Harmonic Distortion	V_{AUDIO} = 2.7 V_{bus} = 0V	f = 20Hz to 20kHz, R_L = 32Ω, $V_{R,L}$ = $2V_{pp}$ See Figure 27		0.05		%
PSRR	Power Supply Rejection Ratio	V_{AUDIO} = 3.3 V_{bus} = 0V	f= 217Hz on V_{AUDIO} $V_{R,L}$ = 1.0V, R_T = 32Ω, V_{Ripple} = 600mV_{pp}		-56		dB

Notes:

8. Guaranteed by characterization, not production tested.

USB Full-Speed Related AC Electrical Characteristics

Symbol	Parameter	V_{AUDIO}/ V_{bus}(V)	Conditions	T_A = - 40ºC to +85ºC			Unit
				Min.	Typ.	Max.	
$t_{SK(o)}$	Channel-to-Channel Skew[9]	V_{AUDIO} = 2.7V V_{bus} = 4.25V	$t_R = t_F$ = 12ns (10-90%) at 6MHz C_L = 50pF, R_L = 50Ω See Figures 20, 21		150		ps
$t_{SK(P)}$	Skew of Opposite Transitions of the Same Output[9]	V_{AUDIO} = 2.7V V_{bus} = 4.25V	$t_R = t_F$ = 12ns (10-90%) at 6MHz C_L = 50pF, R_L = 50Ω See Figures 20, 21		150		
t_J	Total Jitter[10]	V_{AUDIO} = 2.7V V_{bus} = 4.25V	R_L = 50Ω, C_L = 50pF, $t_R = t_F$ = 12ns (10-90%) at 12Mbps (PRBS = 2^{15} – 1)		1.6		ns

Notes:

9. Guaranteed by characterization, not production tested.

Capacitance

Symbol	Parameter	V_{AUDIO}/ V_{bus}(V)	Conditions	T_A = - 40ºC to +85ºC			Unit
				Min.	Typ.	Max.	
$C_{IN\ (ASel)}$	Control Pin Input Capacitance (A_{Sel})	V_{AUDIO} = 2.7V V_{bus} = 4.25V	V_{Bias} = 0.2V		2.5		pF
$C_{ON(D+/R,\ D-/L)}$	D+/R, D-/L (Common Port) On Capacitance	V_{AUDIO} = 2.7V V_{bus} = 4.25V A_{Sel} = 0V (C_{ONUSB})	V_{Bias} = 0.2V f = 6MHz See Figure 26		25		pF
		V_{AUDIO} = 2.7V V_{bus} = 4.25V A_{Sel} = 2.7V ($C_{ONAudio}$)	V_{Bias} = 0.2V f = 6MHz See Figure 26		29		
$C_{OFF(D+,\ D-)}$	USB Input Source Off Capacitance	V_{AUDIO} = 2.7V V_{bus} = 4.25V A_{Sel} = 2.7V	f = 6MHz See Figure 25		5		pF
$C_{OFF(R/L)}$	Audio Input Source Off Capacitance	V_{AUDIO} = 2.7V V_{bus} = 4.25V A_{Sel} =0V	f = 6MHz See Figure 25		17		pF

Typical Characteristics

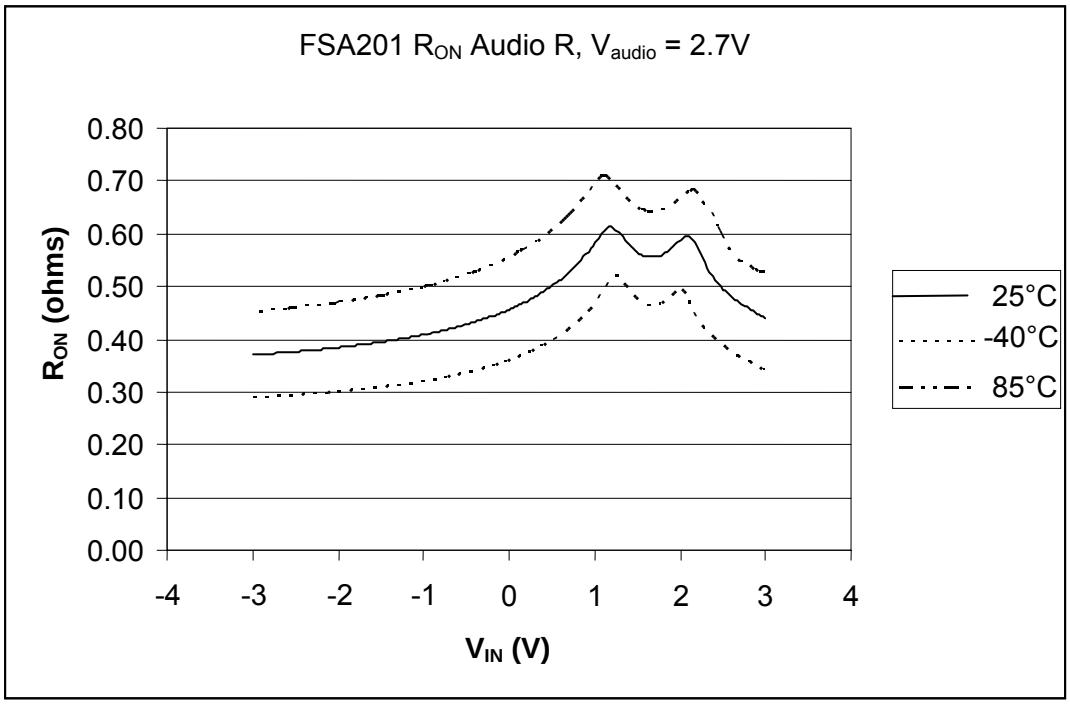


Figure 5. R_{ON} Audio Characterization

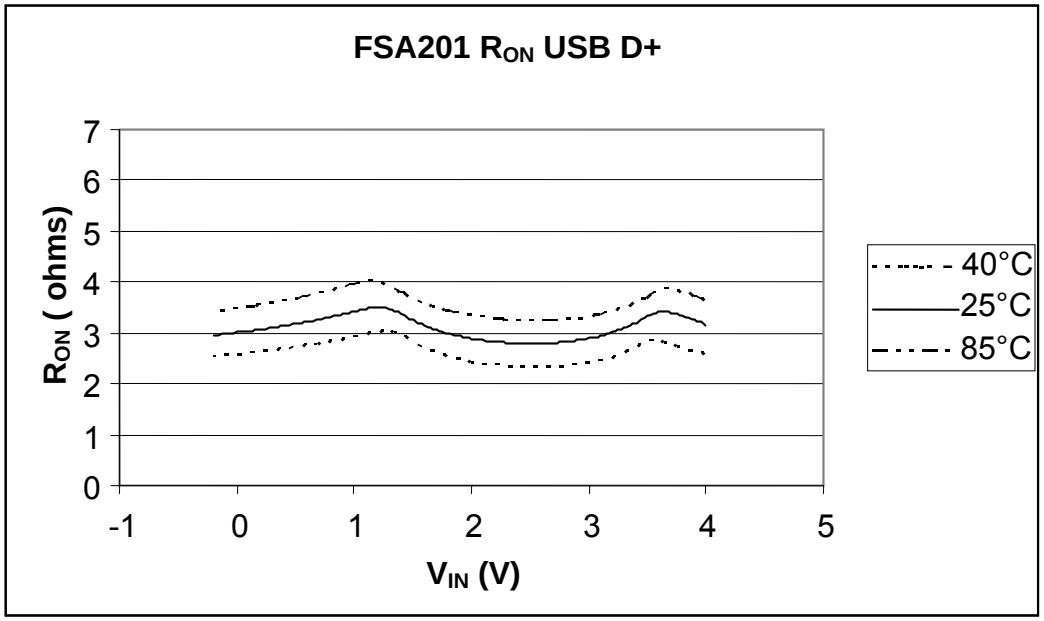


Figure 6. R_{ON} USB Characterization

Typical Characteristics (Continued)

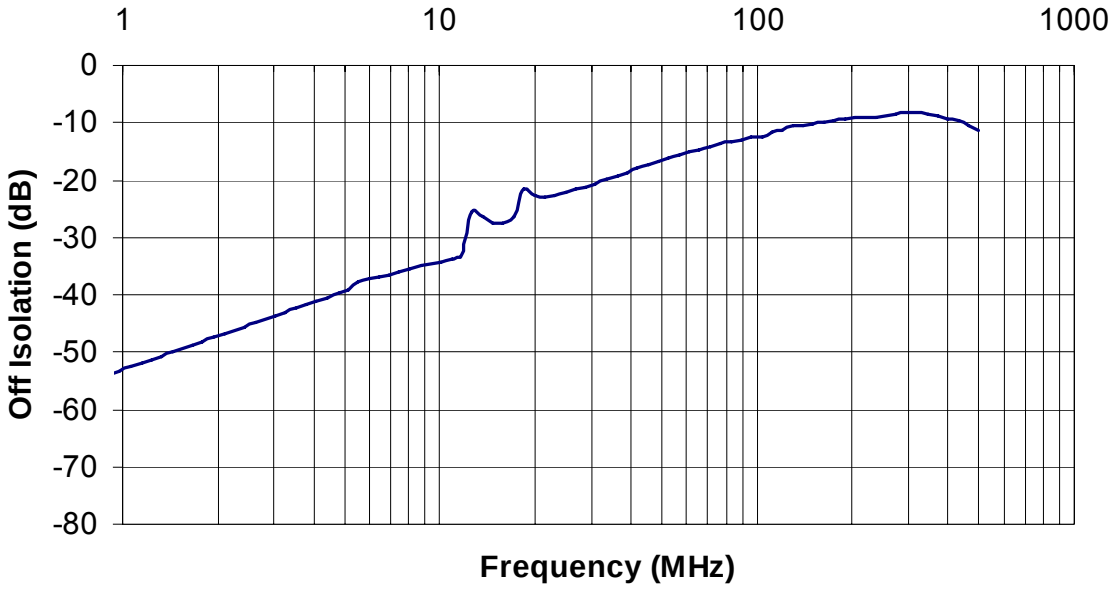


Figure 7. Off-Isolation (Audio) Characterization Frequency Response at V_{CC} (V_{AUDIO}) = 2.7V

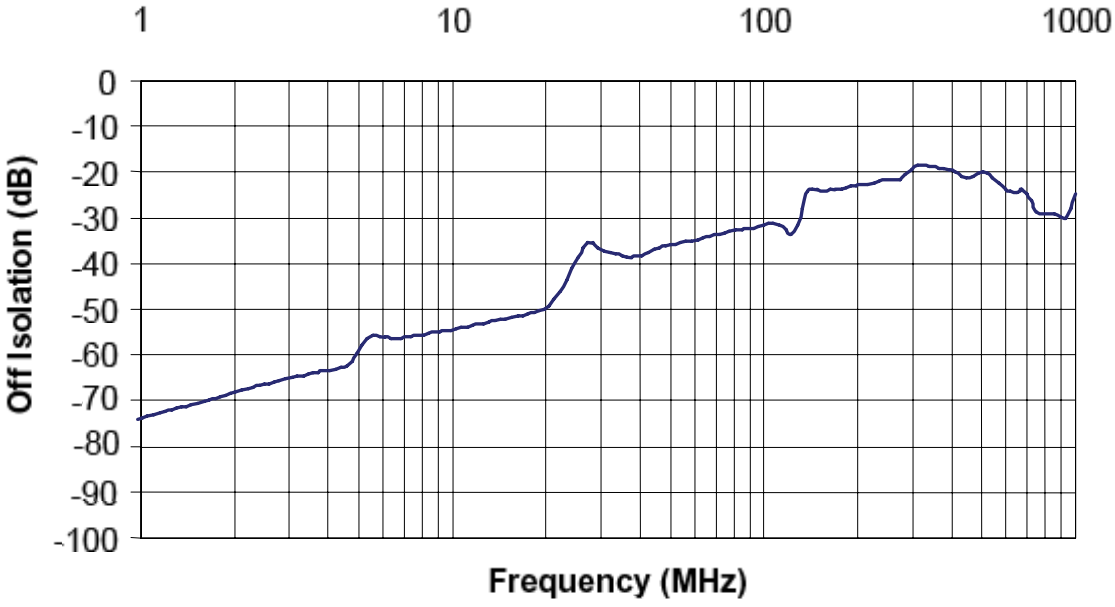


Figure 8. Off-Isolation (USB) Characterization Frequency Response at V_{CC} (V_{bus}) = 4.25V

Typical Characteristics (Continued)

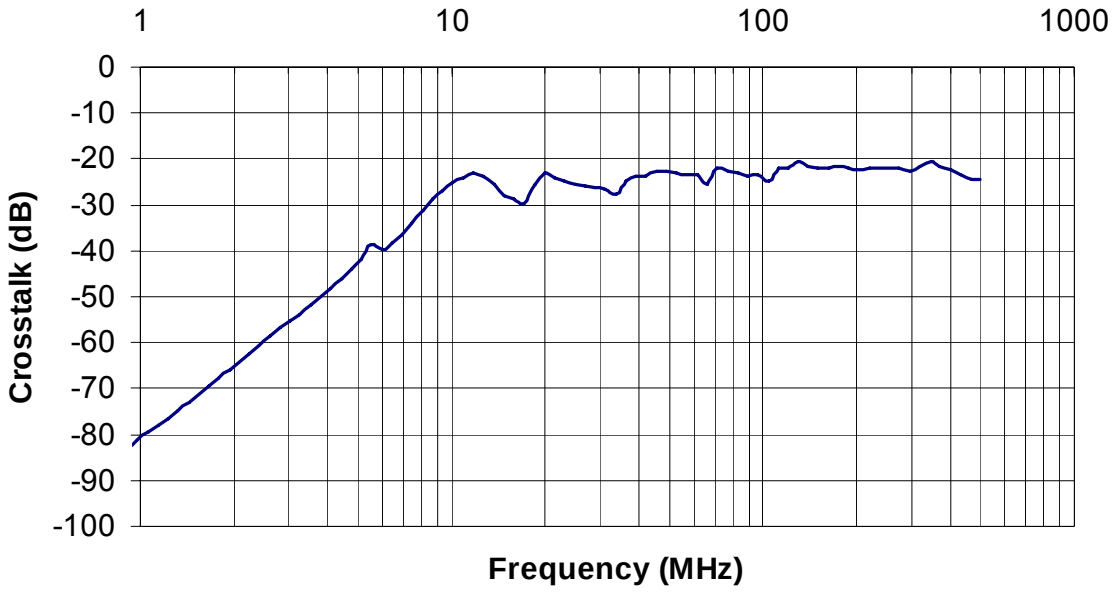


Figure 9. Non-Adjacent Channel Crosstalk (Audio) Characterization at V_{CC} (V_{AUDIO}) = 2.7V

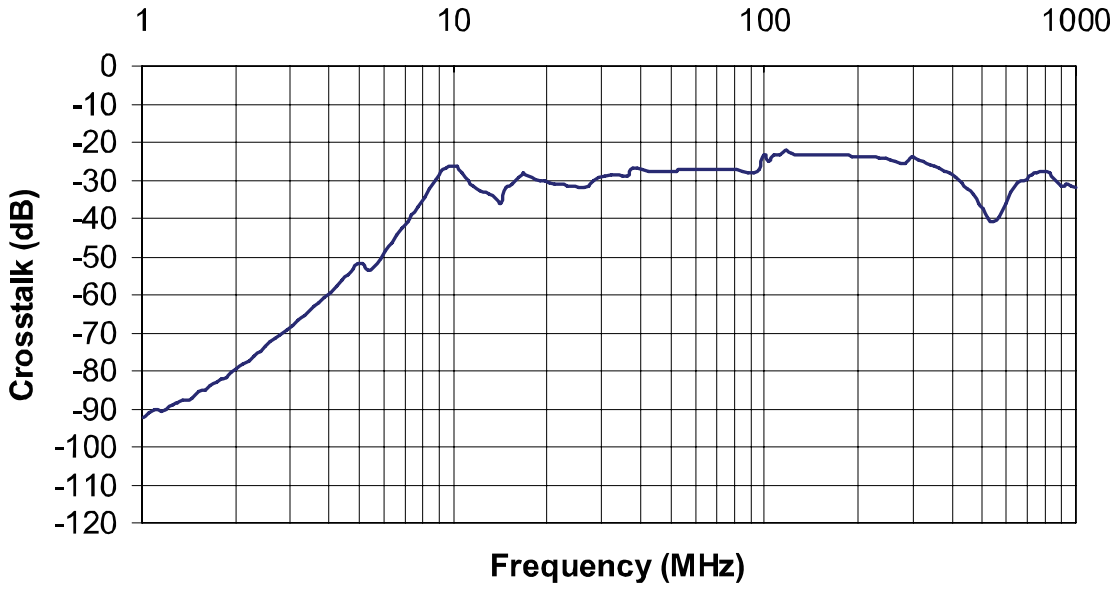


Figure 10. Non-Adjacent Channel Crosstalk (USB) Characterization at V_{CC} (V_{bus}) = 4.25V

Typical Characteristics (Continued)

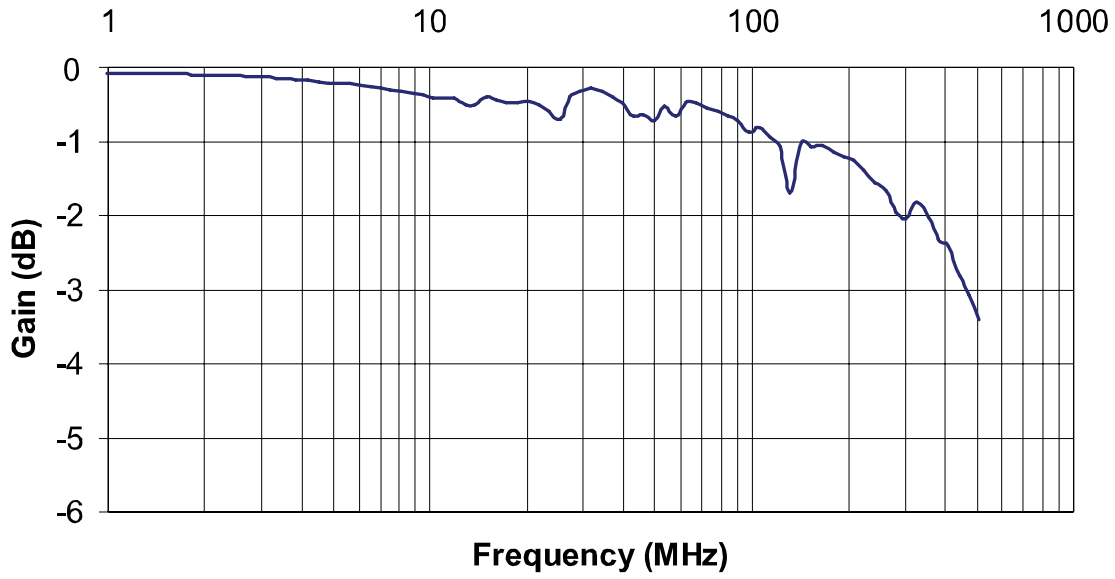


Figure 11. Bandwidth Characterization, Frequency Response at C_L = 0pF, V_{CC} (V_{AUDIO}) = 2.7V

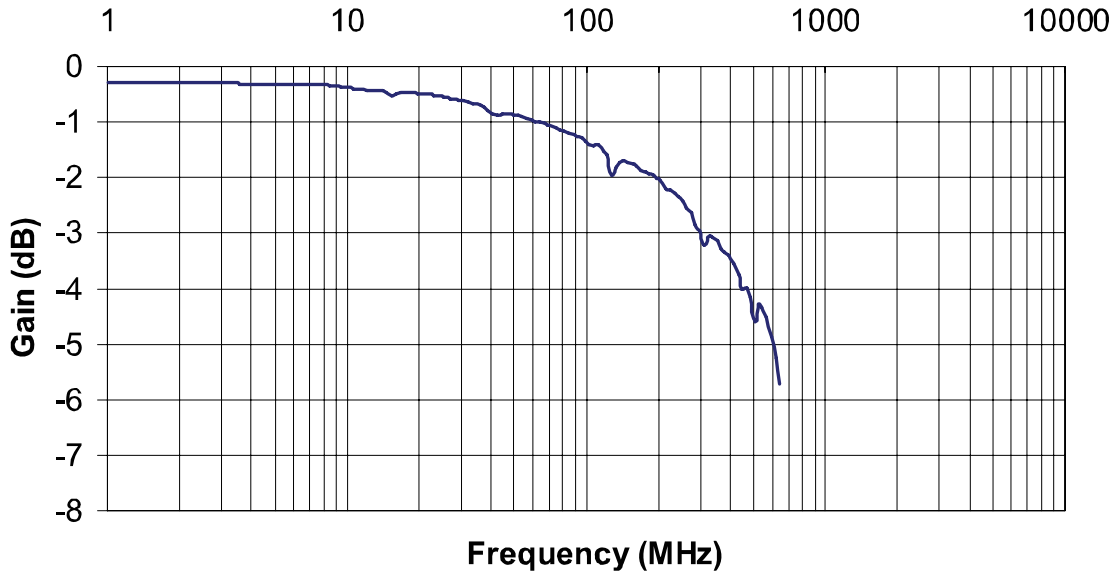


Figure 12. Bandwidth Characterization, Frequency Response at C_L = 0pF, V_{CC} (V_{BUS}) = 4.25V

Test Diagrams

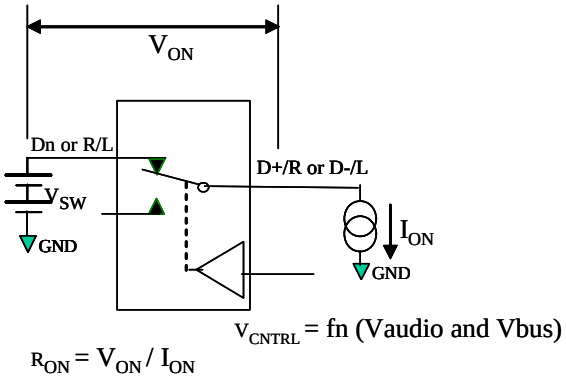


Figure 13. On Resistance

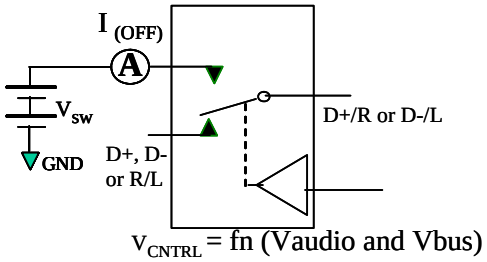


Figure 14. Off Leakage

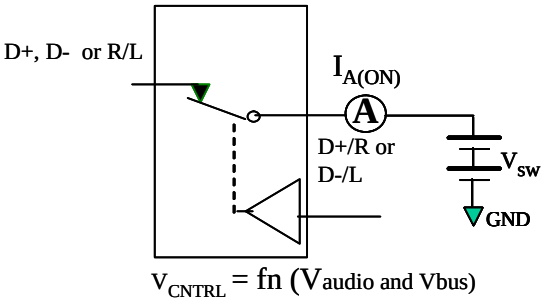
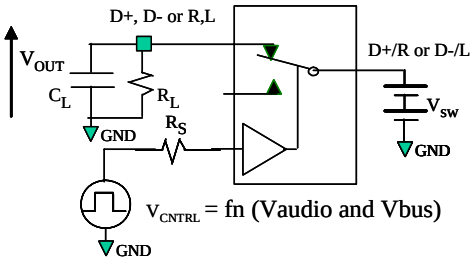


Figure 15. On Leakage



R_L , R_S and C_L are function of application environment (see AC Tables for specific values)
C_L includes test fixture and stray capacitance

Figure 16. AC Test Circuit Load

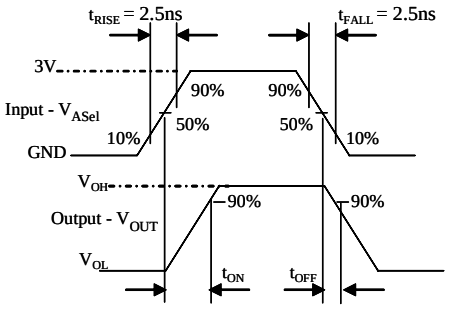


Figure 17. Turn-On / Turn-Off Waveforms (A_{Sel})

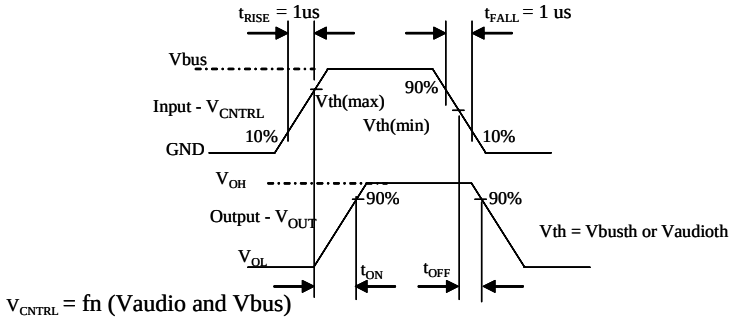


Figure 18. Turn-On / Turn-Off Waveforms (USB/Audio)

Test Diagrams (Continued)

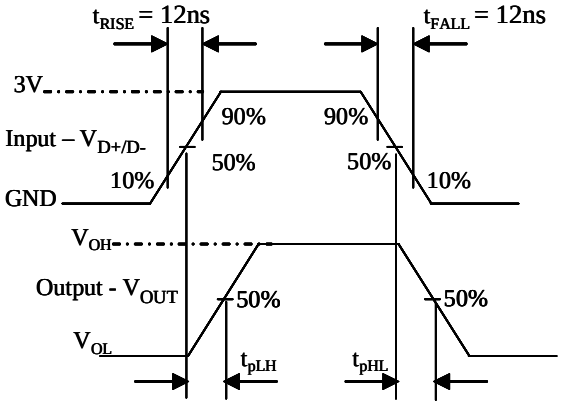


Figure 19. USB Switch Propagation Delay Waveforms

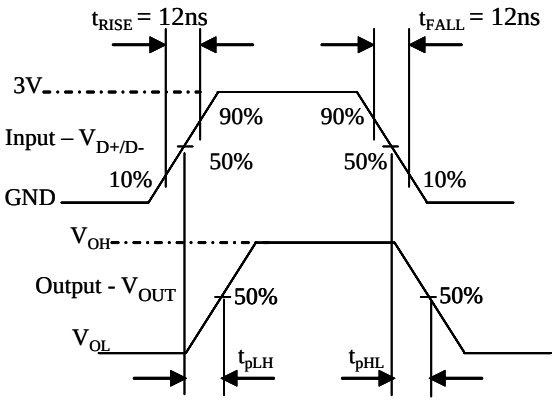


Figure 20. Pulse Skew: $t_{SK(P)}$ = | t_{PHL} – t_{PLH} |

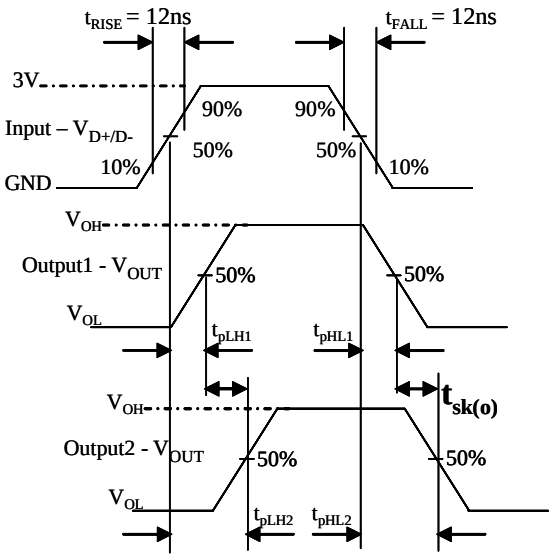


Figure 21. Output Skew: $t_{SK(O)}$ = | t_{PLH1} – t_{PLH2} | or | t_{PHL1} – t_{PHL2} |

Test Diagrams (Continued)

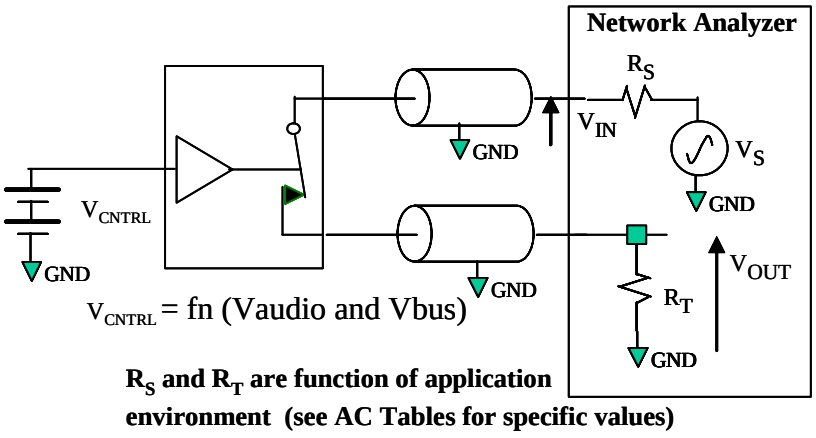


Figure 22. USB Bandwidth

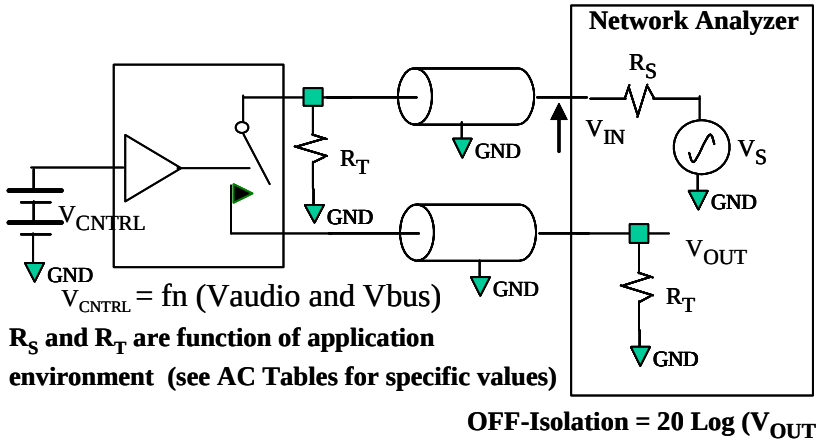


Figure 23. Channel Off Isolation

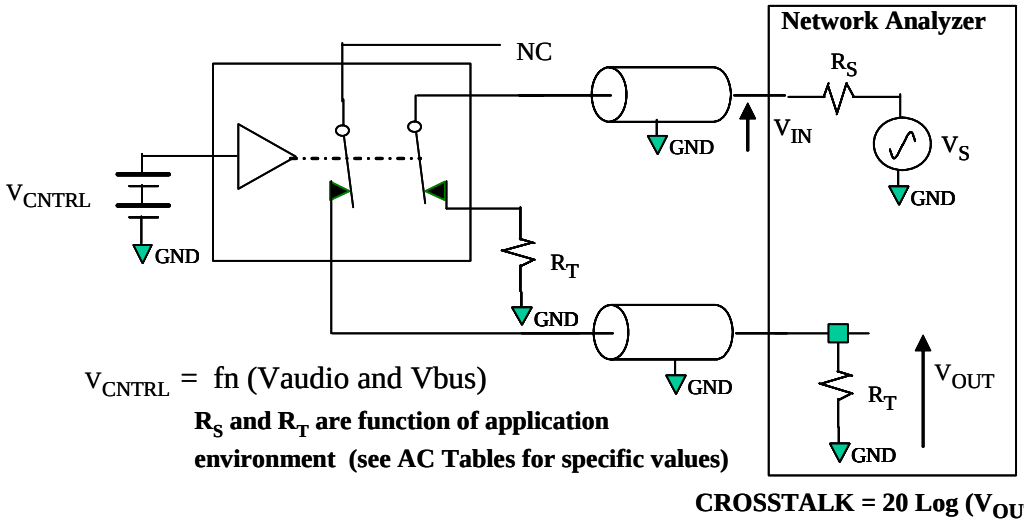


Figure 24. Non-Adjacent Channel-to-Channel Crosstalk

Test Diagrams (Continued)

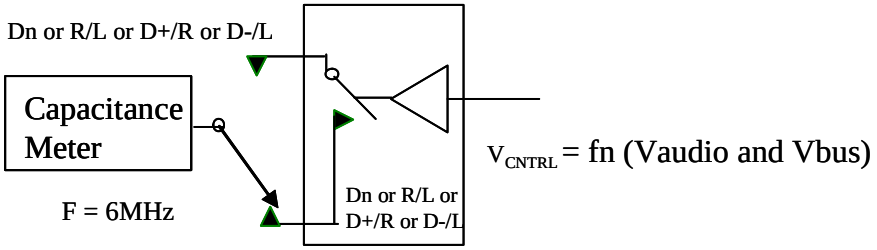


Figure 25. Channel Off Capacitance

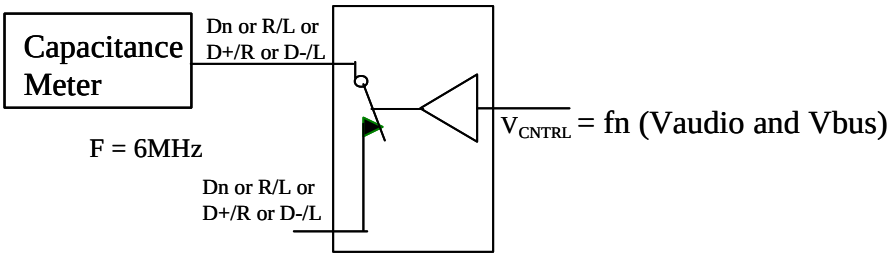
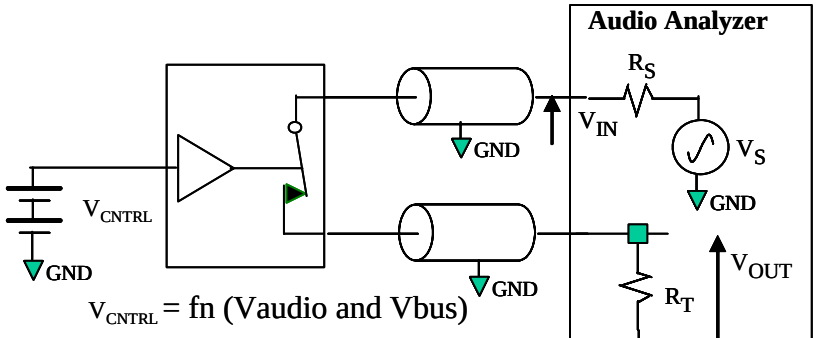


Figure 26. Channel On Capacitance

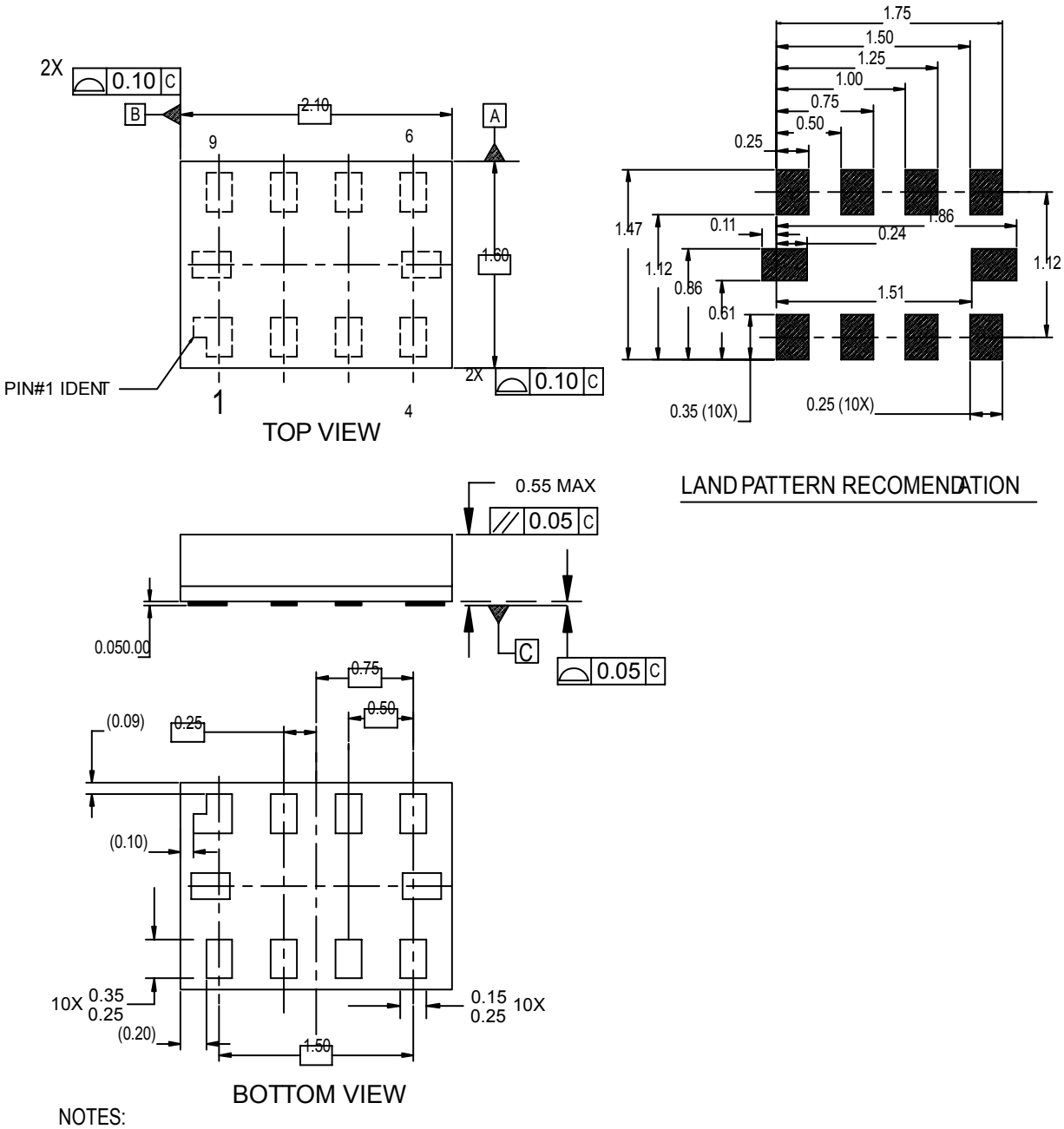


R_S and R_T are function of application environment (see AC Tables for specific values)

Figure 27. Total Harmonic Distortion

Physical Dimensions

Dimensions are in millimeters unless otherwise noted.



NOTES:

A. PACKAGE CONFORMS TO JEDEC MO255, VARIATION UABD

B. DIMENSIONS ARE IN MILLIMETERS.

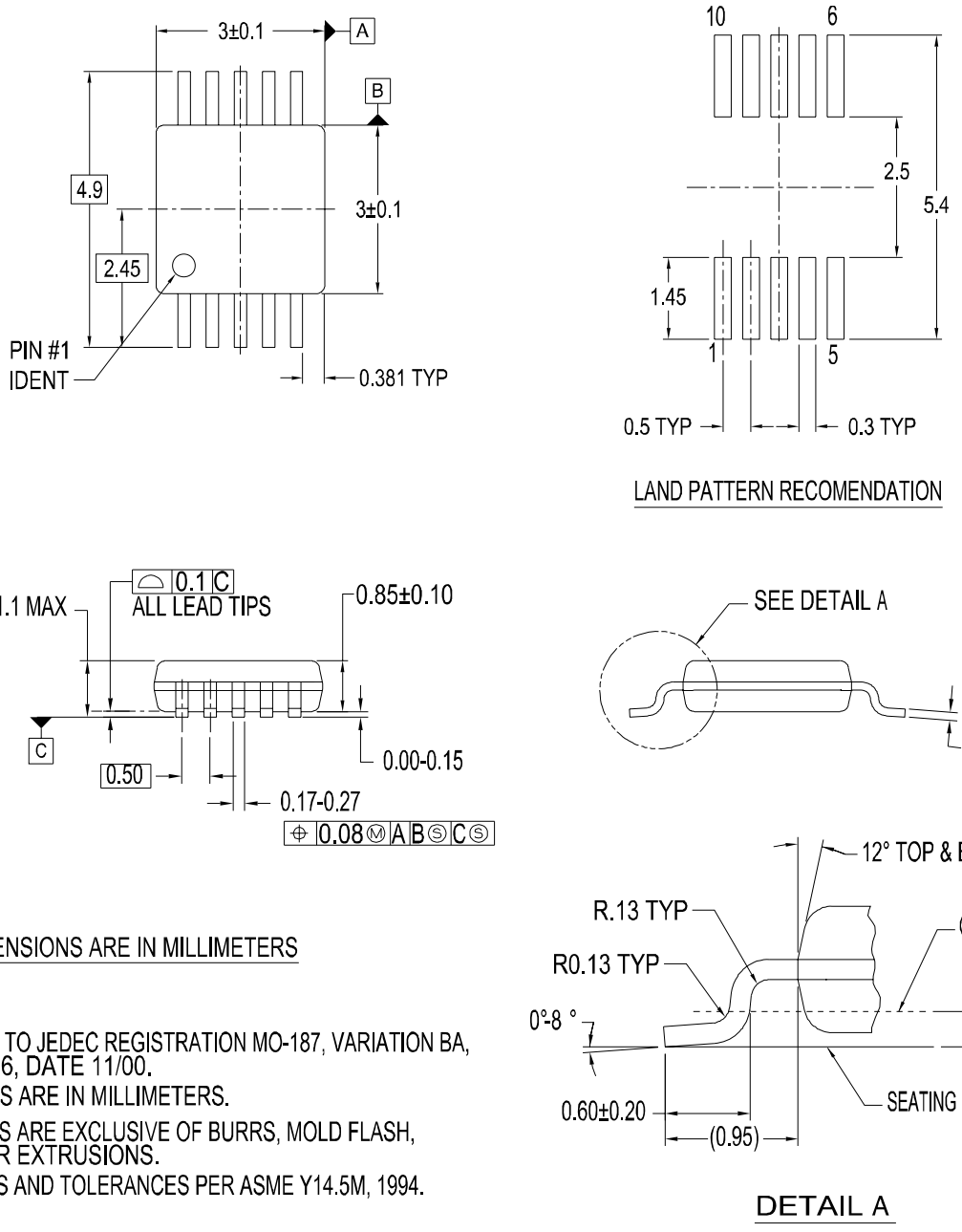
C. DIMENSIONS AND TOLERANCES CONFORM TO ASME Y14.5M, 1994.

MAC010ARevC

Figure 28. 10-Lead MicroPak

Physical Dimensions

Dimensions are in millimeters unless otherwise noted.



DIMENSIONS ARE IN MILLIMETERS

NOTES:

A. CONFORMS TO JEDEC REGISTRATION MO-187, VARIATION BA, REF NOTE 6, DATE 11/00.

B. DIMENSIONS ARE IN MILLIMETERS.

C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

D. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.

MUA10AREVA

Figure 29. 10-Lead MSOP (Preliminary)

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	GlobalOptoisolator™	OCXPro™	μSerDes™	TinyBoost™
ActiveArray™	GTO™	OPTOLOGIC®	SILENT SWITCHER®	TinyBuck™
Bottomless™	HiSeC™	OPTOPLANAR™	SMART START™	TinyLogic®
Build it Now™	I^2C™	PACMAN™	SPM™	TINYOPTO™
CoolFET™	*i-Lo*™	POP™	Stealth™	TinyPower™
CROSSVOLT™	ImpliedDisconnect™	Power247™	SuperFET™	TinyPWM™
DOME™	IntelliMAX™	PowerEdge™	SuperSOT™-3	TruTranslation™
EcoSPARK™	ISOPLANAR™	PowerSaver™	SuperSOT™-6	UHC®
E^2CMOS™	LittleFET™	PowerTrench®	SuperSOT™-8	UniFET™
EnSigna™	MICROCOUPLER™	QFET®	SyncFET™	VCX™
FACT®	MicroFET™	QS™	TCM™	Wire™
FACT Quiet Series™	MicroPak™	QT Optoelectronics™		
FAST®	MICROWIRE™	Quiet Series™		
FASTr™	MSX™	RapidConfigure™	Across the board. Around the world.™	
FPS™	MSXPro™	RapidConnect™	Programmable Active Droop™	
FRFET™	OCX™	ScalarPump™	The Power Franchise®	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild Semiconductor. The datasheet is printed for reference information only.

Rev. I22