

January 2011

FSAV433 — High-Bandwidth (550MHz), 3-Channel, 3:1 Video Switch

Features

- Ground between Channels to Optimize Isolation and Reduce Hostile Crosstalk
- -70dB Non-Adjacent Channel Crosstalk at 30MHz
- On Resistance: 6.5Ω (Typical)
- -3dB Bandwidth: 550MHz
- Low Power Consumption: 1µA (Maximum)

Applications

- RGB Video Switch in LCD, Plasma and Projector Displays
- DVD-RW

Description

The FSAV433 is an ultra-low power, high-bandwidth video switch specially designed for switching three analog video signals, including computer RGB and high-definition YPbPr signals. The wide bandwidth (550MHz) of the switch allows signal passage with minimum edge and phase distortion, while -70dB non-adjacent channel crosstalk generates negligible image noise between active channels. Optimized differential gain and phases maintain the image integrity of video applications, while low on resistance offers low signal insertion loss.

The Fairchild switch family derives from and embodies Fairchild's proven switch technology used for years in its 74LVX3L384 (FST3384) bus switch product.

Ordering Information

Part Number	Operating Temperature Range	Package	Packing Method
FSAV433MTCX	-40 to +85°C	20-Lead, Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide	Tape and Reel

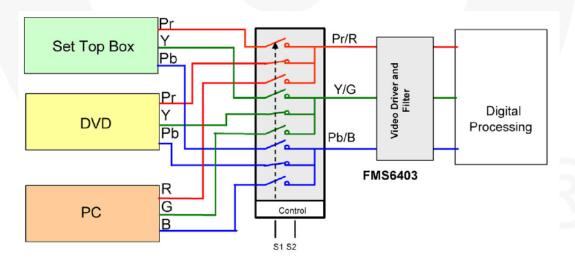


Figure 1. Typical Application Diagram

Pin Configurations

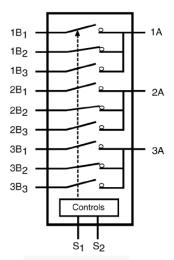


Figure 2. Analog Symbol

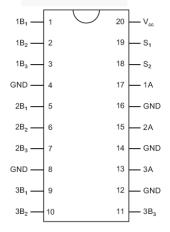


Figure 3. TSSOP Pin Assignments (Top Through View)

Pin Descriptions

Pin #	Name	Description	
1, 2, 3, 5, 6, 7, 9, 10, 11	1B ₁ , 2B ₂ , 3B ₂	Bus B	
4, 8, 12, 14 ,16	GND	Ground	
13, 15, 17	1A, 2A, 3A	Bus A	
18, 19	S ₁ , S ₂	Select Input	
20	V _{CC}	Supply Voltage	

Truth Table

S ₁	S ₂	Function
LOW	LOW	Disconnected
LOW	HIGH	A=B ₁
HIGH	LOW	A=B ₂
HIGH	HIGH	A=B ₃

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit
V _{CC}	Supply Voltage	-0.5	+4.6	V
Vs	DC Switch Voltage	-0.5	V _{CC} +0.5	V
V _{IN}	DC Input Voltage ⁽¹⁾	-0.5	+4.6	V
I _{IK}	DC Input Diode Current, V _{IN} < 0V	-50		mA
I _{OUT}	DC Output Sink Current		100	mA
I _{CC} /I _{GND}	DC V _{CC} / GND Current		±100	mA
T _{STG}	Storage Temperature Range	-65	+150	°C
ESD	Human Body Model, JESD22-A114		7000	V

Note

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
V _{CC}	Power Supply Operating	2.3	3.6	V
V_{IN}	Input Voltage	0	V_{CC}	V
T _A	Operating Temperature, Free Air	-40	+85	°C

Note:

DC Electrical Characteristics

Typical values are at T_A = +25°C.

Cumbal	Donomoton	Conditions V (V)	V (\(\)	T _A =	Γ _A = -40 to +85°C		11
Symbol	Parameter	Conditions	V _{cc} (V)	Min.	Тур.	Max.	Units
V _{ANALOG}	Analog Signal Range		2.3 to 3.0	0		2	V
V _{IK}	Clamp Diode Voltage	I _{IN} =-18mA	3.0			-1.2	V
W	High Lovel Input Voltage		2.3	1.8			V
V _{IH}	V _{IH} High-Level Input Voltage		3.0 to 3.6	2.0		/	V
V _{IL} Low-Leve	Law Lavel Input Valtage		2.3			0.8	V
	Low-Level Input Voltage		3.0 to 3.6				V
l _l	Input Leakage Current	$0 \le V_{IN} \le 3.6V$	3.6			±1.0	μΑ
I _{OFF}	Off-State Leakage Current	$0 \le A, B \le V_{CC}$, See Figure 9	3.6			±1.0	μΑ
		V _{IN} =1.0V, I _{ON} =13mA,	2.3		9.0	13.0	
П	Switch On Resistance ⁽³⁾	See Figure 7	3.0		6.5	9.0	
R _{ON} Switch On Resi	Switch On Resistance	V _{IN} =2.0V, I _{ON} =26mA,	2.3		10.0	15.0	Ω
		See Figure 7	3.0		6.5	9.0	
I _{CC}	Quiescent Supply Current	V _{IN} =V _{CC} or GND, I _{OUT} =0	3.6			1	μA
I _{CCT}	Increase in I _{CC} per Input	One Control Input at 3.0V Other Inputs at V _{CC} or GND	3.6			10	μA

Note:

The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

^{2.} Unused control inputs must be held HIGH or LOW; they may not float.

^{3.} Measured by the voltage drop between the A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the A or B pins.

AC Electrical Characteristics

Typical values are at V_{CC} =3.3V and T_{A} = +25°C.

Symbol Parameter		Conditions	V	T _A =	T _A =-40 to+85°C		Units	Ciaura	
Symbol	Parameter	Conditions V _{CC}		Min.	Тур.	Max.	Units	Figure	
t _{on}	Turn On Time	V _B =2V	3.0 to 3.6			5.5	ns	Figure 8,	
LON	S to Bus A	VB-2V	2.3 to 2.7			7	113	Figure 10	
+	Turn Off Time	V _B =2V	3.0 to 3.6			4	no	Figure 8,	
t _{OFF}	S to Bus A	V _B -2V	2.3 to 2.7			5	ns	Figure 10	
D_G	Differential Gain	R _L =75Ω, f=3.58MHz	3.0 to 3.6		0.2		%		
D _P	Differential Phase	R _L =75Ω, f=3.58MHz	3.0 to 3.6		0.1		٥		
0	Non-Adjacent Off Isolation	D 750 (00M)	3.0 to 3.6		-45		15	F: 40	
O _{IRR}	Adjacent Off Isolation	R _L =75Ω, f=30MHz	2.3 to 2.7		-45		dB	Figure 13	
_	Non-Adjacent Channel Crosstalk	D -750 f-20MU-	3.0 to 3.6		-70		dB	Figure 15	
X _{TALK}	Adjacent Channel Crosstalk	R _L =75Ω, f=30MHz	2.3 to 2.7		-70		uБ	Figure 14	
В	2dD Dandwidth	R _L =50Ω	0.01.00			550		N 41 1-	Figure 40
B_W	-3dB Bandwidth	$R_L=75\Omega$	3.0 to 3.6		300		MHz	Figure 12	

Capacitance

Typical values are at V_{CC} = 3.3V and T_A =+25°C.

Symbol	Parameter	Conditions	Тур.	Units	Figure
C_{IN}	Control Pin Input Capacitance	V _{CC} =0V	3	pF	
C _{ON}	A/B On Capacitance	V _{CC} =3.0V=0V	15	pF	Figure 17
C_OFF	Port B Off Capacitance	V _{CC} =3.3V	4	рF	Figure 16

Typical Characteristics

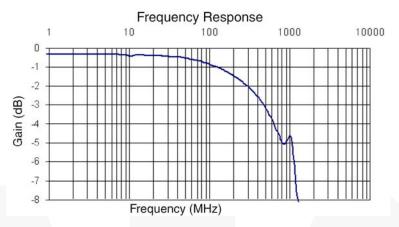


Figure 4. Gain vs. Frequency ($V_{BAIS}=0.5V$, $V_{CC}=3.6V$, $R_L=50\Omega$)

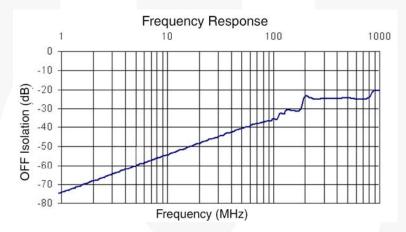


Figure 5. Off Isolation ($V_{BAIS}=0.5V$, $V_{CC}=3.0V$, $SEL_n=LOW$)

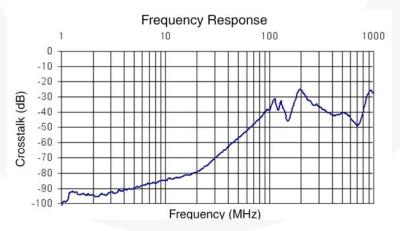


Figure 6. Crosstalk (V_{BAIS}=0.5V, V_{CC}=3. 0V, SEL_n=HIGH)

Test Diagrams

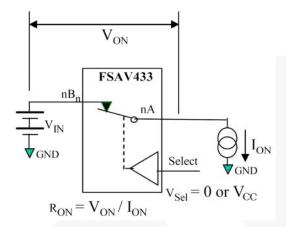


Figure 7. On Resistance

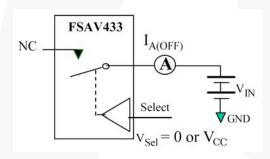
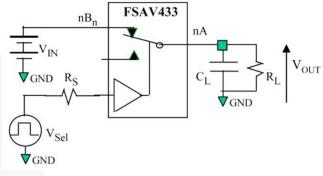


Figure 9. Off Leakage



Note:

4. R_L and C_L are functions of the application environment (50, 75, or 100 Ω) C_L includes test fixture and stray capacitance.

Figure 8. Test Circuit Load

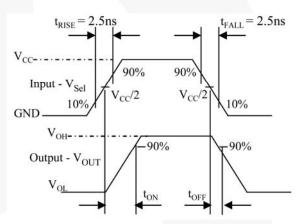


Figure 10. Turn On / Off Waveforms

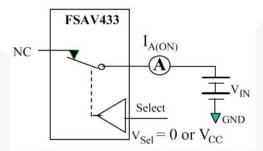
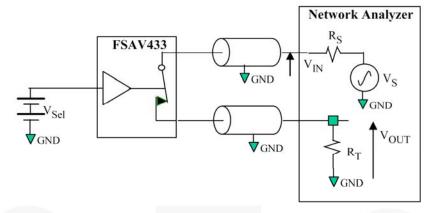


Figure 11. On Leakage

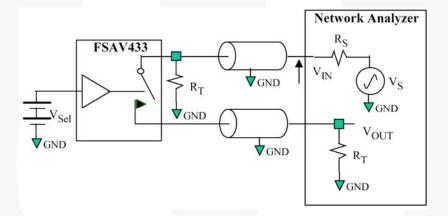
Test Diagrams (Continued)



Notes:

5. R_S and R_T are functions of the application environment (50, 75, or 100 Ω).

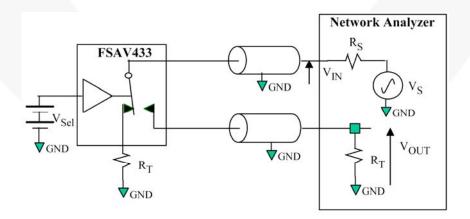
Figure 12. Bandwidth



Notes:

- 6. R_S and R_T are functions of the application environment (50, 75, or 100 Ω).
- 7. Off isolation=20 Log (V_{OUT} / V_{IN}).

Figure 13. Channel Off Isolation

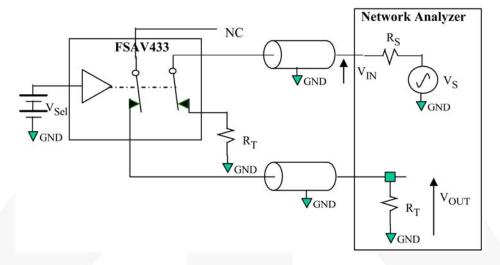


Note:

8. Crosstalk=20 Log (V_{OUT} / V_{IN}).

Figure 14. Adjacent Channel Crosstalk

Test Diagrams (Continued)



Notes:

- 9. R_S and R_T are functions of the application environment (50, 75, or 100 Ω).
- 10. Crosstalk=20 Log (V_{OUT} / V_{IN}).

Figure 15. Non-Adjacent Channel-to-Channel Crosstalk

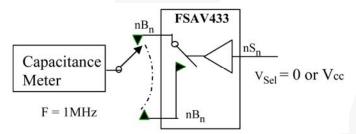


Figure 16. Channel Off Capacitance

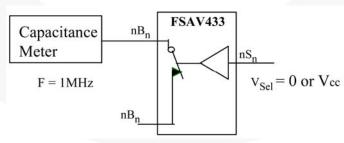
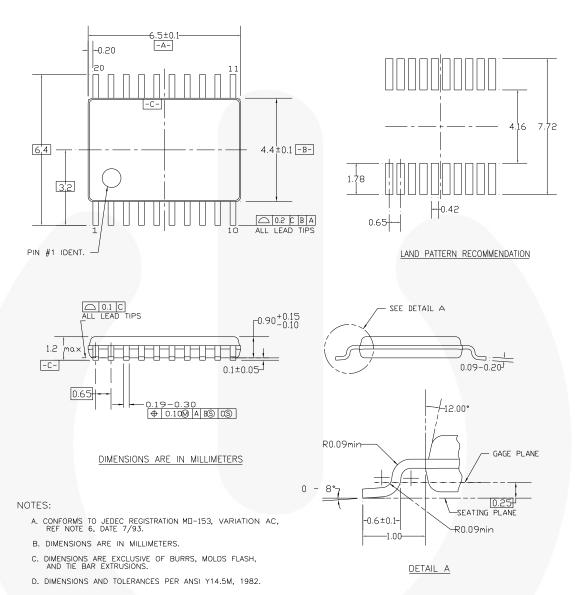


Figure 17. Channel On Capacitance

Physical Dimensions



MTC20REVD1

Figure 18. 20-Lead, Thin Shrink Small Outline Package (TSSOP)

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