

2012 Size

Chip Multilayer RF Transformer

FSLT-S009T[]-[]

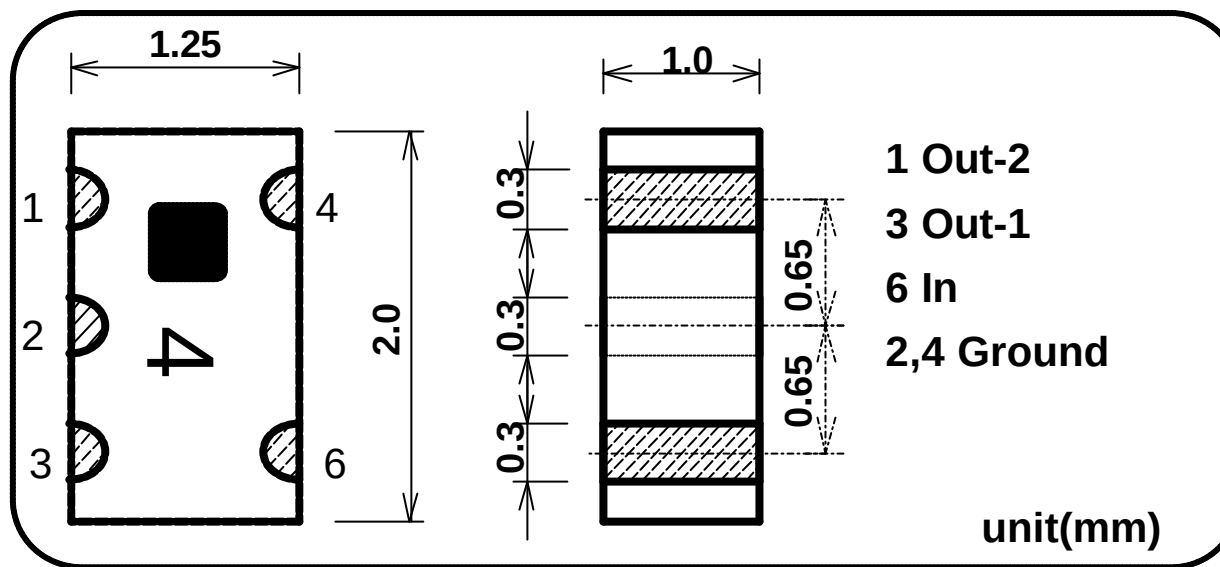
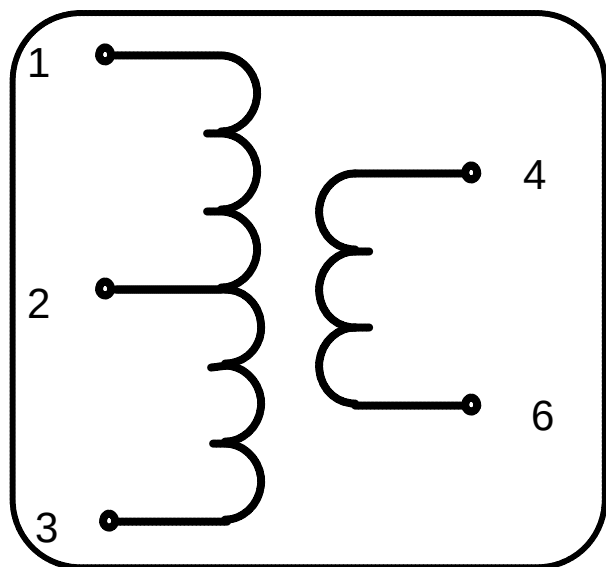


All the technical data and information contained herein are subject to change without notice.

Equivalent Circuits

Shape & Size

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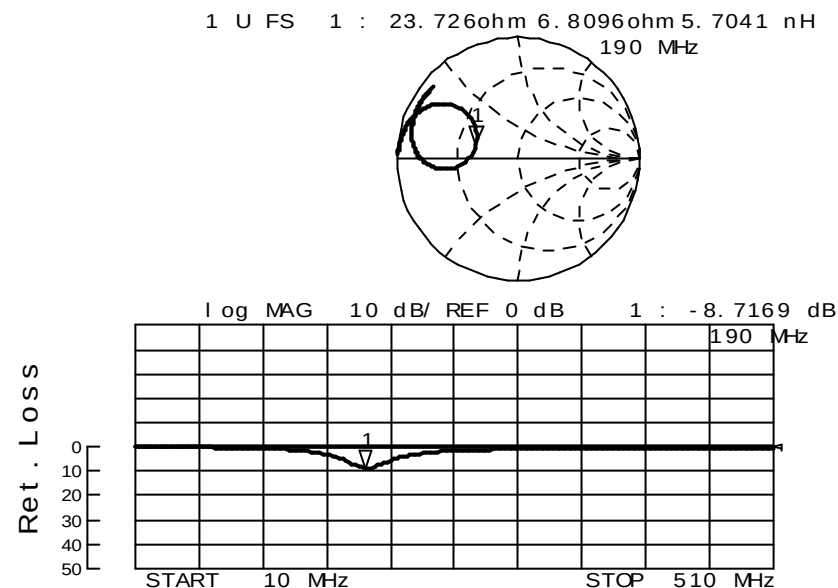
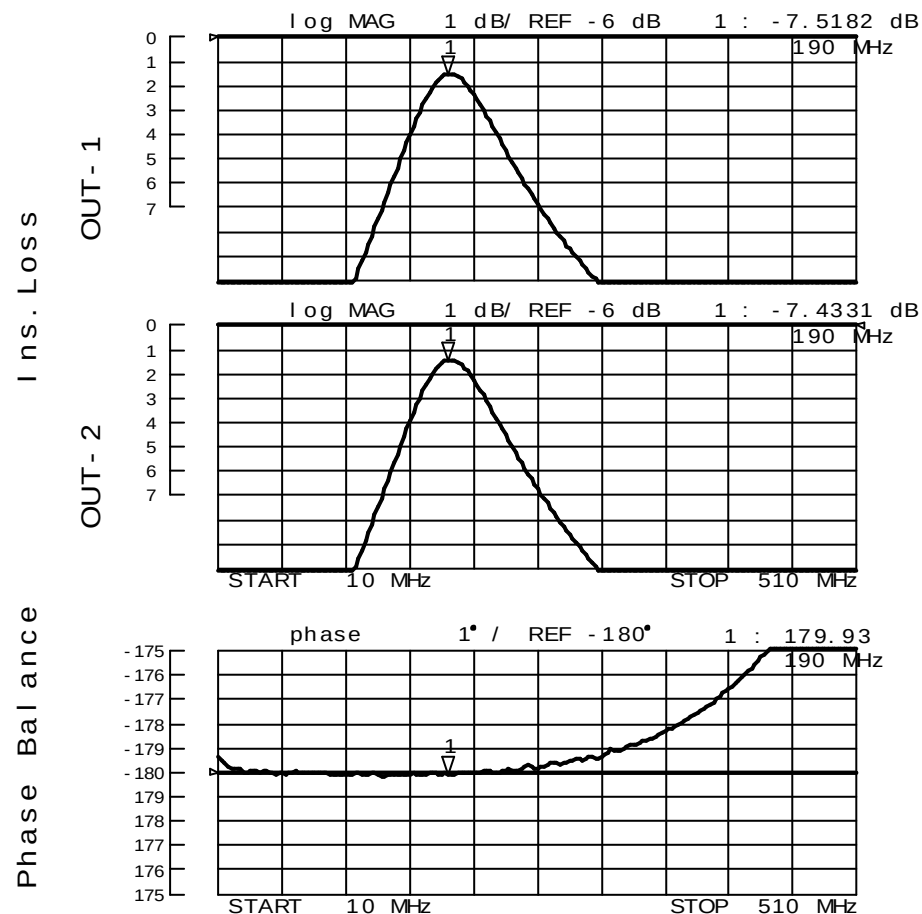
Specifications

P/N	Impedance	Frequency [MHz]	Matching Capacitance	Insertion Loss *1	Phase Balance	Handling Power	Temperature Range
FSLT-S009T4-019	50:200	190	15pF	4.0dB Max. (1.5dB Typ.)	180 +/- 10deg.	100mW Max.	-35 to +85 deg.C
FSLT-S009T4-038		380	3pF	4.0dB Max. (1.0dB Typ.)			
FSLT-S009T8-019	50:400	190	15pF	4.0dB Max. (1.5dB Typ.)			
FSLT-S009T8-038		380	3pF	4.0dB Max. (0.8dB Typ.)			

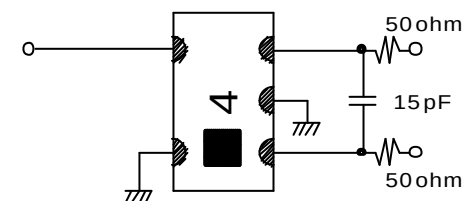
*1:Not include splitting loss & resistor loss

FSLT-S009T4-019 Sample Data

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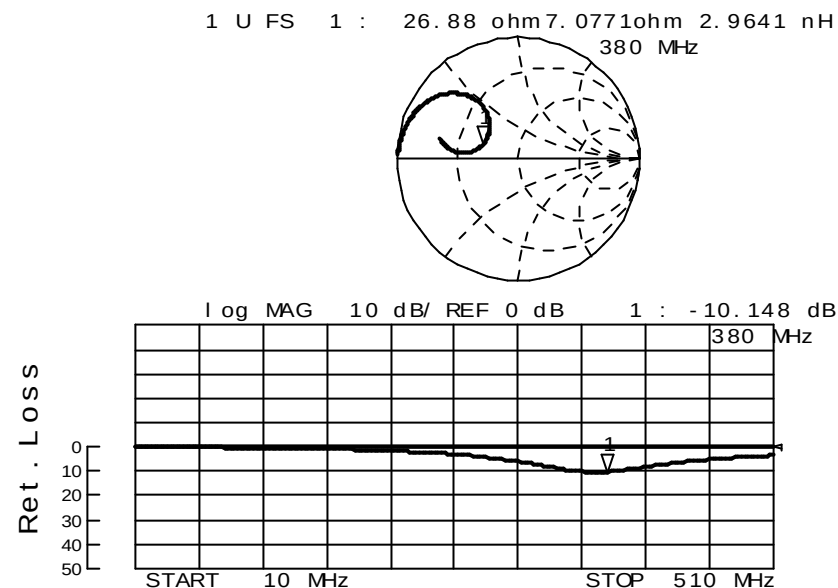
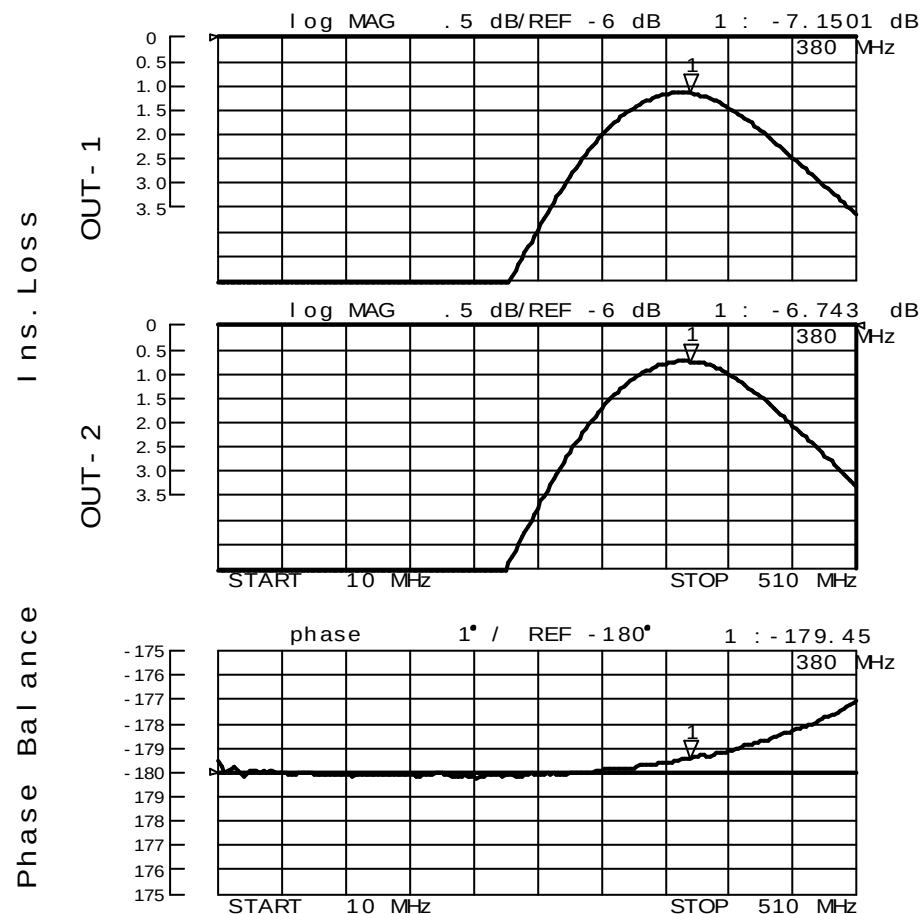
Chip RF Transformer "FSLT-S009T4-019"
50ohm : 200ohm



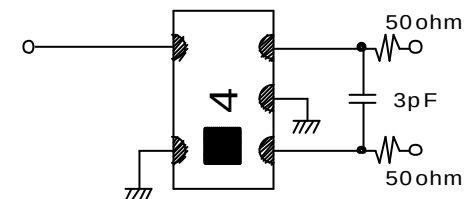
Insertion Loss(Out 1, Out 2):
measurement - splitting loss-resistor loss

FSLT-S009T4-038 Sample Data

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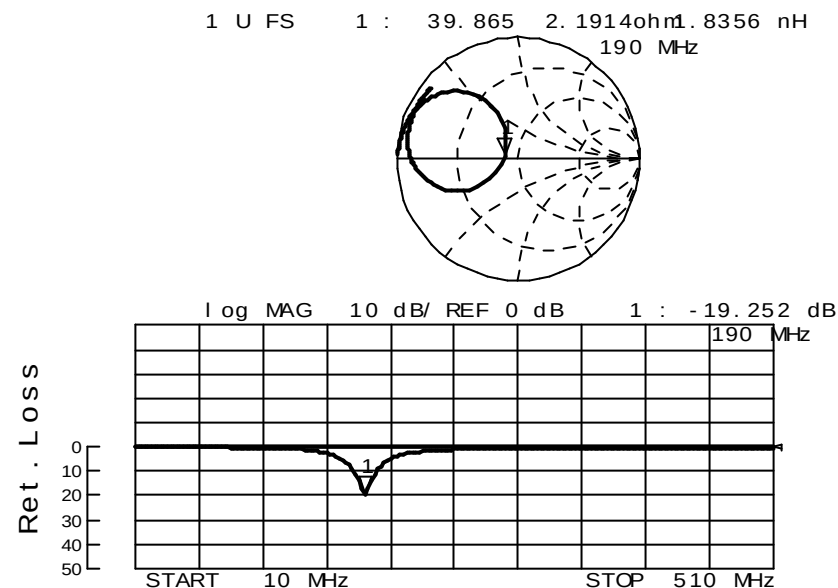
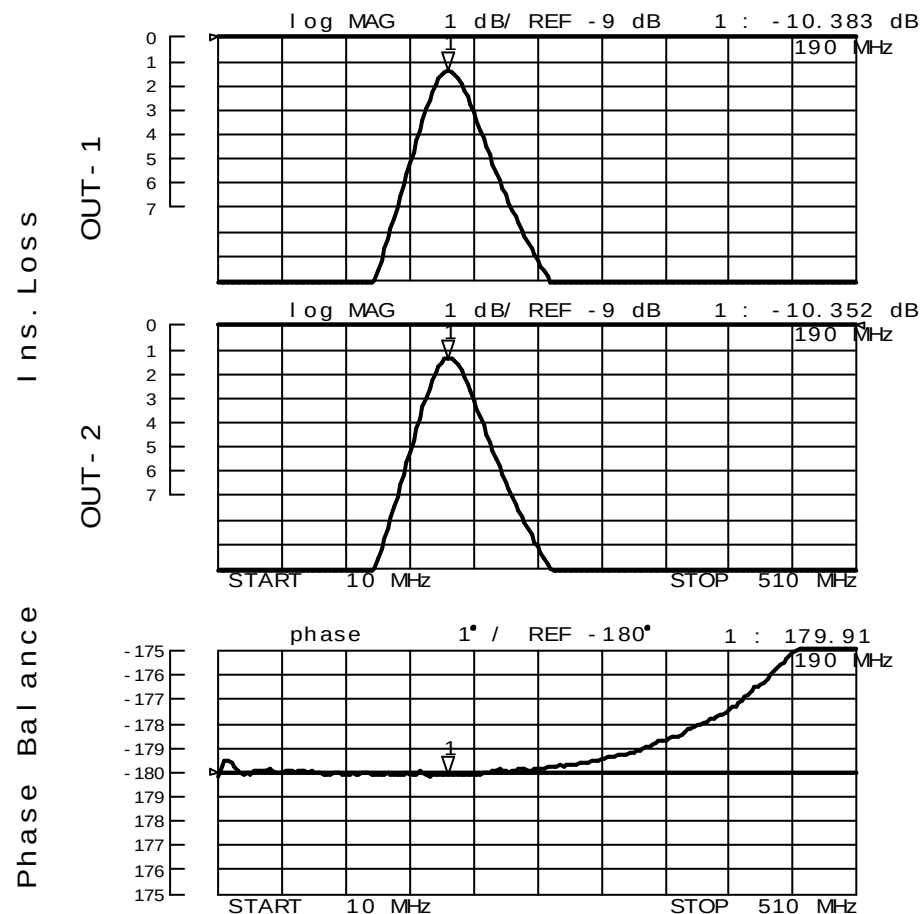
Chip RF Transformer "FSLT-S009T4-038"
50ohm : 200ohm



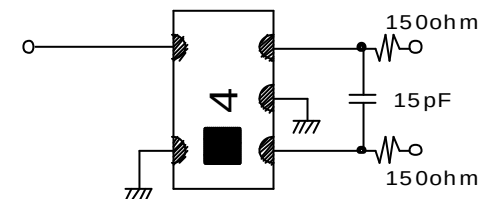
Insertion Loss(Out 1, Out 2):
measurement - splitting loss-resistor loss

FSLT-S009T8-019 Sample Data

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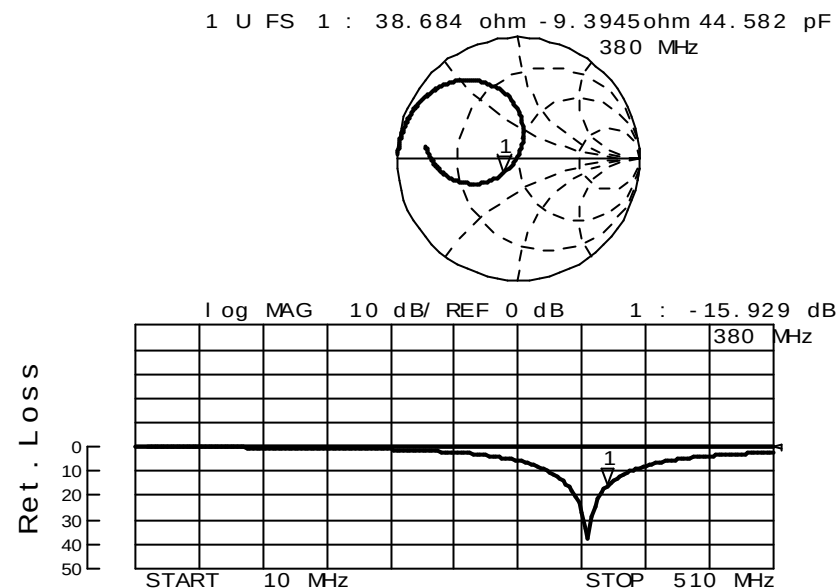
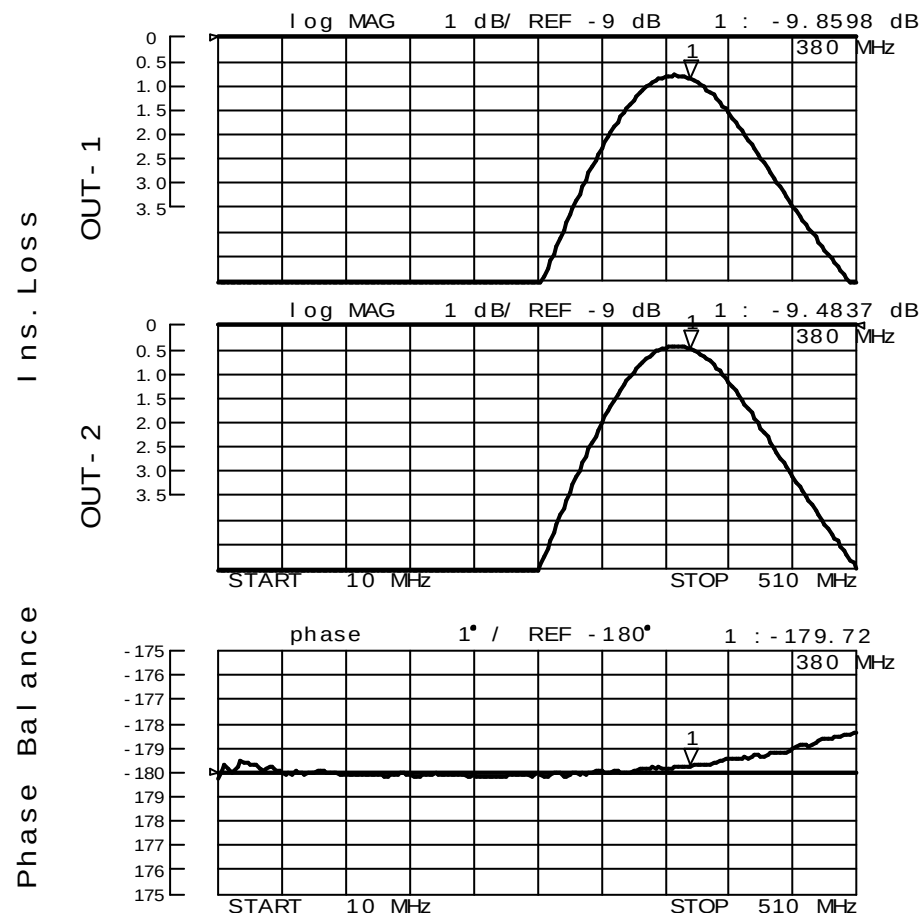
Chip RF Transformer "FSLT-S009T8-019"
50ohm : 400ohm



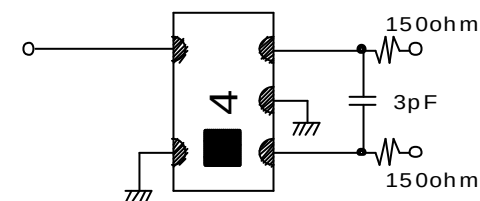
Insertion Loss(Out 1, Out 2):
measurement - splitting loss-resistor loss

FSLT-S009T8-038 Sample Data

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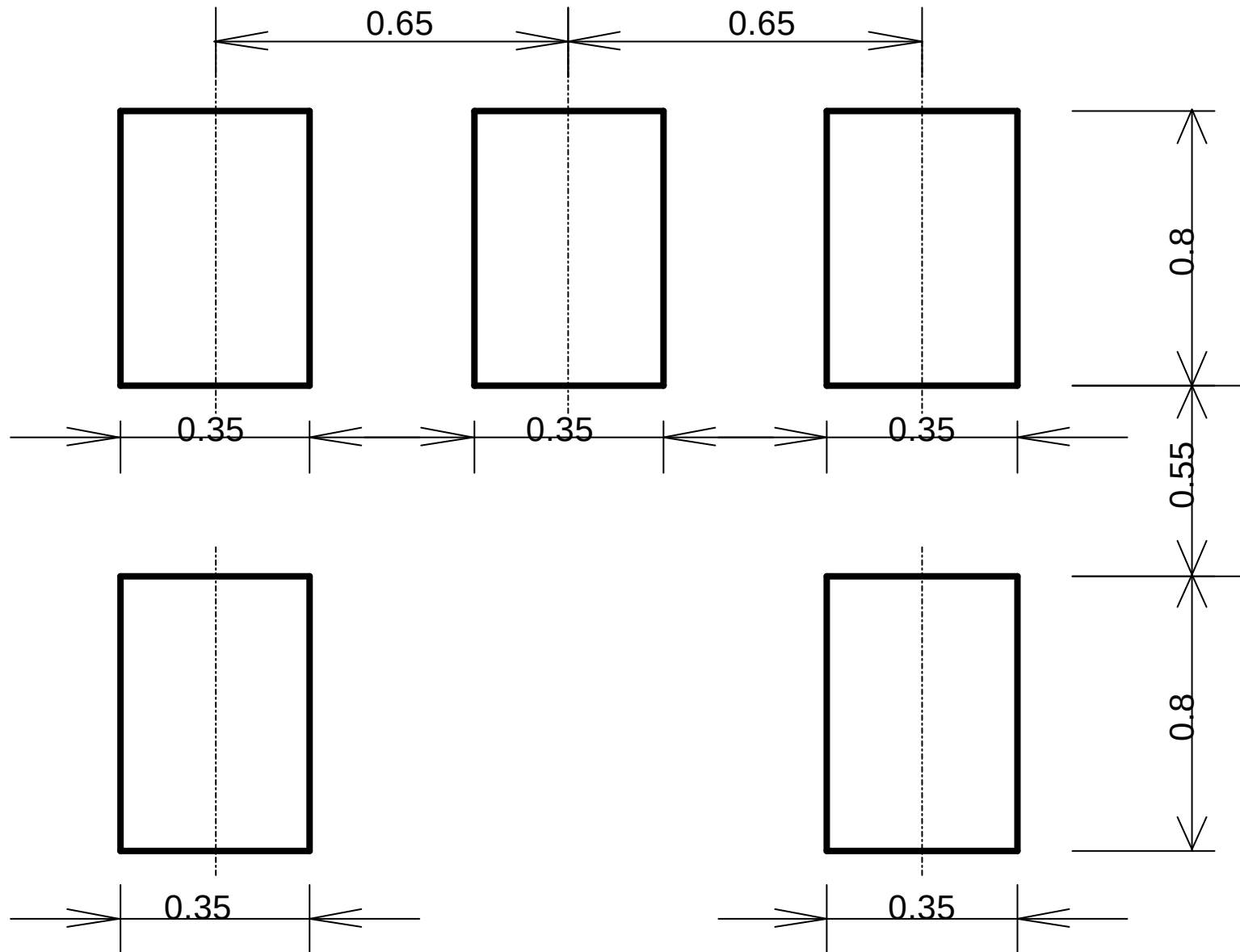
Chip RF Transformer "FSLT-S009T8-038"
50ohm : 400ohm



Insertion Loss(Out 1, Out 2):
measurement - splitting loss-resistor loss

FOOT PATTERN

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Do not apply DC voltage between primary and secondary winding.
If you apply DC voltage between primary and secondary winding,
use DC cut capacitor as below circuit A.

- C i r c u i t A -

