

2x5 SFF GE-PON ONU Transceiver

FTM-9412P-F20

(IEEE 802.3ah™-2004 1000BASE-PX20-U)

Members of Flexon™ Family



Applications

- ◆ Gigabit Ethernet Passive Optical Networks (GE-PON) – ONU side
- ◆ Gigabit Ethernet Point-to-Point Bi-directional Transmission
- ◆ Media Converts for Fiber-In-The-Loop (FITL)

Features

- ◆ Single fiber bi-directional data links with symmetric 1.25Gbps upstream and 1.25Gbps downstream
- ◆ 20km transmission application supported
- ◆ Integrated micro-optics WDM filter for dual wavelength Tx/Rx operation at 1310/1490nm
- ◆ Burst mode operation transmitter with 1310nm DFB laser
- ◆ Continuous mode operation receiver with 1490nm high sensitivity PIN-TIA and 1550nm optical signal rejection
- ◆ 0 to 70°C operating ambient temperature
- ◆ 2 × 5 SFF package with single mode fiber pigtailed SC/UPC connector
- ◆ Single +3.3V power supply
- ◆ LVPECL compatible data input/output interface
- ◆ LVTTTL transmitter laser burst mode control
- ◆ LVTTTL receiver signal-detected indication
- ◆ Low EMI and excellent ESD protection
- ◆ Class I laser safety standard IEC-60825 compliant

Standard

- ◆ Compliant with IEEE Std 802.3ah™ -2004 1000BASE-PX20-U
- ◆ Compliant with FCC 47 CFR Part 15, Class B
- ◆ Compliant with FDA 21 CFR 1040.10 and 1040.11, Class I

Description

FTM-9412P-F20 is Optical Network Unit (ONU) for IEEE802.3ah™-2004 1000BASE-PX20 application.

The transceiver is the high performance module for 1.25Gbps data link in single fiber by using 1310nm burst mode transmitter and 1490nm continuous mode receiver with 1550nm optical signal rejection.

The transmitter section uses a multiple quantum well 1310nm DFB laser and is class I laser complaint product according to International Safety Standard IEC-60825, supporting burst-mode operation. The receiver section uses an integrated 1490nm PIN and preamplifier mounted in an optical header and limiting post-amplifier IC.

Regulatory Compliance

The transceivers have been tested according to American and European product safety and electromagnetic compatibility regulations (See Table 1). For further information regarding regulatory certification, please refer to Flexon™ regulatory specification and safety guidelines, or contact with Fiberxon, Inc. America sales office listed at the end of documentation.

Table 1 - Regulatory Compliance

Feature	Standard	Performance
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1(>500 V)
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022 Class B (CISPR 22B) VCCI Class B	Compliant with standards
Immunity	IEC 61000-4-3	Compliant with standards
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN60950, EN (IEC) 60825-1,2	Compliant with Class I Laser Product
Component Recognition	UL and CSA	Compliant with standards

Absolute Maximum Ratings

Absolute Maximum Ratings are those values, beyond which, some damages may occur to the devices. Exposure to conditions above the Absolute Maximum Ratings listed in Table 2 may negatively impact the reliability of the products.

Table 2 - Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit	Note
Storage Ambient Temperature	T _{STG}	-40	85	°C	
Operating Ambient Temperature	T _{OPR}	0	70	°C	1
Power Supply Voltage	V _{CC}	0	4	V	
Input Voltage		GND	V _{CC}	V	
Receiver Damaged Threshold		+7		dBm	
Soldering Temperature			400	°C	2
Bending Radius		30		mm	
Pigtail Fiber Contact Temperature			85	°C	

Note 1: With airflow 1m/sec

Note 2: Only for soldering by iron and 10 seconds on leads only

Recommended Operating Conditions

Table 3 - Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage	V_{CC}	3.13	3.3	3.47	V	$3.3 \pm 5\%$
Operating Ambient Temperature	T_{OPR}	0		70	°C	1
Operating Humidity	H_{OPR}	5		95	%	
Data Rate			1.25		Gbit/s	
Data Rate Drift		-100		+100	PPM	

Note 1: When ambient temperature is above 60°C, airflow at rate higher than 1m/sec is required

Optical and Electrical Characteristics

Table 4 - Transmitter Optical and Electrical Characteristics (0°C < T_{OPR} < 70°C and 3.13V < V_{CC} < 3.47V)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Output Optical Center Wavelength	λ_C	1280		1340	nm	
Output Optical Spectral Width (-20dB)	$\Delta\lambda$			1	nm	
Average Launch Power (BOL)	P_{OUT}	0		+4	dBm	1
Average Launch Power (EOL)		-1		+4		
Average Launch Power-OFF Transmitter	P_{OFF}			-45	dBm	
Extinction Ratio	ER	9			dB	2
Burst Turn On Time	T_{BURST_ON}			30	ns	3
Burst Turn Off Time	T_{BURST_OFF}			30	ns	
Rise/Fall Time (20%-80%)	T_R/T_F			260	ps	2,4
Total Jitter	J_{TOTAL_T}			0.35	UI	
Relative Intensity Noise	RIN_{15OMA}			-115	dB/Hz	
Optical Return Loss Tolerance				15	dB	
Transmitter Reflectance				-10	dB	
Transmitter and Dispersion Penalty	TDP			1.8	dB	5
Transmitter Eye Diagram	Compliant With IEEE Std 802.3ah™-2004					2,6
Data Input Differential Swing	V_{IN}	200		1600	mV	7
Input Differential Impedance	Z_{IN}	90	100	110	Ω	
Power Supply Current	I_{CC_TX}			200	mA	
Transmitter Burst Control Voltage - Low	V_{BURST_L}	0		0.8	V	8
Transmitter Burst Control Voltage - High	V_{BURST_H}	2.0		V_{CC}	V	

Note 1: Launched into 9/125um SMF.

Note 2: Measured with PRBS 2⁷-1 test pattern @1.25Gbps.

Note 3: Refer to [Timing Parameter Definition in Burst Mode Sequence](#).

Note 4: Measured with the Bessel-Thompson filter OFF.

Note 5: Maximum sensitivity penalty due to transmitter and dispersion effect through 20km of SMF optical fiber at 25°C ambient temperature

Note 6: Transmitter eye mask definition is {0.22UI, 0.375UI, 0.20UI, 0.20UI, 0.30UI}.

Note 7: Compatible with LVPECL/CML input, AC coupled internally. (See [Recommended Interface Circuit](#))

Note 8: TX_BRST (See [Pin Function Definitions](#))

Table 5 - Receiver Optical and Electrical Characteristics (0°C < T_{OPR} < 70°C and 3.13V < V_{CC} < 3.47V)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Operating Wavelength		1480		1500	nm	
Sensitivity	P _{SEN}			-26.5	dBm	1
Saturation Power	P _{SAT}	-3			dBm	
Signal Detect Assert Level	P _{SDA}			-28	dBm	2
Signal Detect Deassert Level	P _{SDD}	-39			dBm	3
Signal Detect Hysteresis	P _{SDA} - P _{SDD}	0.5		6	dB	
Power Supply Current	I _{CC_RX}			120	mA	4
Receiver Reflectance				-12	dB	
Data Output Voltage – Low	V _{OL} - V _{CC}	-2.0		-1.58	V	5
Data Output Voltage – High	V _{OH} - V _{CC}	-1.1		-0.74	V	
Signal Detect Voltage – Low	V _{SD,L}	0		0.8	V	6
Signal Detect Voltage – High	V _{SD,H}	2.0		V _{CC}	V	
Signal Detect Assert Time	T _{ASS}			100	μs	
Signal Detect Deassert Time	T _{DAS}			100	μs	
WDM Filter Isolation		38			dB	7
		35			dB	8

Note 1: Measured with a PRBS 2⁷-1 test pattern @1.25Gbps and ER=9dB, BER ≤ 1 × 10⁻¹².

Note 2: An increase in optical power above the specified level will cause the Signal Detect output to switch from a low state to a high state.

Note 3: A decrease in optical power below the specified level will cause the Signal Detect output to switch from a high state to a low state.

Note 4: Supply current including receiver output load.

Note 5: LVPECL output, AC coupled internally. (See [Recommended Interface Circuit](#))

Note 6: SD (See [Pin Function Definitions](#))

Note 7: Tested at 1550nm

Note 8: Tested at 1650nm

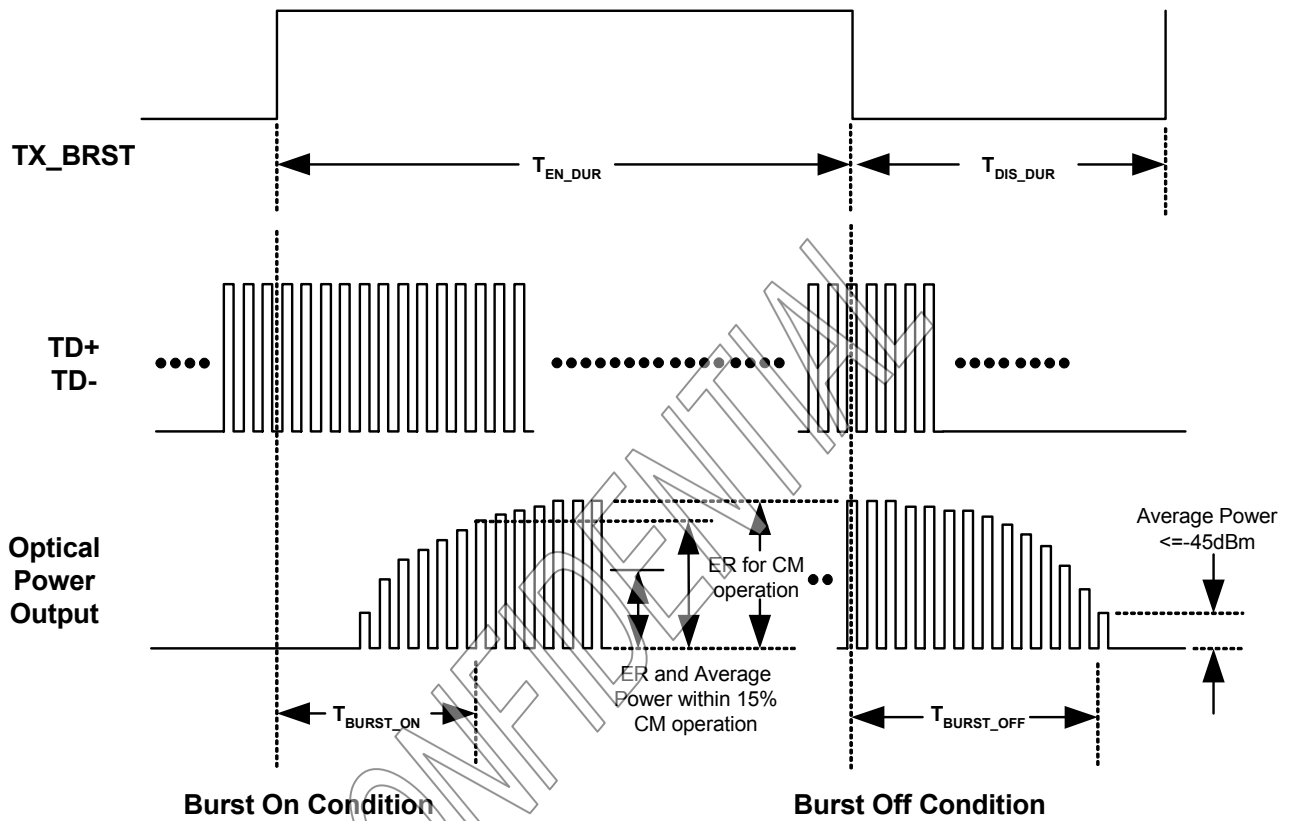


Figure 1 Timing Parameter Definition in Burst Mode Sequence

Recommended Interface Circuit

Figure 2 shows the recommended interface schemes.

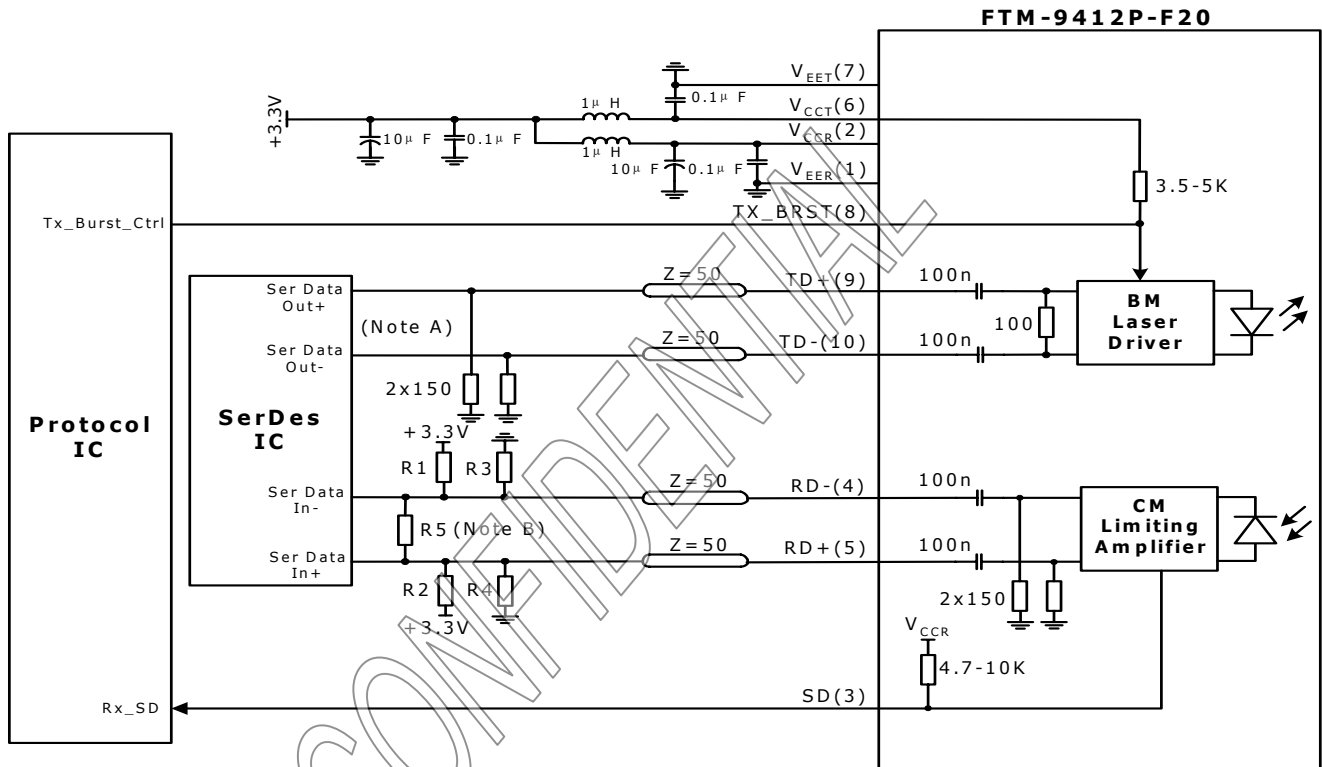


Figure 2 Recommended Interface Circuit

Note A: Open emitter output internally.

Note B: LVPECL output, AC coupled internally.

Input stage in SerDes IC is assumed with high impedance and internal bias to Vcc-1.3V

R1=R2=R3=R4=N.C, R5=100Ω

Input stage in SerDes IC is assumed without internal bias to Vcc-1.3V

R1=R2=82Ω, R3=R4=130Ω, R5=N.C

Pin Definitions

2×5 SFF platform in Figure 3 below shows the pin information of electrical interface and mounting studs. Functions are described in Table 6 with some accompanying notes.

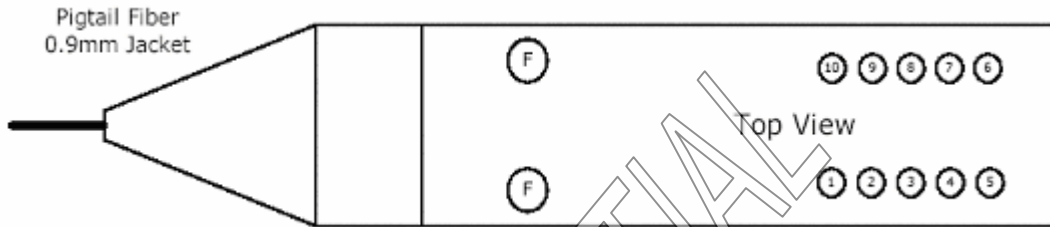


Figure 3 2×5 SFF Platform

Table 6 – Pin Function Definitions

Pin No.	Name	Description	Notes
1	V _{EER}	Receiver Signal Ground	
2	V _{CCR}	Receiver Power Supply	
3	SD	Receiver Signal Detect	1
4	RD-	Inverted Receiver Data Output	2
5	RD+	Non-inverted Receiver Data Output	2
6	V _{CCT}	Transmitter Power Supply	
7	V _{EET}	Transmitter Signal Ground	
8	TX_BRST	Transmitter Burst Output Control	3
9	TD+	Transmitter Non-inverted Data Input	4
10	TD-	Transmitter Inverted Data Input	4
F	MS	Mounting Studs	5

Note 1: TTL logic output, with internal 4.7-10KΩ pull-up resistor.

Optical Signal Detected: High; Optical Signal Loss: Low

Note 2: LVPECL logic output, internally AC-coupled. (See [Recommended Interface Circuit](#))

Note 3: A positive pulse controls the burst mode optical power output with an internal 3.5~5KΩ pull-up resistor. (See [Timing Parameter Definition in Burst Mode Sequence](#))

Note 4: LVPECL logic input, internally terminated and AC-coupled. (See [Recommended Interface Circuit](#))

Note 5: The mounting studs are provided for transceiver mechanical attachment to circuit board. They may also provide an optional connection of the transceiver to the equipment chassis ground. **The holes in the circuit board must be tied to chassis ground. It is not recommended that the mounting studs be connected to signal ground.**

Mechanical Design Diagram

The form factor is 2×5 SFF with pigtail fiber. The pigtail fiber has a length of 613 ± 30 mm and 30mm minimum bending radius. The fiber connector type is SC/UPC. The mechanical design diagram is shown in Figure 4. (Dimension in mm)

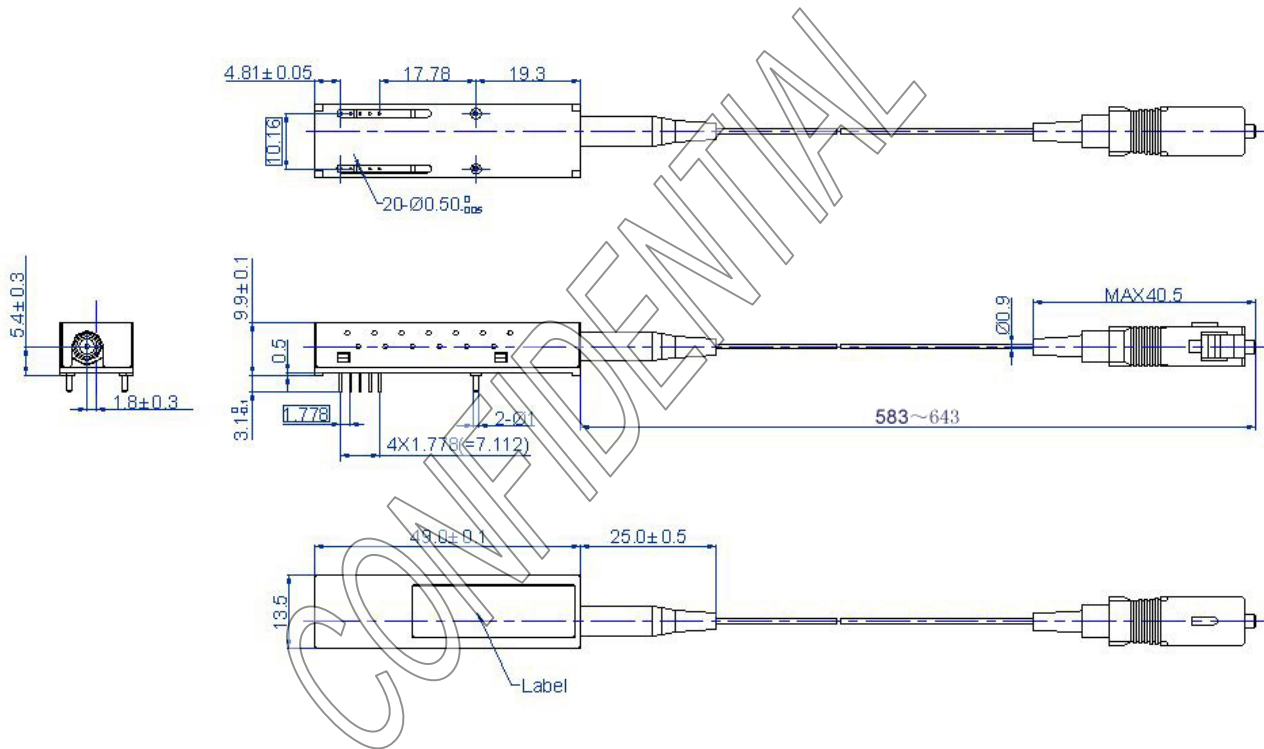
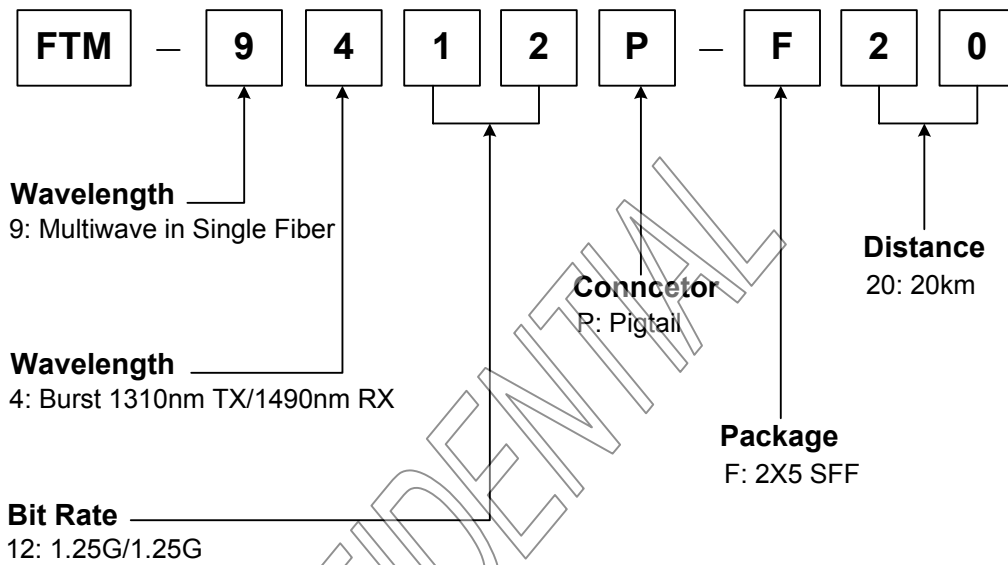


Figure 4 Mechanical Design Diagram

Table 7 – Pigtail Fibre Characteristics

Parameter	Min.	Typical	Max.	Unit
Mode Field Diameter		9		μ m
Gladding Diameter		125		μ m
Jacket Diameter		0.9		mm
Bending Radius of Pigtail Fibre	30			mm
Tension Force on Pigtail Fibre			1	Kg
Pigtail Fibre Length	583	613	643	mm
Optical Return Loss (UPC type)-1310nm	50			dB

Ordering Information



Part No.	Product Description
FTM-9412P-F20	1310nm(DFB)(TX)/1490nm(RX), Pigtail 2 × 5 SFF for GE-PON ONU 20km application, with 1550nm optical signal rejection, 0°C ~+70°C
Note: The pigtail fiber length and connector type definition can be customized	

Related Documents

For further information, please refer to the following documents:

- IEEE Std 802.3ah™-2004

Obtaining Document

You can visit our website:

<http://www.fiberxon.com/>

Or contact with Fiberxon, Inc. America Sales Office listed at the end of documentation to get the latest documents.

Revision History

Version	Subject	Release Date
Preliminary 1a	Initial datasheet	2005-2-23
Preliminary 1b	Page4: Modify sensitivity form -27.5dBm to -26.5dBm	2005-4-12

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