D



600V, N-Channel MOSFET

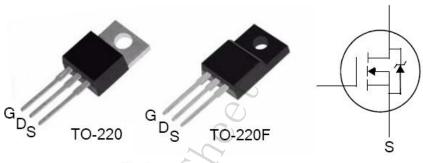
General Features

- Low ON Resistance
- Low Gate Charge
- Peak Current vs. Pulse Width Curve
- RoHS Compliant/Lead Free Package
- Inductive Switching Curves

$\mathrm{BV}_{\mathrm{DSS}}$	R _{DS(ON)} (Max.)	I_{D}
600V	2.2Ω	4.0A

Applications:

- SMPS Power Supply
- Adaptor/Charger
- TV Main Power
- LCD Panel Power



Packages Not to Scale

Ordering Codes

Part Number	Package	Marking
FTP04N60	TO-220	FTP04N60
FTA04N60	TO-220F	FTA04N60

Electrical Ratings

Absolute Maximum Ratings T_C=25 °C unless otherwise specified

Symbol	Parameter	FTP04N60	FTA04N60	Units		
$V_{ m DS}$	Drain-to-Source Voltage (NOTE*	1)	60	600		
I_{D}	Continuous Drain Current		4.0)		
I _D @100℃	Continuous Drain Current		2.	5	A	
I_{DM}	Pulsed Drain Current, V _{GS} @10V (NOTE*2	2)	16.	0		
D	Power Dissipation		86	28	W	
P_{D}	Derating Factor above 25 ℃		0.69	0.22	$W/^{\circ}C$	
V_{GS}	Gate-to-Source Voltage	±3	V			
E _{AS}	Single Pulse Avalanche Energy L=10mH, I _D =4.0A		80)	mJ	
dv/dt	Peak Diode Recovery dv/dt (NOTE*	3)	3.0	V/ns		
$T_{L} \ T_{PKG}$	Maximum Temperature for Soldering Leads 0.063 in (1.6mm) from Case for 10 secon Package Body for 10 seconds	300 260		$^{\circ}\!$		
T _J and T _{STG}	Operating Junction and Storage Temperat Range	ure	-55 to	150		

^{*}Drain Current limited by Maximum Junction Temperature. Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.



Thermal data

Symbol	Parameter	FTP04N60	FTA04N60	Units	Test Conditions
$R_{ heta JC}$	Thermal resistance Junction-to-Case Max	1.45	4.5	°C/W	Water cooled heat sink, P_D adjusted for a peak junction temperature of $+150^{\circ}\text{C}$
$R_{ heta JA}$	Thermal resistance Junction-to-Ambient Max	62	100	C/ W	1 cubic foot chamber, free air.

Electrical Characteristics

OFF Characteristics T_J=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
$\mathrm{BV}_{\mathrm{DSS}}$	Drain-to-Source Breakdown Voltage	600	1		V	V _{GS} =0V, I _D =250μA
$\triangle BV_{DSS}/\triangle T_{J}$	Breakdown Voltage Temperature Coefficient		0.6	0	V/℃	Reference to 25° C, $I_D=250\mu A$
			-(7)	12		V_{DS} =600V, V_{GS} =0V
I_{DSS}	Drain-to-Source Leakage Current		72	250	μΑ	V_{DS} =480V, V_{GS} =0V, T_{J} =125 °C
I_{GSS}	Gate-to-Source Forward Leakage	(100	nA	$V_{GS}=+30V$
			-	-100		V_{GS} =-30V

ON Characteristics T_J =25 °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
R _{DS(ON)}	Static Drain-to-Source On-Resistance	- -	1.76	2.2	Ω	V _{GS} =10V, I _D =2.4A (NOTE*4)
$V_{GS(TH)}$	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
gfs	Forward Transconductance		3.75		S	$V_{DS} = 15V$, $I_D = 4A$ (NOTE*4)

Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
C_{ISS}	Input Capacitance					V_{GS} = $0V$
Coss	Output Capacitance				pF	$V_{\rm DS}=25V$
C_{ISS}	Reverse Transfer Capacitance					$f=1.0MH_Z$
Q_{G}	Total Gate Charge					V _{DD} =300V
Q_{GS}	Gate-to-Source Charge				nC	V_{DD} =300V I_{D} =4A
Q_{GD}	Gate-to-Drain (Miller) Charge					



Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
$t_{d(ON)}$	Turn-on Delay Time					$V_{DD}=300V$
t _{rise}	Rise Time					$I_D=4A$
$t_{d(OFF)}$	Turn-off Delay Time				ns	$V_{GS}=10V$ $R_{G}=9.1 \Omega$
t_{fall}	Fall Time					

Source-Drian Diode Characteristics T_J=25 [°]C unless otherwise specified

Symbol	Parameter	Mi n.	Тур.	Max.	Units	Test Conditions
I_{SD}	Continuous Source Current(Body Diode)	1		4.0	A	Integral pn-diode
I_{SM}	Maximum Pulsed Current(Body Diode)	1		16.0	A	in MOSFET
V_{SD}	Diode Forward Voltage	1		1.2	V	I_{S} =4.0A, V_{GS} =0V
t_{rr}	Reverse Recovery Time	-	0) -	ns	$V_{GS}=0V$
Q _{rr}	Reverse Recovery Charge	-		1	nC	$I_F=4A,di/dt=100A/us$

NOTE:

^{*1.} T_J=+25°C to +150°C

^{*2.} Repetitive rating; pulse width limited by maximum junction temperature.

^{*3.} I_{SD} =4A di/dt \leq 100A/ μ s, $V_{DD}\leq$ B V_{DSS} , T_{J} =+150°C.

^{*4.} Pulse width \(380\mu s; \) duty cycle \(2\% \).



Disclaimers:

ARK Microelectronics Co., Ltd. reserves the right to make change without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to ARK Microelectronics Co., Ltd's terms and conditions supplied at the time of order acknowledgement.

ARK Microelectronics Co., Ltd. warrants performance of its hardware products to the specifications at the time of sale, Testing, reliability and quality control are used to the extent ARK Microelectronics Co., Ltd deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessary performed.

ARK Microelectronics Co., Ltd. does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using ARK Microelectronics Co., Ltd's components. To minimize risk, customers must provide adequate design and operating safeguards.

ARK Microelectronics Co., Ltd. does not warrant or convey any license either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in ARK Microelectronics Co., Ltd's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. ARK Microelectronics Co., Ltd is not responsible or liable for such altered documentation.

Resale of ARK Microelectronics Co., Ltd's products with statements different from or beyond the parameters stated by ARK Microelectronics Co., Ltd. for the product or service voids all express or implied warrantees for the associated ARK Microelectronics Co., Ltd's product or service and is unfair and deceptive business practice. ARK Microelectronics Co., Ltd is not responsible or liable for any such statements.

Life Support Policy:

ARK Microelectronics Co., Ltd's products are not authorized for use as critical components in life devices or systems without the expressed written approval of ARK Microelectronics Co., Ltd.

As used herein:

- 1. Life support devices or systems are devices or systems which:
 - a. are intended for surgical implant into the human body,
 - b. support or sustain life,
 - c. whose failure to perform when properly used in accordance with instructions for used provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.