

PRELIMINARY
Notice: This is not a final specification.
Some parametric limits are subject to change.

MITSUBISHI Nch POWER MOSFET

FY8BCH-02F

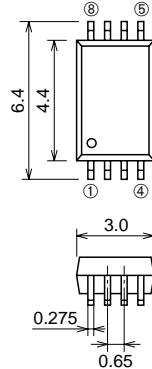
HIGH-SPEED SWITCHING USE

FY8BCH-02F

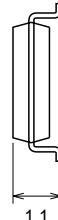


- 2.5V DRIVE
- V_{DSS} 20V
- r_{Ds} (ON) (MAX) 16mΩ
- I_D 8A

OUTLINE DRAWING



Dimensions in mm



②③⑥⑦ SOURCE
④⑤ GATE
①⑧ DRAIN

TSSOP8

APPLICATION

Li - ion battery protection

MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$)

Symbol	Parameter	Conditions	Ratings	Unit
V _{DSS}	Drain-source voltage	V _{GS} = 0V	20	V
V _{GSS}	Gate-source voltage	V _{DS} = 0V	±10	V
I _D	Drain current		8	A
I _{DM}	Drain current (Pulsed)		56	A
I _{DA}	Avalanche current (Pulsed)	L = 10μH	8	A
I _S	Source current		1.5	A
I _{SM}	Source current (Pulsed)		6.0	A
P _D	Maximum power dissipation		1.6	W
T _{ch}	Channel temperature		-55 ~ +150	°C
T _{stg}	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	0.035	g

Sep. 2000

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FY8BCH-02F**HIGH-SPEED SWITCHING USE****ELECTRICAL CHARACTERISTICS** ($T_{ch} = 25^{\circ}\text{C}$)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	$Id = 1\text{mA}$, $V_{GS} = 0\text{V}$	20	—	—	V
V (BR) GSS	Gate-source breakdown voltage	$IG = \pm 100\mu\text{A}$, $V_{DS} = 0\text{V}$	± 10	—	—	V
I_{GSS}	Gate-source leakage current	$V_{GS} = \pm 10\text{V}$, $V_{DS} = 0\text{V}$	—	—	± 10	μA
I_{DSS}	Drain-source leakage current	$V_{DS} = 20\text{V}$, $V_{GS} = 0\text{V}$	—	—	0.1	mA
$V_{GS(\text{th})}$	Gate-source threshold voltage	$Id = 1\text{mA}$, $V_{DS} = 10\text{V}$	0.5	0.9	1.5	V
$r_{DS(\text{ON})}$	Drain-source on-state resistance	$Id = 8\text{A}$, $V_{GS} = 4\text{V}$	—	13	16	$\text{m}\Omega$
$r_{DS(\text{ON})}$	Drain-source on-state resistance	$Id = 4\text{A}$, $V_{GS} = 2.5\text{V}$	—	17	22	$\text{m}\Omega$
$V_{DS(\text{ON})}$	Drain-source on-state voltage	$Id = 8\text{A}$, $V_{GS} = 4\text{V}$	—	0.104	0.128	V
$ y_{fs} $	Forward transfer admittance	$Id = 8\text{A}$, $V_{DS} = 10\text{V}$	—	—	—	S
C_{iss}	Input capacitance	$V_{DS} = 10\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$	—	1800	—	pF
C_{oss}	Output capacitance		—	—	—	pF
C_{rss}	Reverse transfer capacitance		—	—	—	pF
$t_{d(\text{on})}$	Turn-on delay time	$V_{DD} = 10\text{V}$, $Id = 4\text{A}$, $V_{GS} = 4\text{V}$, $R_{GEN} = R_{GS} = 50\Omega$	—	—	—	ns
t_r	Rise time		—	—	—	ns
$t_{d(\text{off})}$	Turn-off delay time		—	—	—	ns
t_f	Fall time		—	—	—	ns
V_{SD}	Source-drain voltage	$I_S = 1.5\text{A}$, $V_{GS} = 0\text{V}$	—	0.85	1.1	V
$R_{th(\text{ch-a})}$	Thermal resistance	Channel to ambient	—	—	78.1	$^{\circ}\text{C/W}$
t_{rr}	Reverse recovery time	$I_S = 1.5\text{A}$, $dI/dt = -50\text{A}/\mu\text{s}$	—	50	—	ns