200V PNP MEDIUM POWER TRANSISTOR IN SOT223

Features

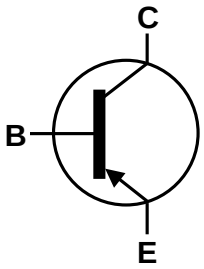
- BV_{CEO} > -200V
- I_C = -2A High Continuous Collector Current
- I_C = -5A Peak Pulse Current
- Low Saturation Voltage $V_{CE(SAT)}$ < -165mV @ -1A
- h_{FE} Specified up to -5A for a High Gain Hold-Up
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Mechanical Data

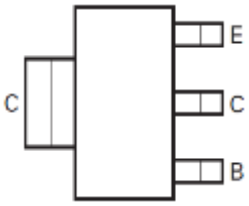
- Case: SOT223
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.112 grams (Approximate)

SOT223

Top View



Device Symbol



Top View
Pin-Out

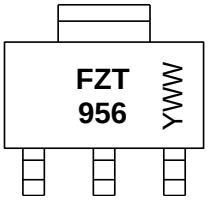
Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
FZT956TA	AEC-Q101	FZT956	7	12	1,000
FZT956QTA	Automotive	FZT956	7	12	1,000

Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/.
5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

SOT223



FZT 956 = Product Type Marking Code
YWW = Date Code Marking
Y or $\overline{Y}$ = Last Digit of Year (ex: 7 = 2017)
WW or $\overline{W}W$ = Week Code (01–53)

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-220	V
Collector-Emitter Voltage	V_{CEO}	-200	V
Emitter-Base Voltage	V_{EBO}	-7	V
Continuous Collector Current	I_C	-2	A
Peak Pulse Current	I_{CM}	-5	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

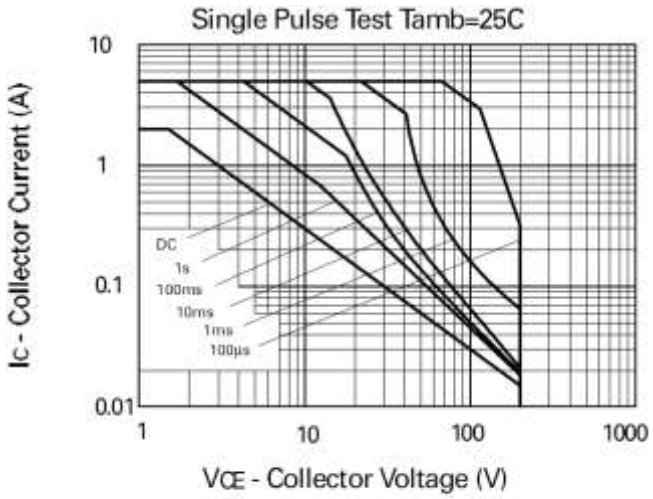
Characteristic		Symbol	Value	Unit
Power Dissipation Linear Derating Factor	(Note 6)	P_D	3.0 24	W mW /°C
	(Note 7)		1.6 12.8	
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	42	°C/W
	(Note 7)	$R_{\theta JA}$	78	
Thermal Resistance Junction to Lead	(Note 8)	$R_{\theta JL}$	8.8	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

ESD Ratings (Note 9)

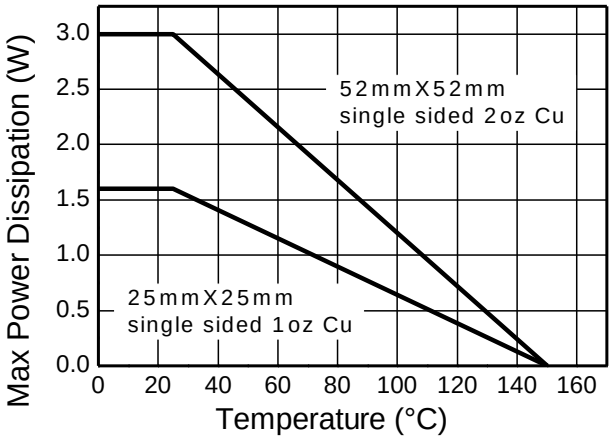
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

Notes:
6. For a device mounted with the collector lead on 52mm x 52mm 2oz copper that is on a single sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
7. Same as Note 6, except mounted on 25mm x 25mm 1oz copper.
8. Thermal resistance from junction to solder-point (at the end of the collector lead).
9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

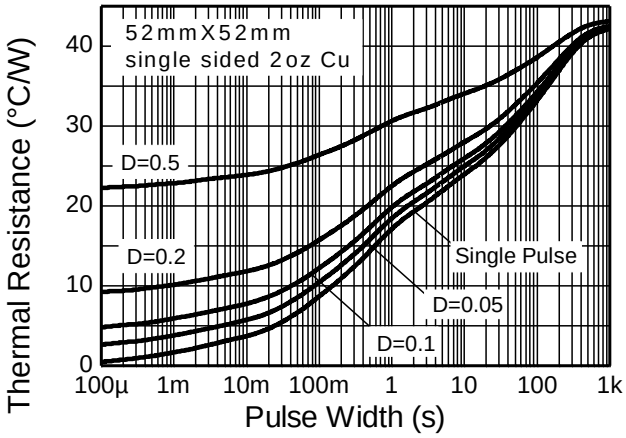
Thermal Characteristics and Derating Information



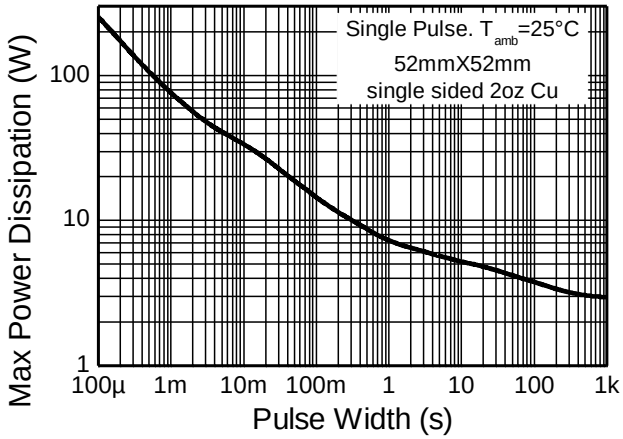
Safe Operating Area



Derating Curve



Transient Thermal Impedance



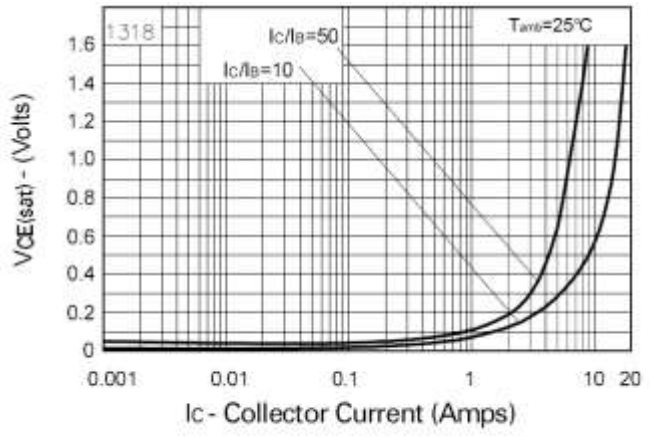
Pulse Power Dissipation

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

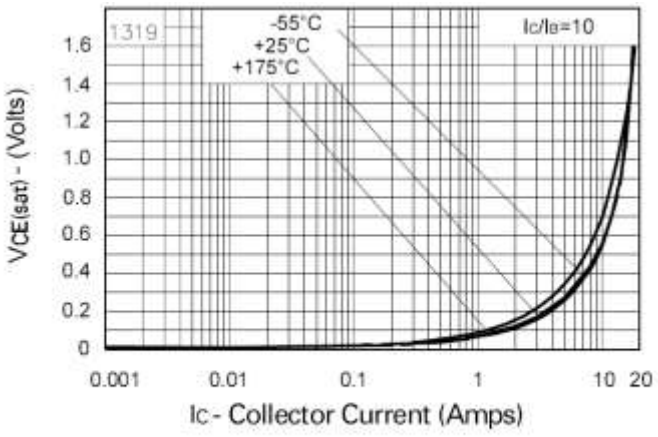
Characteristic	Symbol	Min	Typ.	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV_{CBO}	-220	-300	—	V	I_C = -100µA
Collector-Emitter Breakdown Voltage (Note 10)	BV_{CER}	-220	-300	—	V	I_C = -1µA, R_B ≤ 1kΩ
Collector-Emitter Breakdown Voltage (Note 10)	BV_{CEO}	-200	-240	—	V	I_C = -1mA
Emitter-Base Breakdown Voltage	BV_{EBO}	-7	-8.3	—	V	I_E = -100µA
Collector Cut-Off Current	I_{CBO}	—	—	-50 -1	nA µA	V_{CB} = -200V V_{CB} = -200V, T_A = +100°C
Collector Cut-Off Current	I_{CER}	—	—	-50 -1	nA µA	V_{CE} = -200V, R ≤ 1kΩ V_{CE} = -200V, T_A = +100°C
Emitter Cut-Off Current	I_{EBO}	—	—	-10	nA	V_{EB} = -6V
DC Current Transfer Static Ratio (Note 10)	h_{FE}	100	200	—	—	I_C = -10mA, V_{CE} = -5V
		100	200	300		I_C = -1A, V_{CE} = -5V
		50	150	—		I_C = -2A, V_{CE} = -5V
		—	10	—		I_C = -5A, V_{CE} = -5V
Collector-Emitter Saturation Voltage (Note 10)	$V_{CE(SAT)}$	—	-30	-50	mV	I_C = -100mA, I_B = -10mA
		—	-120	-165		I_C = -1A, I_B = -100mA
		—	-168	-275		I_C = -2A, I_B = -400mA
Base-Emitter Saturation Voltage (Note 10)	$V_{BE(SAT)}$	—	-970	-1,110	mV	I_C = -2A, I_B = -400mA
Base-Emitter Turn-On Voltage (Note 10)	$V_{BE(ON)}$	—	-810	-950	mV	I_C = -2A, V_{CE} = -5V
Transitional Frequency (Note 10)	f_T	—	110	—	MHz	I_C = -100mA, V_{CE} = -10V, f = 50MHz
Output Capacitance	C_{OBO}	—	32	—	pF	V_{CB} = -20V, f = 1MHz
Switching Time	t_{ON}	—	67	—	ns	V_{CC} = -50V, I_C = -1A, $-I_{B1}$ = I_{B2} = -100mA
	t_{OFF}	—	1,140	—		

Note: 10. Measured under pulsed conditions. Pulse width ≤ 300µs. Duty cycle ≤ 2%.

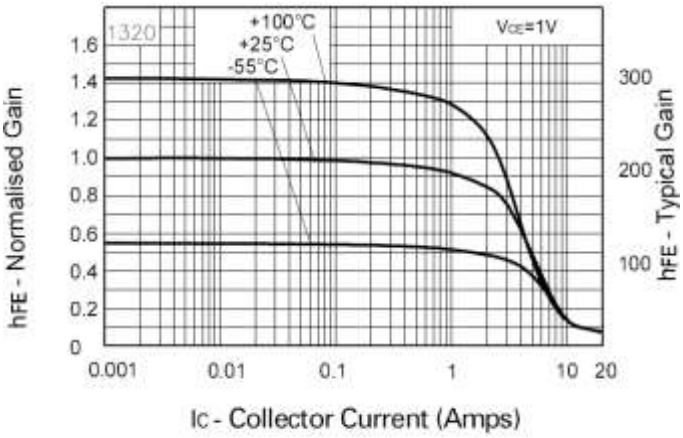
Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)



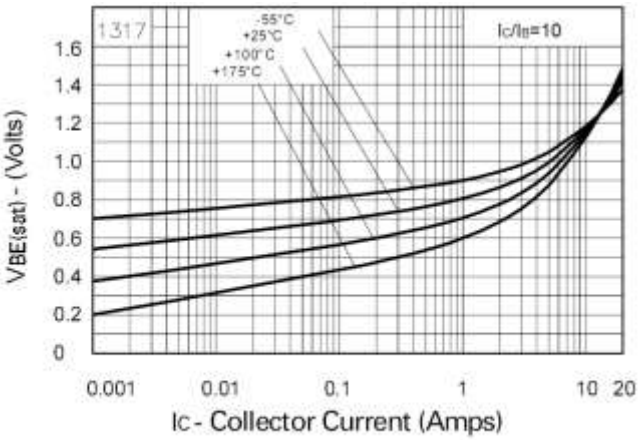
VCE(sat) v IC



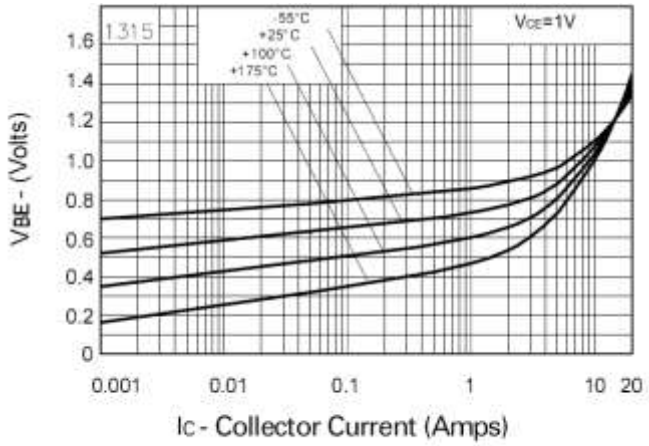
VCE(sat) v IC



hFE v IC



VBE(sat) v IC

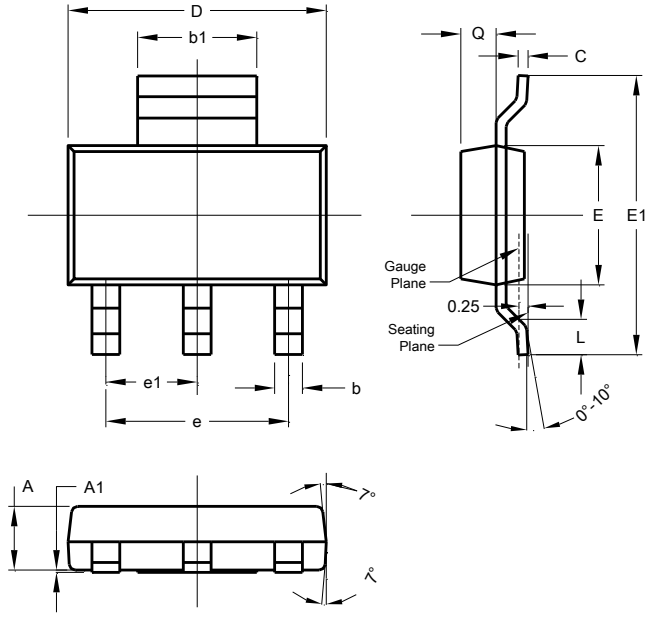


VBE(on) v IC

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223

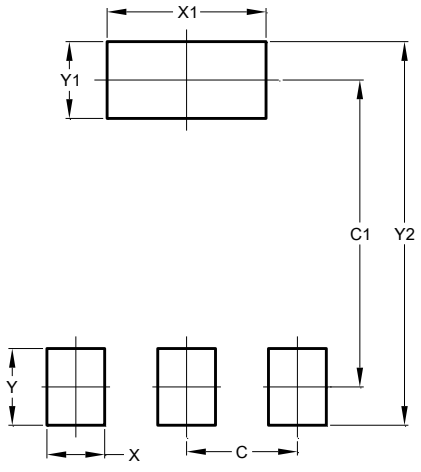


SOT223			
Dim	**Min**	**Max**	**Typ**
A	1.55	1.65	1.60
A1	0.010	0.15	0.05
b	0.60	0.80	0.70
b1	2.90	3.10	3.00
C	0.20	0.30	0.25
D	6.45	6.55	6.50
E	3.45	3.55	3.50
E1	6.90	7.10	7.00
e	-	-	4.60
e1	-	-	2.30
L	0.85	1.05	0.95
Q	0.84	0.94	0.89
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223



Dimensions	Value (in mm)
C	2.30
C1	6.40
X	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.

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