Pb Free Plating Product

ISSUED DATE :2005/07/05
REVISED DATE :

G2U9972

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

BV_{DSS}	60V
$R_{DS(ON)}$	18mΩ
I_D	60A

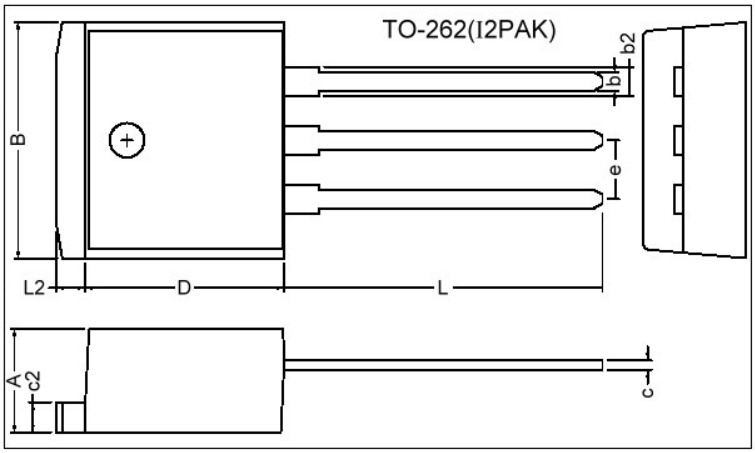
Description

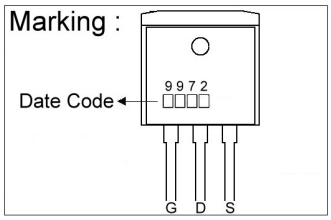
The G2U9972 provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.
The TO-262 package is universally preferred for all commercial-industrial applications and suited for low voltage applications such as DC/DC converters.

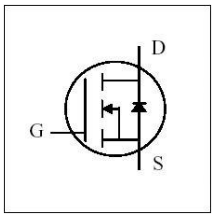
Features

*Simple Drive Requirement
*Lower Gate Charge

Package Dimensions







REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.40	4.80	c2	1.25	1.45
b	0.76	1.00	b2	1.17	1.47
c	0.36	0.50	L	13.25	14.25
D	8.60	9.00	e	2.54 REF.	
E	9.80	10.4	L2	1.27 REF.	

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±25	V
Continuous Drain Current, V_{GS}@10V	I_D @Tc=25℃	60	A
Continuous Drain Current, V_{GS}@10V	I_D @Tc=100℃	38	A
Pulsed Drain Current[1]	I_{DM}	230	A
Total Power Dissipation	P_D @Tc=25℃	89	W
Linear Derating Factor		0.7	W/℃
Avalanche Current[2]	I_{AR}	30	A
Operating Junction and Storage Temperature Range	Tj, Tstg	-55 ~ +150	℃

Thermal Data

Parameter		Symbol	Value	Unit
Thermal Resistance Junction-case	Max.	Rthj-c	1.4	℃/W
Thermal Resistance Junction-ambient	Max.	Rthj-a	62	℃/W

Electrical Characteristics(Tj = 25℃ Unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	60	-	-	V	V_{GS}=0, I_D=250uA
Breakdown Voltage Temperature Coefficient	$\triangle BV_{DSS}/\triangle Tj$	-	0.06	-	V/℃	Reference to 25℃, I_D=1mA
Gate Threshold Voltage	$V_{GS(th)}$	1.0	-	3.0	V	V_{DS}=V_{GS}, I_D=250uA
Forward Transconductance	g_{fs}	-	55	-	S	V_{DS}=10V, I_D=35A
Gate-Source Leakage Current	I_{GSS}	-	-	±100	nA	V_{GS}= ±25V
Drain-Source Leakage Current(Tj=25℃)	I_{DSS}	-	-	10	uA	V_{DS}=60V, V_{GS}=0
Drain-Source Leakage Current(Tj=150℃)		-	-	25	uA	V_{DS}=48V, V_{GS}=0
Static Drain-Source On-Resistance[3]	$R_{DS(ON)}$	-	-	18	mΩ	V_{GS}=10V, I_D=35A
		-	-	22		V_{GS}=4.5V, I_D=25A
Total Gate Charge[3]	Q_g	-	32	51	nC	I_D=35A V_{DS}=48V V_{GS}=4.5V
Gate-Source Charge	Q_{gs}	-	8	-		
Gate-Drain ("Miller") Change	Q_{gd}	-	20	-		
Turn-on Delay Time[3]	$T_{d(on)}$	-	11	-	ns	V_{DS}=30V I_D=35A V_{GS}=10V R_G=3.3Ω R_D=0.86Ω
Rise Time	T_r	-	58	-		
Turn-off Delay Time	$T_{d(off)}$	-	45	-		
Fall Time	T_f	-	80	-		
Input Capacitance	C_{iss}	-	3170	5070	pF S nA	V_{GS}=0V V_{DS}=25V f=1.0MHz
Output Capacitance	C_{oss}	-	280	-		
Reverse Transfer Capacitance	C_{rss}	-	230	-		
Gate Resistance	R_g	-	1.7	-	Ω	f=1.0MHz

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage[3]	V_{SD}	-	-	1.2	V	I_S=35A, V_{GS}=0V
Reverse Recovery Time	T_{rr}	-	50	-	ns	I_S=35A, V_{GS}=0V dI/dt=100A/μs
Reverse Recovery Charge	Q_{rr}	-	48	-	nC	

Notes: 1. Pulse width limited by Max. junction temperature.

2. Staring Tj=25℃, V_{DD}=30V, L=1mH, R_G=25Ω, I_{AS}=30A.

3. Pulse width≦300us, duty cycle≦2%.

Characteristics Curve

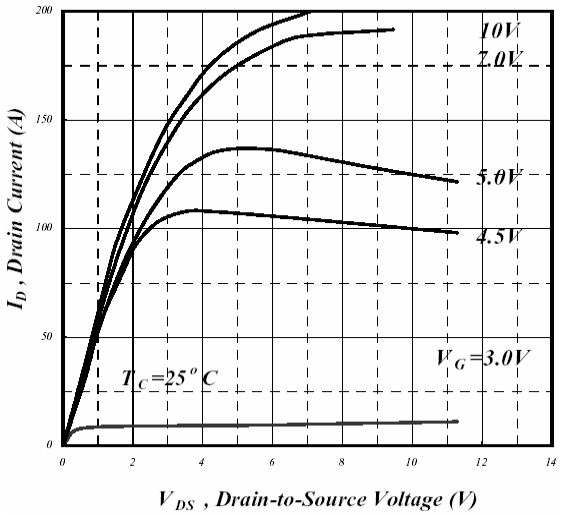


Fig 1. Typical Output Characteristics

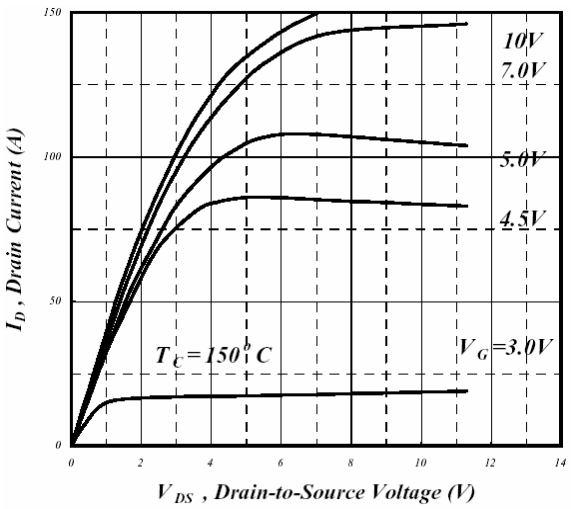


Fig 2. Typical Output Characteristics

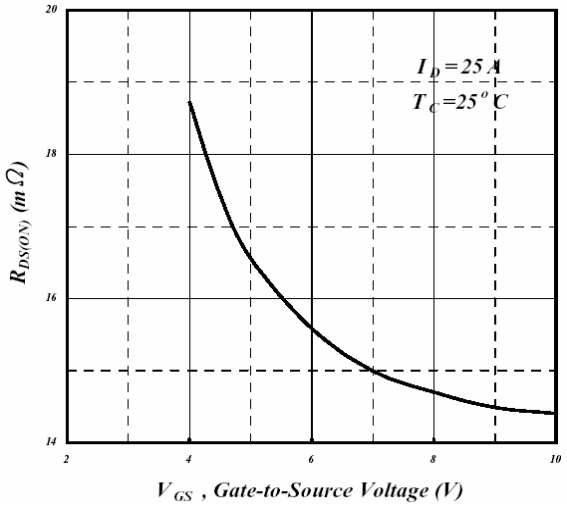


Fig 3. **On-Resistance v.s. Gate Voltage**

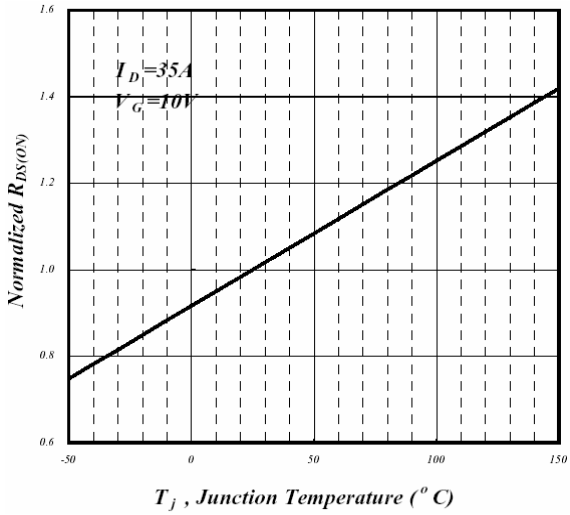


Fig 4. Normalized On-Resistance v.s. Junction Temperature

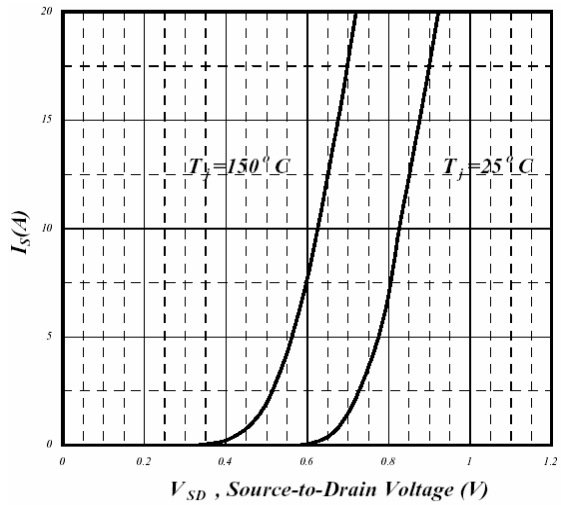


Fig 5. Forward Characteristics of Reverse Diode

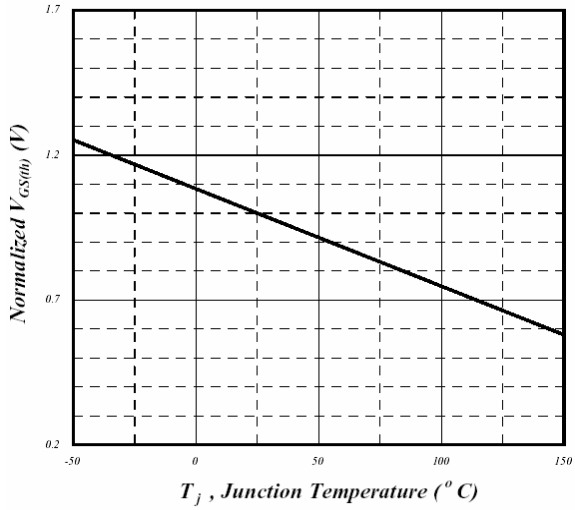


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

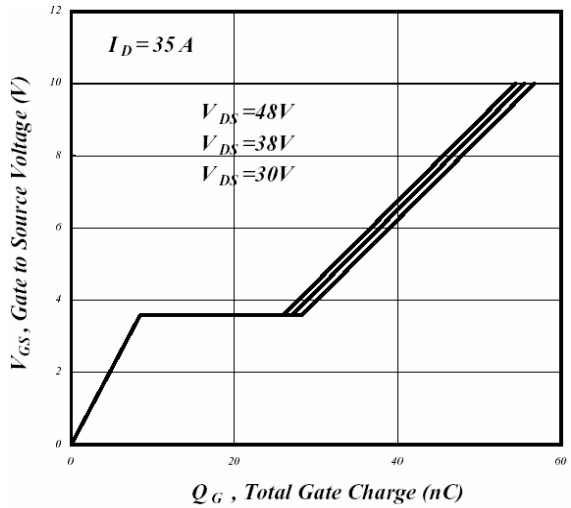


Fig 7. Gate Charge Characteristics

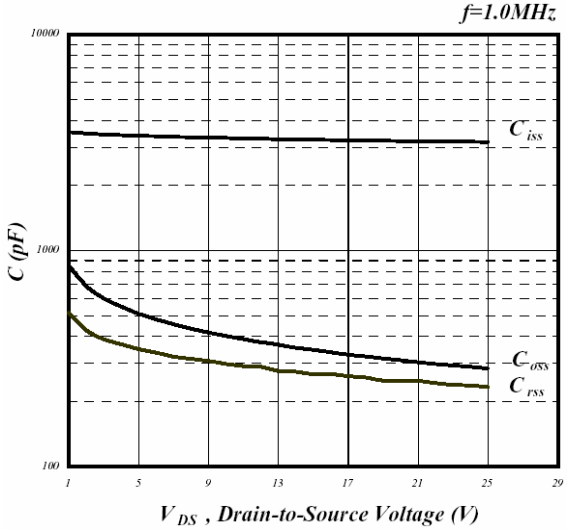


Fig 8. Typical Capacitance Characteristics

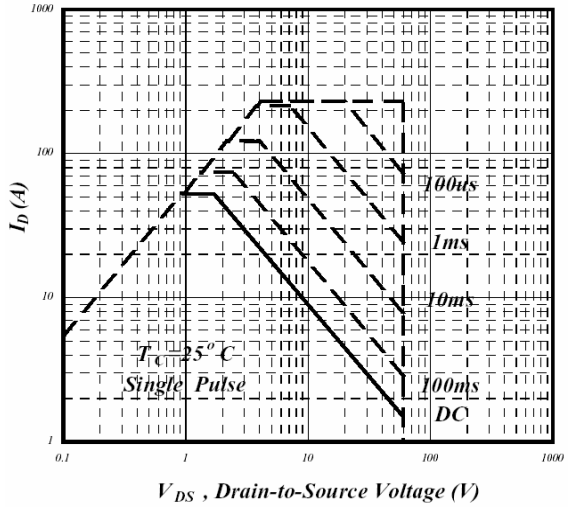


Fig 9. Maximum Safe Operating Area

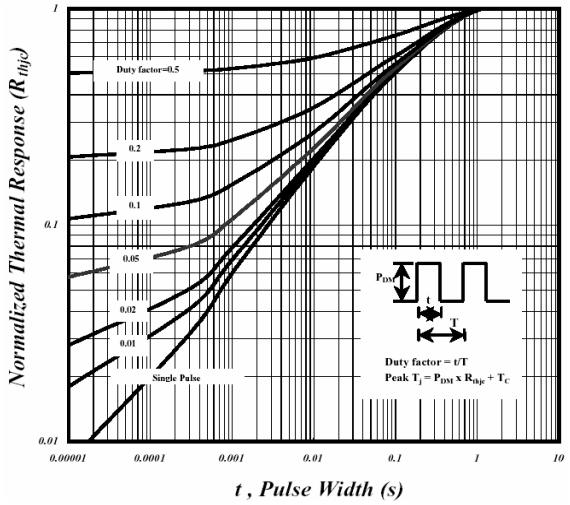


Fig 10. Effective Transient Thermal Impedance

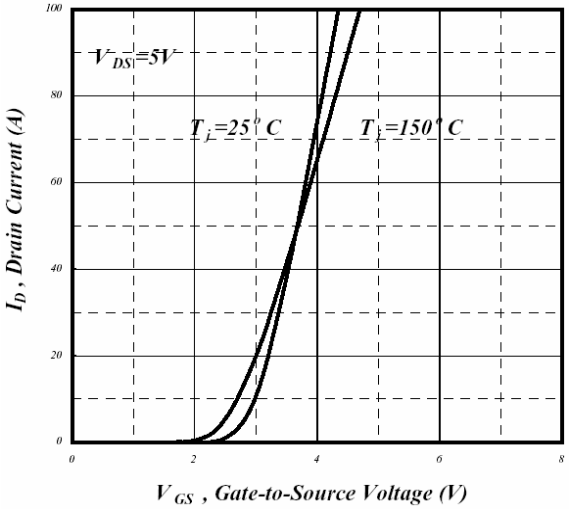


Fig 11. Transfer Characteristics

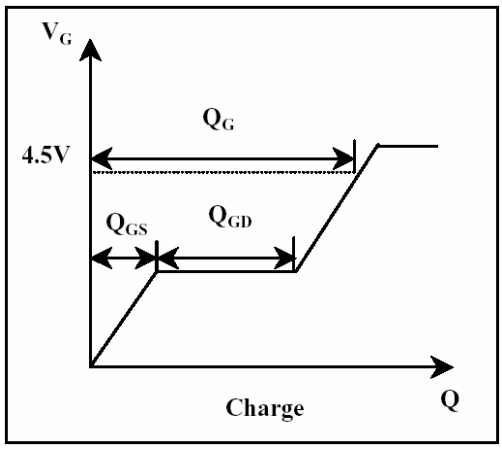


Fig 12. Gate Charge Waveform

Head Office And Factory:
- **Taiwan**: No. 17-1 Tatung Rd. Fu Kou Hsin-Chu Industrial Park, Hsin-Chu, Taiwan, R. O. C.
  TEL : 886-3-597-7061 FAX : 886-3-597-9220, 597-0785
- **China**: (201203) No.255, Jang-Jiang Tsai-Lueng RD. , Pu-Dung-Hsin District, Shang-Hai City, China
  TEL : 86-21-5895-7671 ~ 4 FAX : 86-21-38950165