

Bidirectional I²C Bus and SMBus Voltage Level Translator

Features

- 1-Bit Bidirectional Translator
- I²C and SMBus Compatible
- Less than 1.5ns Maximum Propagation Delay to Accommodate Standard-Mode and Fast-Model I²C Devices and Multiple Masters
- Allows Voltage-Level Translator Between
 - ◆ 1.2V V_{REF1} and 2.5V, 3.3V, 5V V_{REF2}
 - ◆ 1.8V V_{REF1} and 3.3V, 5V V_{REF2}
 - ◆ 3.3V V_{REF1} and 5V V_{REF2}
- Provides Bidirectional Voltage Translation without Direction Pin
- Low 3.5Ω ON-State Connection Between Input and Output Ports Provides Less Signal Distortion
- Open-Drain I²C I/O Ports
- 5V Tolerant I²C I/O Ports to Support Mixed Mode Signal Operation
- High Impedance for SCL1 and SCL2 as EN=Low
- Lock-up-Free Operation for Isolation When EN=Low

General Description

The G3403 is a bidirectional I²C and SMBUS voltage-level translator with an enable (EN) input, and is operational from 1.2V to 3.3V V_{REF1} and 2.5V to 5.5V V_{REF2}. It allows bidirectional voltage translations between 1.2V and 5V, without use of directional pin. The low ON-state resistance (r_{ON}) of the switch ensures the connections to be with minimal propagation delay. When EN is high, the translator switch is ON, and the SCL1 I/O is connected to the SCL2 I/O, respectively, allowing bidirectional data flow between ports. When

EN is low, the translator switch is off, and a high-impedance exists between ports.

In I²C applications, the bus capacitance limit of 400pF restricts the number of devices and bus length. The system designer could isolate two halves of a bus by using the G3403; thus, more I²C devices or longer trace length can be accommodated.

In standard I²C system, pull-up resistors are required to provide the logic high levels on the translator's bus. The size of these pull-up resistors depends on the system, but each side of the repeater must have a pull-up resistor. The G3403 is designed to work with standard-mode and fast-mode I²C devices. Standard mode I²C devices only specify 3mA in a generic I²C system where standard mode devices and multiple masters are possible. Under certain conditions, high termination currents can be used.

When the SCL1 or SCL2 port is low, the clamp is in the ON state, and a low resistance connection exists between the SCL1 and SCL2 ports. Assuming the higher voltage is on the SCL2 port, when the SCL2 port is high, the voltage on the SCL1 port is limited to the voltage set by V_{REF1}. When the SCL1 is high, the SCL2 port is pulled up to the drain pull-up supply voltage (V_{DPU}) by pull-up resistors. This function allows a seamless translation between higher and lower voltages selected by the user, without any directional control.

Ordering Information

ORDER NUMBER	MARKING	TEMP. RANGE	PACKAGE (Green)
G3403TL1U	343x	-40°C to +85°C	SC-70-6 (SOT-363)

Note: TL: SC-70-6 (SOT-363)

1: Bonding Code

U: Tape & reel

Pin Configuration

