

**G3403A****P-CHANNEL ENHANCEMENT MODE POWER MOSFET**

BVDSS	-30V
RDS(ON)	70mΩ
ID	-3.2A

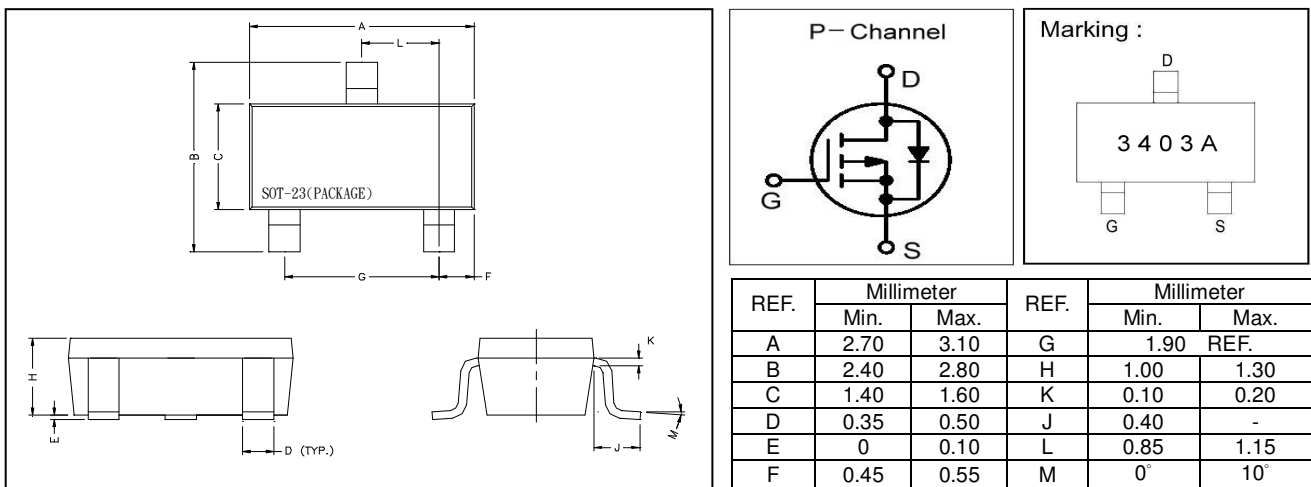
**Description**

The G3403A provide the designer with best combination of fast switching, low on-resistance and cost-effectiveness.

The G3403A is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

**Features**

- \*Simple Drive Requirement
- \*Small Package Outline

**Package Dimensions****Absolute Maximum Ratings**

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current <sup>3</sup>	$I_D @ TA=25^\circ C$	-3.2	A
Continuous Drain Current <sup>3</sup>	$I_D @ TA=70^\circ C$	-2.6	A
Pulsed Drain Current <sup>1,2</sup>	$I_{DM}$	-10	A
Power Dissipation	$P_D @ TA=25^\circ C$	1.38	W
Linear Derating Factor		0.01	W/°C
Operating Junction and Storage Temperature Range	$T_j, T_{stg}$	-55 ~ +150	°C

**Thermal Data**

Parameter	Symbol	Ratings	Unit
Thermal Resistance Junction-ambient <sup>3</sup> Max.	$R_{thj-a}$	90	°C/W

**Electrical Characteristics(T<sub>j</sub> = 25°C Unless otherwise specified)**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =-250uA
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS} / \Delta T_j$	-	-0.1	-	V/°C	Reference to 25°C, I <sub>D</sub> =-1mA
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.5	-	-1.2	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA
Forward Transconductance	g <sub>fs</sub>	-	9	-	S	V <sub>DS</sub> =-5V, I <sub>D</sub> =-3A
Gate-Source Leakage Current	I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> = ±12V
Drain-Source Leakage Current(T <sub>j</sub> =25°C)	I <sub>DSS</sub>	-	-	-1	uA	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0
Drain-Source Leakage Current(T <sub>j</sub> =70°C)		-	-	-25	uA	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	-	70	mΩ	V <sub>GS</sub> =-10V, I <sub>D</sub> =-2.6A
		-	-	85		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.0A
		-	-	120		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1.0A
Total Gate Charge <sup>2</sup>	Q <sub>g</sub>	-	10	18	nC	I <sub>D</sub> =-3.2A V <sub>DS</sub> =-24V V <sub>GS</sub> =-4.5V
Gate-Source Charge	Q <sub>gs</sub>	-	1.8	-		
Gate-Drain ("Miller") Change	Q <sub>gd</sub>	-	3.6	-		
Turn-on Delay Time <sup>2</sup>	T <sub>d(on)</sub>	-	7	-	ns	V <sub>DS</sub> =-15V I <sub>D</sub> =-3.2A V <sub>GS</sub> =-10V R <sub>G</sub> =3.3Ω R <sub>D</sub> =4.6Ω
Rise Time	T <sub>r</sub>	-	15	-		
Turn-off Delay Time	T <sub>d(off)</sub>	-	21	-		
Fall Time	T <sub>f</sub>	-	15	-		
Input Capacitance	C <sub>iss</sub>	-	735	1325	pF	V <sub>GS</sub> =0V V <sub>DS</sub> =-25V f=1.0MHz
Output Capacitance	C <sub>oss</sub>	-	100	-		
Reverse Transfer Capacitance	C <sub>rss</sub>	-	80	-		

**Source-Drain Diode**

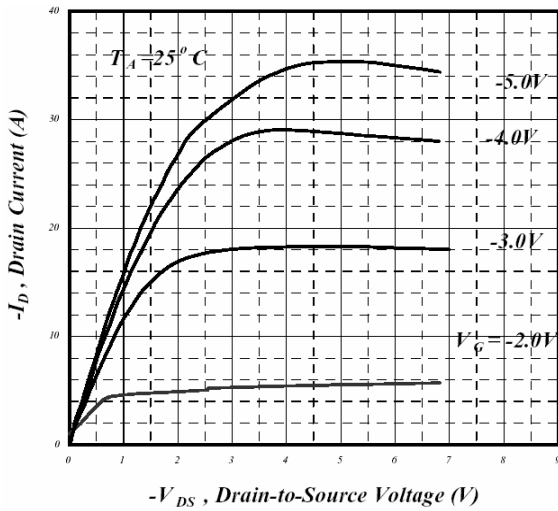
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage <sup>2</sup>	V <sub>SD</sub>	-	-	-1.2	V	I <sub>S</sub> =-1.2A, V <sub>GS</sub> =0V
Reverse Recovery Time <sup>2</sup>	T <sub>rr</sub>	-	24	-	ns	I <sub>S</sub> =-3.2A, V <sub>GS</sub> =0V di/dt=100A/μs
Reverse Recovery Charge	Q <sub>rr</sub>	-	19	-	nC	

Notes: 1. Pulse width limited by Max. junction temperature.

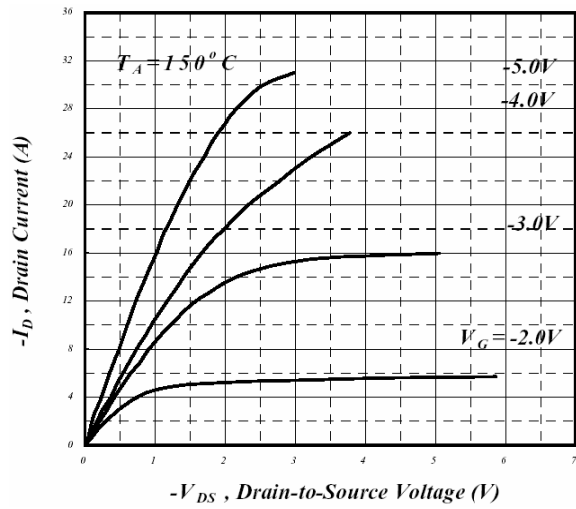
2. Pulse width ≤ 300us, duty cycle ≤ 2%.

3. Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board; 270°C/W when mounted on min. copper pad.

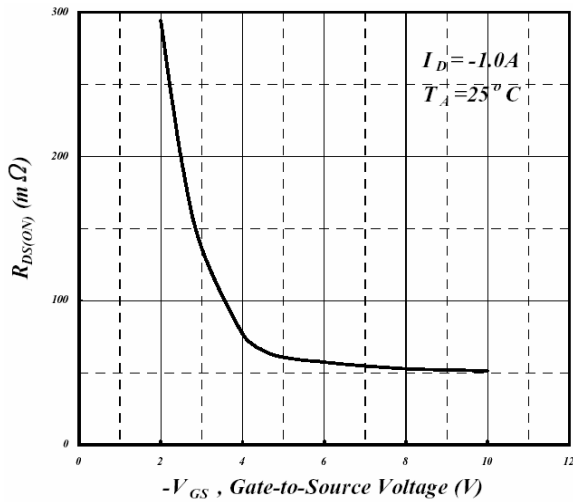
## Characteristics Curve



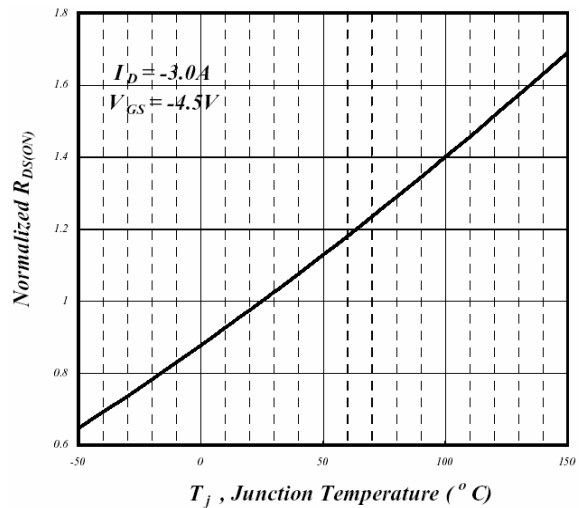
**Fig 1. Typical Output Characteristics**



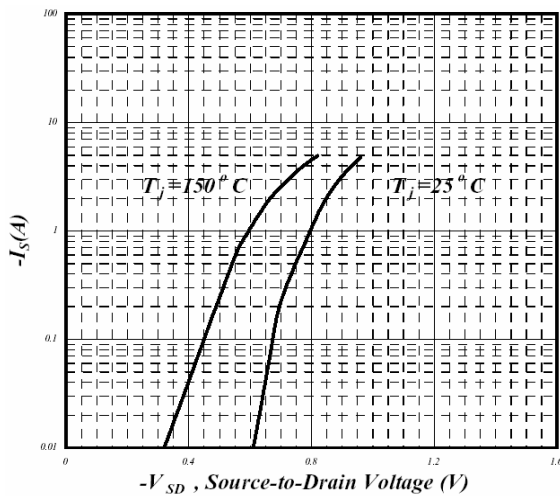
**Fig 2. Typical Output Characteristics**



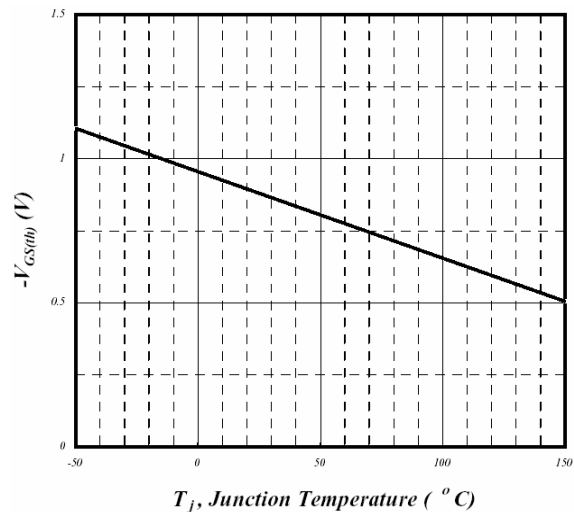
**Fig 3. On-Resistance v.s. Gate Voltage**



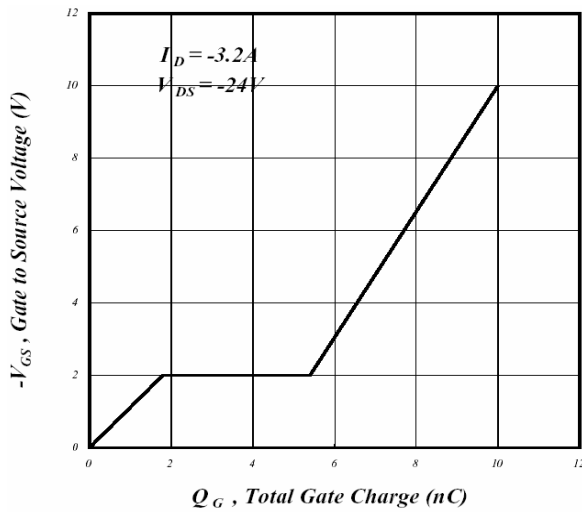
**Fig 4. Normalized On-Resistance v.s. Junction Temperature**



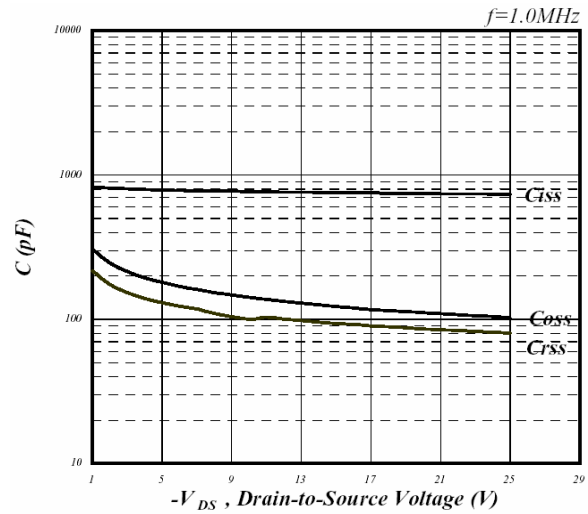
**Fig 5. Forward Characteristics of Reverse Diode**



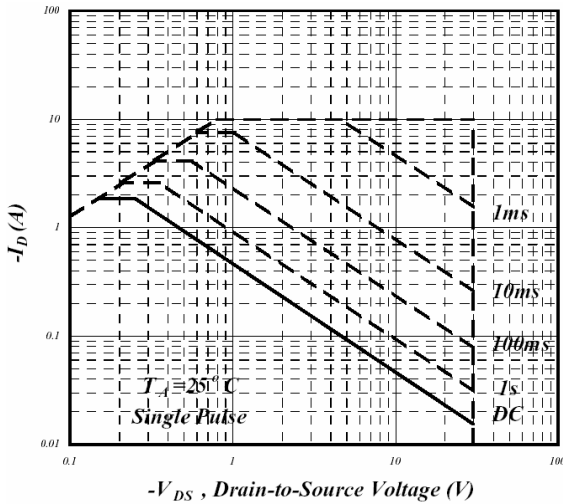
**Fig 6. Gate Threshold Voltage v.s. Junction Temperature**



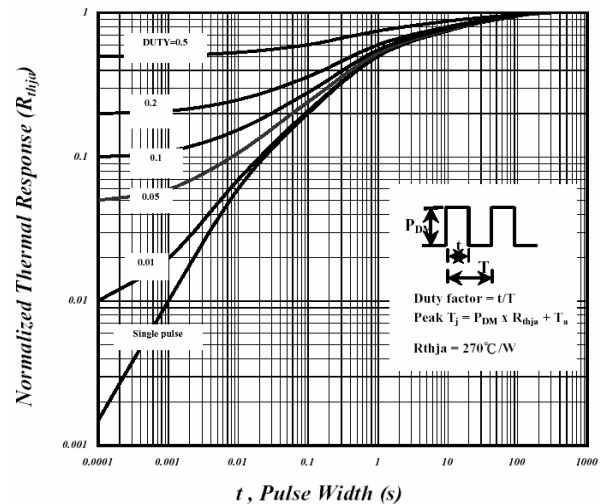
**Fig 7. Gate Charge Characteristics**



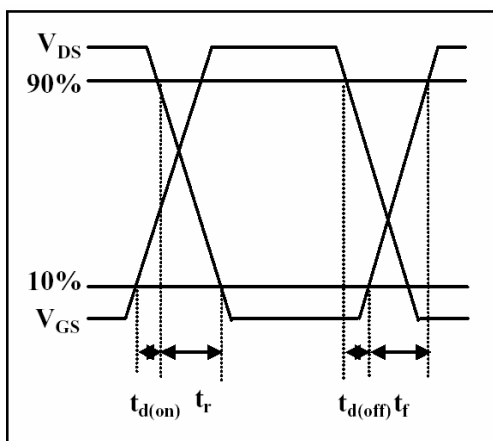
**Fig 8. Typical Capacitance Characteristics**



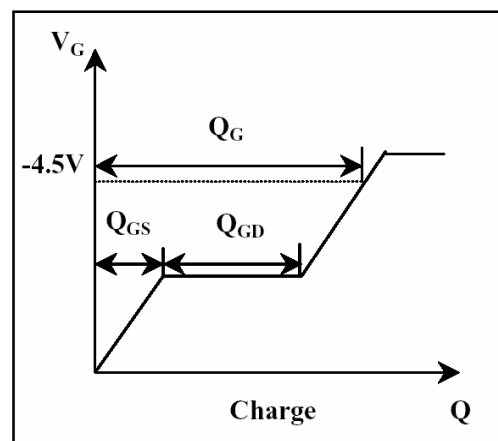
**Fig 9. Maximum Safe Operating Area**



**Fig 10. Effective Transient Thermal Impedance**



**Fig 11. Switching Time Waveform**



**Fig 12. Gate Charge Waveform**

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