

GC2X15MPS12-247

1200 V SiC MPS™ Diode



Silicon Carbide Schottky Diode

V_{RRM}	=	1200 V
$I_F (T_c = 135^\circ C)$	=	74 A*
Q_c	=	132 nC*

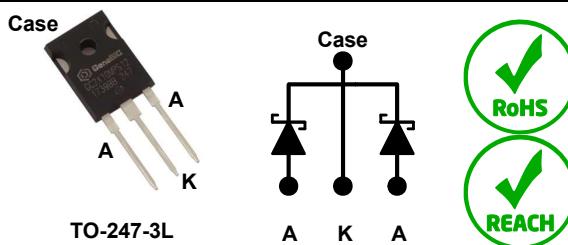
Features

- High Avalanche (UIS) Capability
- Enhanced Surge Current Capability
- Superior Figure of Merit Q_c/I_F
- Low Thermal Resistance
- 175 °C Maximum Operating Temperature
- Temperature Independent Switching Behavior
- Positive Temperature Coefficient of V_F
- Extremely Fast Switching Speeds

Advantages

- Low Standby Power Losses
- Improved Circuit Efficiency (Lower Overall Cost)
- Low Switching Losses
- Ease of Parallelizing without Thermal Runaway
- Smaller Heat Sink Requirements
- Low Reverse Recovery Current
- Low Device Capacitance
- Low Reverse Leakage Current

Package



Applications

- Boost Diode in Power Factor Correction (PFC)
- Switched Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Motor Drives
- Freewheeling / Anti-parallel Diode in Inverters
- Solar Inverters
- Electric Vehicles (EV) & Charging Stations
- Induction Heating & Welding

Absolute Maximum Ratings (At $T_c = 25^\circ C$ Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit
Repetitive Peak Reverse Voltage (Per Leg)	V_{RRM}		1200	V
Continuous Forward Current (Per Leg / Per Device)	I_F	$T_c = 25^\circ C, D = 1$ $T_c = 135^\circ C, D = 1$ $T_c = 166^\circ C, D = 1$	75/150 37/74 15/30	A
Non-Repetitive Peak Forward Surge Current, Half Sine Wave (Per Leg)	$I_{F,SM}$	$T_c = 25^\circ C, t_p = 10 \text{ ms}$ $T_c = 150^\circ C, t_p = 10 \text{ ms}$	100 80	A
Repetitive Peak Forward Surge Current, Half Sine Wave (Per Leg)	$I_{F,RM}$	$T_c = 25^\circ C, t_p = 10 \text{ ms}$ $T_c = 150^\circ C, t_p = 10 \text{ ms}$	66 44	A
Non-Repetitive Peak Forward Surge Current (Per Leg)	$I_{F,max}$	$T_c = 25^\circ C, t_p = 10 \mu\text{s}$	980	A
i^2t Value (Per Leg)	$\int i^2 dt$	$T_c = 25^\circ C, t_p = 10 \text{ ms}$	50	A^2s
Non-Repetitive Avalanche Energy (Per Leg)	E_{AS}	$L = 1.5 \text{ mH}, I_{AS} = 15 \text{ A}$	160	mJ
Diode Ruggedness (Per Leg)	dV/dt	$V_R = 0 \sim 960 \text{ V}$	100	V/ns
Power Dissipation (Per Leg / Per Device)	P_{tot}	$T_c = 25^\circ C$	512/1024	W
Operating and Storage Temperature	T_j, T_{stg}		-55 to 175	°C



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Electrical Characteristics (Per Leg)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Diode Forward Voltage	V_F	$I_F = 15 \text{ A}, T_j = 25^\circ\text{C}$	1.5	1.8	1.8	V
		$I_F = 15 \text{ A}, T_j = 175^\circ\text{C}$	2	2.4	2.4	
Reverse Current	I_R	$V_R = 1200 \text{ V}, T_j = 25^\circ\text{C}$	1.4	14	14	μA
		$V_R = 1200 \text{ V}, T_j = 175^\circ\text{C}$	4.2	50.4	50.4	
Total Capacitive Charge	Q_C	$V_R = 400 \text{ V}$	45	45	45	nC
		$V_R = 800 \text{ V}$	66	66	66	
Switching Time	t_s	$I_F \leq I_{F,\text{MAX}}$				
		$dI_F/dt = 200 \text{ A}/\mu\text{s}$	$T_j = 25^\circ\text{C}$	$V_R = 400 \text{ V}$	< 10	ns
Total Capacitance	C	$V_R = 1 \text{ V}, f = 1 \text{ MHz}, T_j = 25^\circ\text{C}$	1089	1089	1089	pF
		$V_R = 800 \text{ V}, f = 1 \text{ MHz}, T_j = 25^\circ\text{C}$	83	83	83	

Thermal / Mechanical Characteristics

Thermal Resistance, Junction – Case (Per Leg)	R_{thJC}	0.25	$^\circ\text{C}/\text{W}$
Weight	W_T	6.1	g
Mounting Torque	T_M	1.1	Nm

* Per Device



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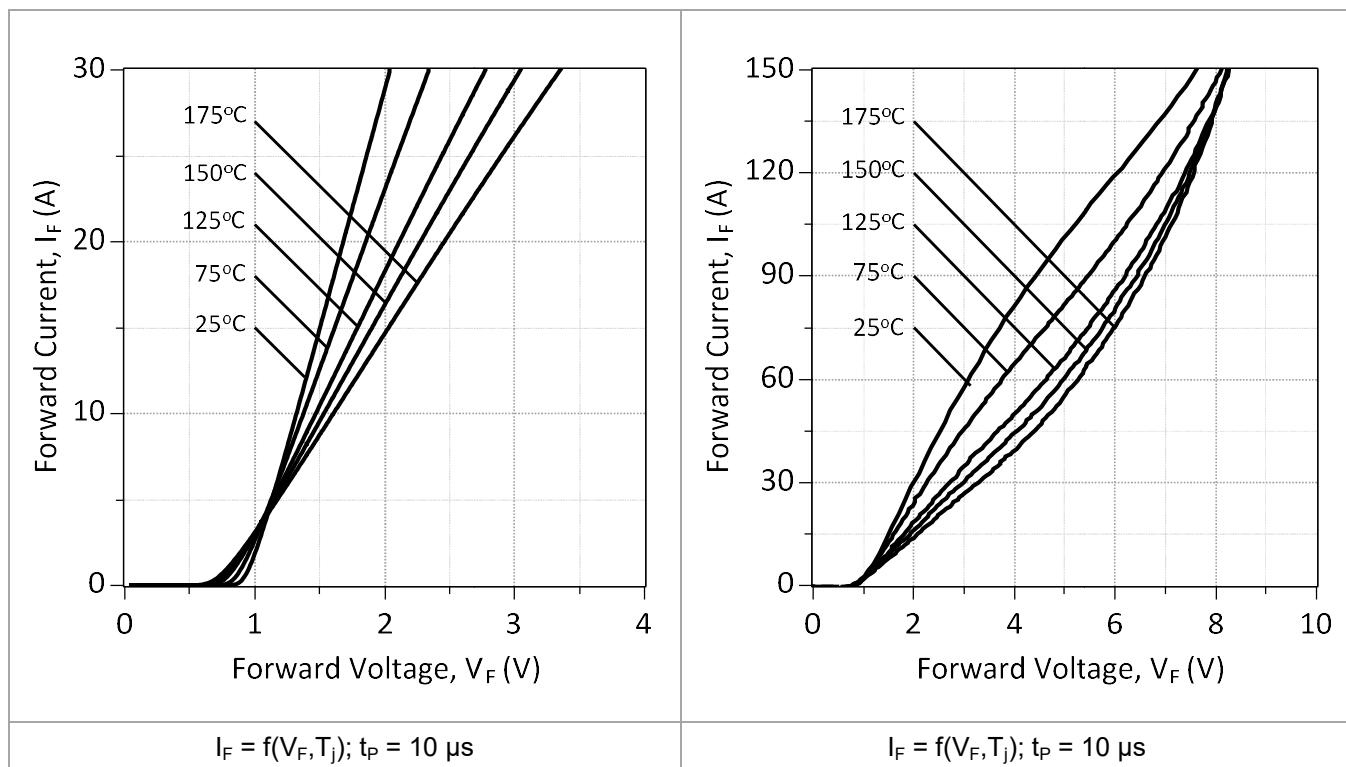


Figure 1: Typical Forward Characteristics (Per Leg)

Figure 2: Typical High Current Forward Characteristics (Per Leg)

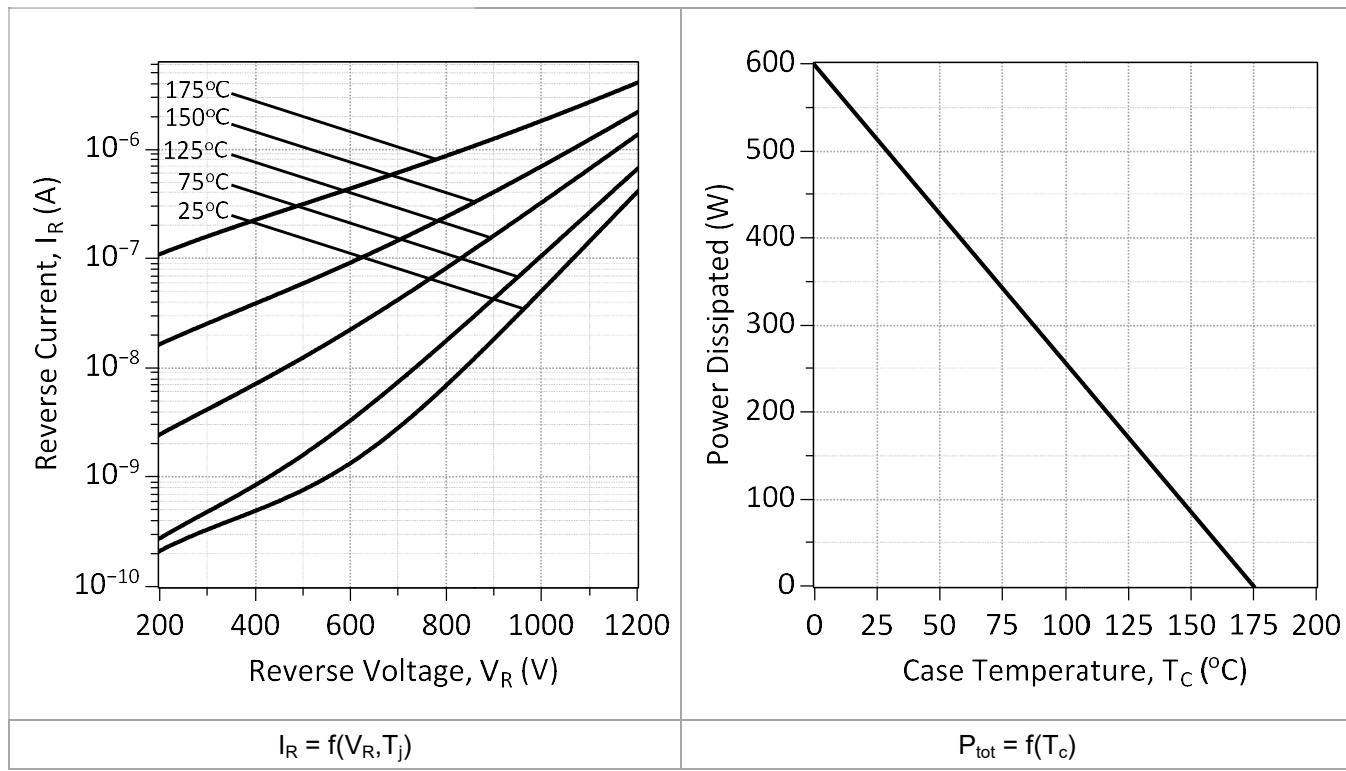


Figure 3: Typical Reverse Characteristics (Per Leg)

Figure 4: Power Derating Curve (Per Leg)



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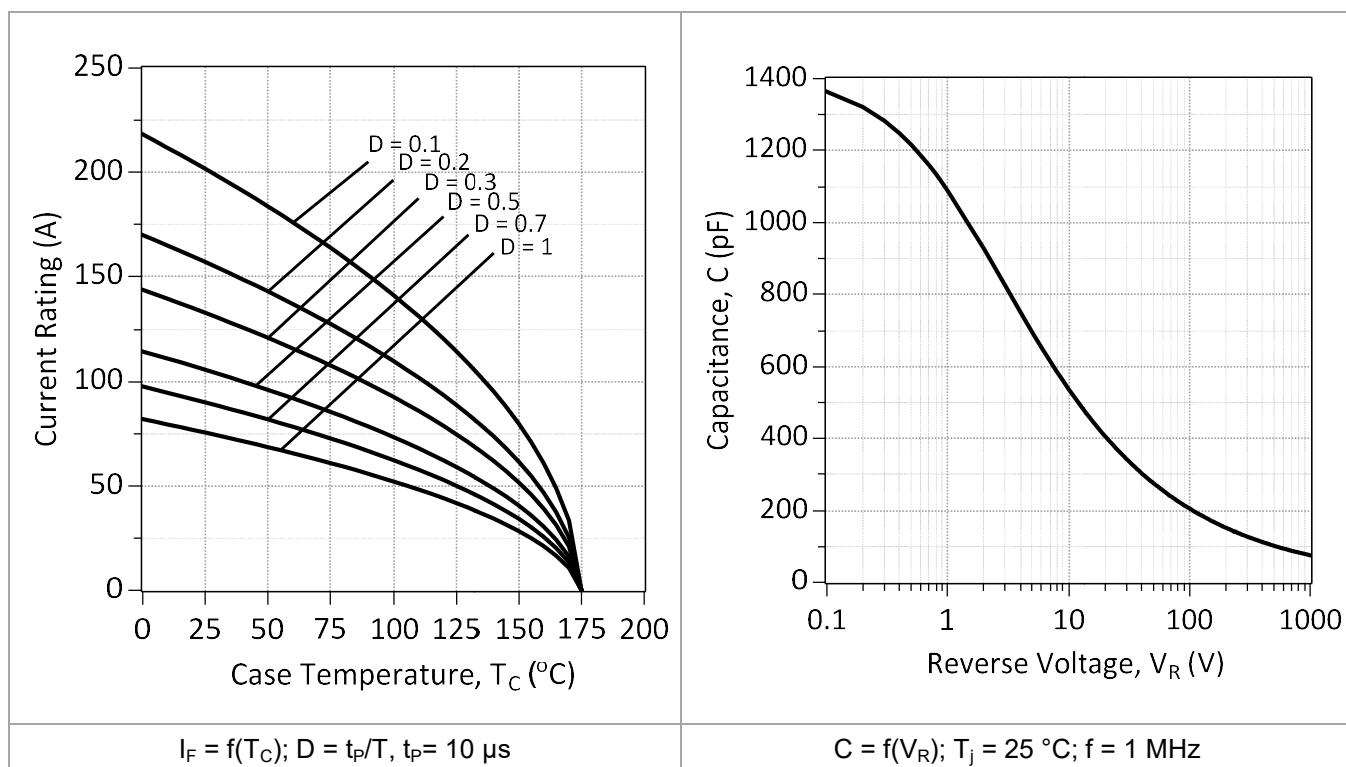


Figure 5: Current Derating Curves(Per Leg)

Figure 6: Typical Junction Capacitance vs. Reverse Voltage Characteristics (Per Leg)

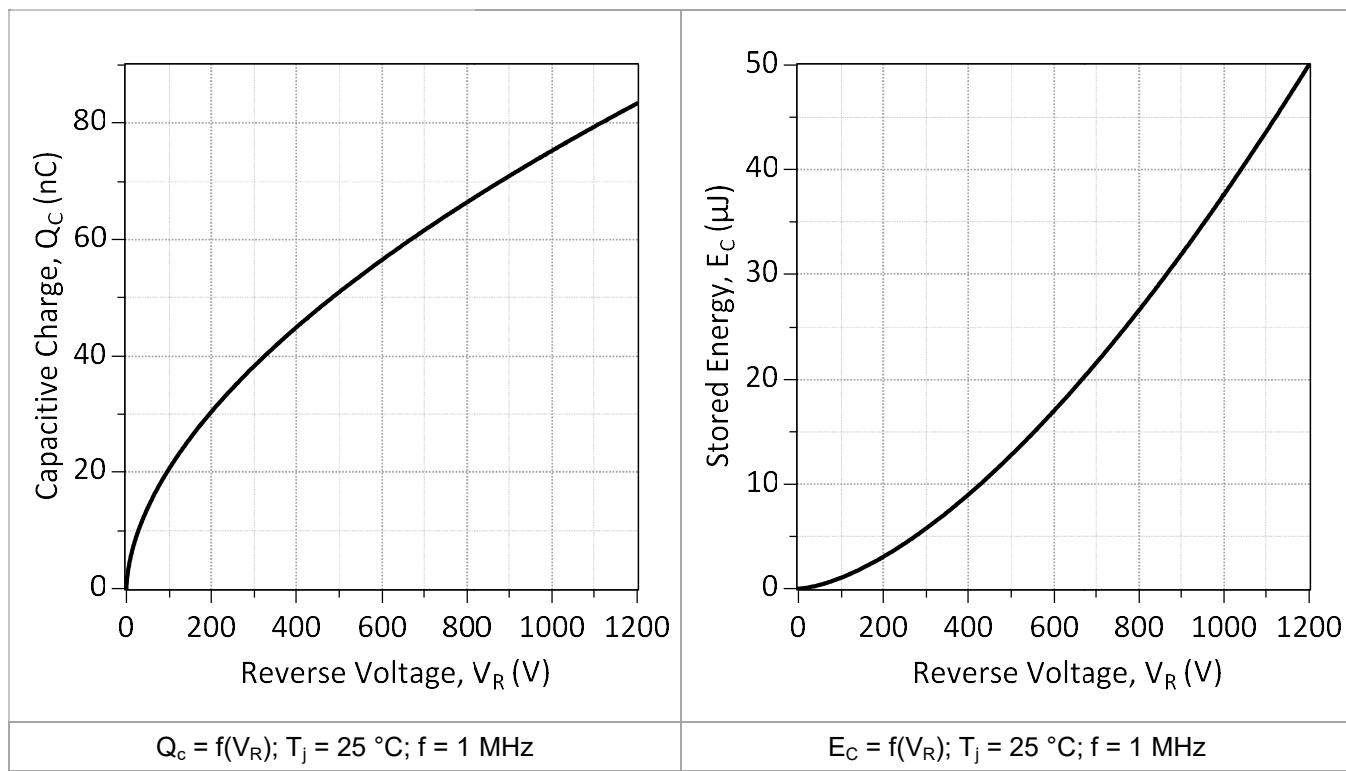
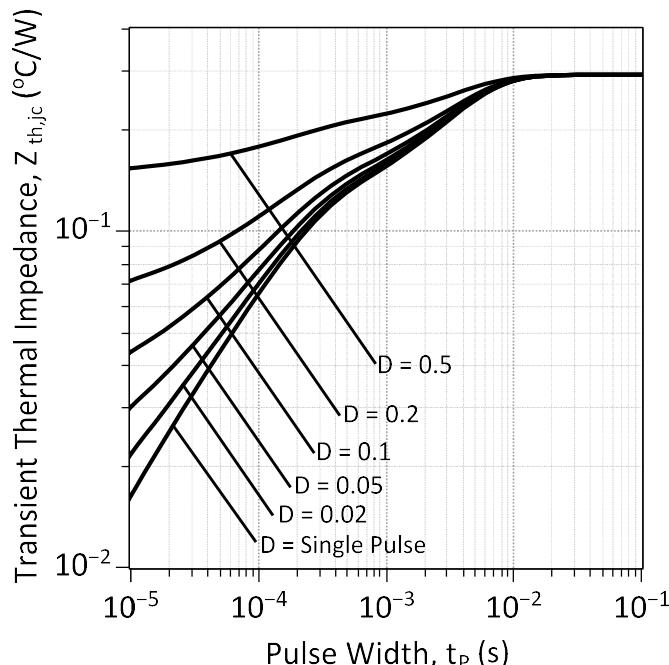


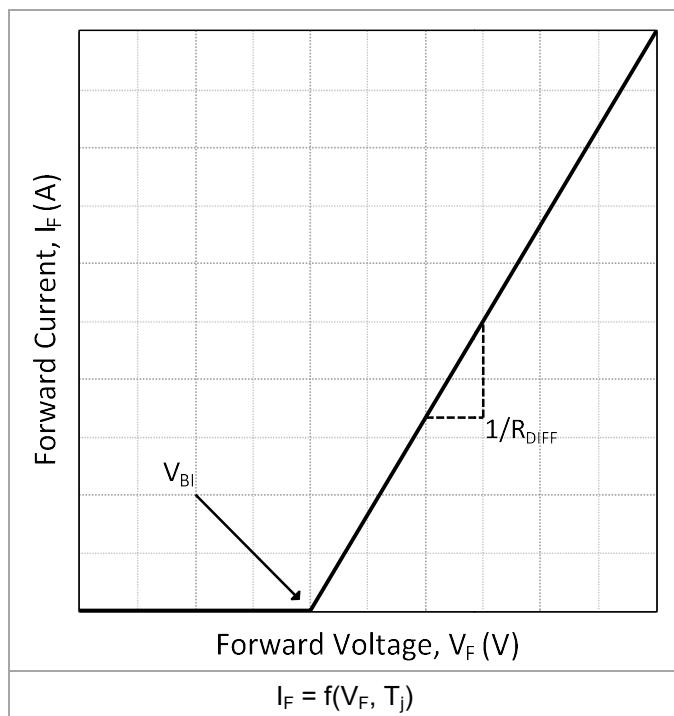
Figure 7: Typical Capacitive Charge vs. Reverse Voltage Characteristics (Per Leg)

Figure 8: Typical Capacitive Energy vs. Reverse Voltage Characteristics (Per Leg)



$$Z_{th,jc} = f(t_p, D); D = t_p/T$$

Figure 9: Transient Thermal Impedance (Per Leg)



$$I_F = (V_F - V_{BI})/R_{DIFF} \text{ (A)}$$

Built-In Voltage (V_{BI}):

$$V_{BI}(T_j) = m * T_j + n \text{ (V)},$$

$$m = -1.54e-03, n = 1.01$$

Differential Resistance (R_{DIFF}):

$$R_{DIFF}(T_j) = a * T_j^2 + b * T_j + c \text{ (\Omega)};$$

$$a = 8.42e-07, b = 1.41e-04, c = 0.0298$$

Figure 10: Forward Curve Model (Per Leg)

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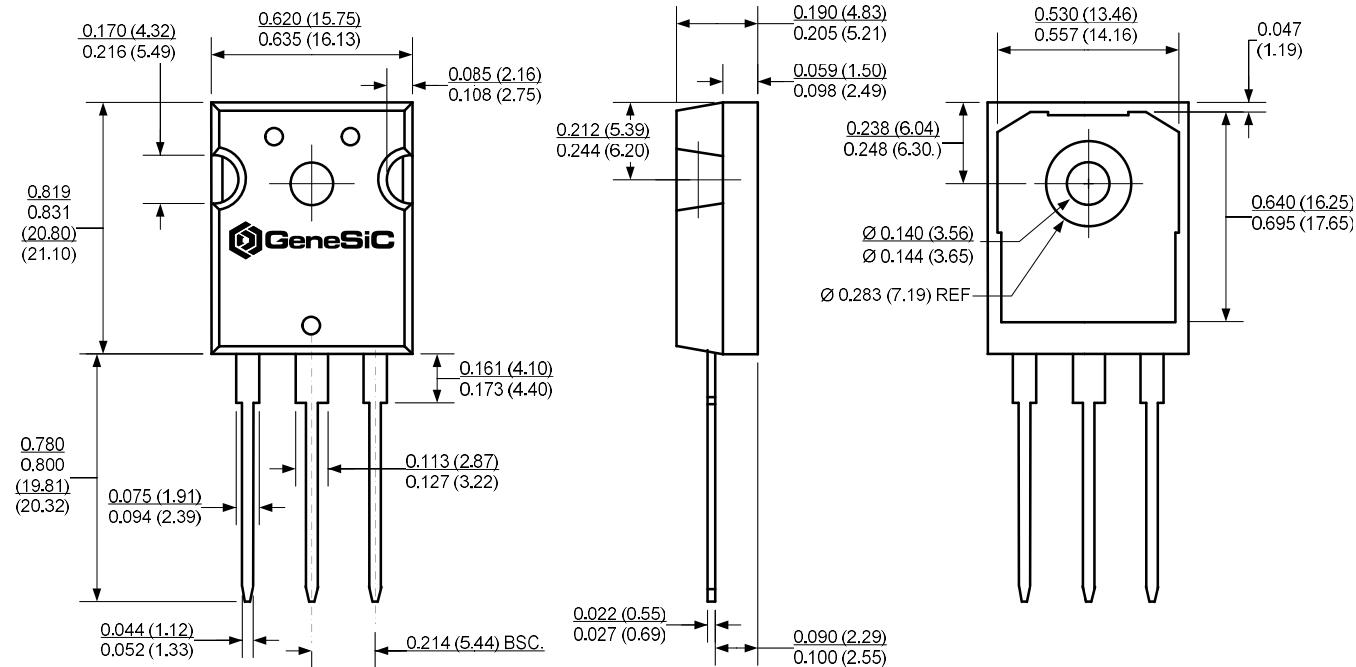
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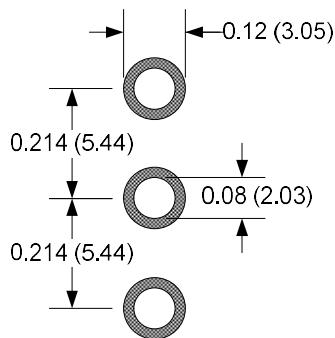
Package Dimensions

TO-247-3L

Package Outline



Recommended Solder Pad Layout



NOTE

1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

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RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS), as implemented November 15, 2017. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

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Related Links

- Soldering Document: <http://www.genesicsemi.com/quality/quality-manual/>
- Tin-whisker Report: <http://www.genesicsemi.com/quality/compliance/>
- Reliability Report: <http://www.genesicsemi.com/quality/reliability/>