1200V 50A SiC Schottky MPS™ Diode



V_{RRM} = 1200 V $I_{F (Tc = 135^{\circ}C)}$ = 70 A Q_{C} = 110 nC

Silicon Carbide Schottky Diode

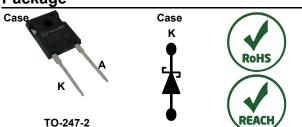
Features

- High Avalanche (UIS) Capability
- Enhanced Surge Current Capability
- Superior Figure of Merit Q_C/I_F
- Low Thermal Resistance
- 175 °C Maximum Operating Temperature
- Temperature Independent Switching Behavior
- Positive Temperature Coefficient of V_F
- Extremely Fast Switching Speeds

Advantages

- Low Standby Power Losses
- Improved Circuit Efficiency (Lower Overall Cost)
- Low Switching Losses
- Ease of Paralleling without Thermal Runaway
- Smaller Heat Sink Requirements
- Low Reverse Recovery Current
- Low Device Capacitance
- Low Reverse Leakage Current

Package



Applications

- Boost Diode in Power Factor Correction (PFC)
- Switched Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Motor Drives
- Freewheeling / Anti-parallel Diode in Inverters
- Solar Inverters & Wind Energy Converters
- Electric Vehicles (EV) & DC Fast Charging
- Induction Heating & Welding

Absolute Maximum Ratings (At T_C = 25 °C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	
Repetitive Peak Reverse Voltage	V_{RRM}		1200	V	
		$T_C = 25 ^{\circ}C, D = 1$	150		
Continuous Forward Current	I_{F}	$T_C = 135 ^{\circ}C, D = 1$	70	Α	
		$T_C = 152 ^{\circ}C, D = 1$	50		
Non-Repetitive Peak Forward Surge Current, Half Sine Wave	I _{F,SM}	T_C = 25 °C, t_P = 10 ms	400	Δ.	
		T_{C} = 150 °C, t_{P} = 10 ms	320	Α	
Repetitive Peak Forward Surge Current, Half Sine Wave	$I_{F,RM}$	$T_C = 25 ^{\circ}\text{C}, t_P = 10 \text{ms}$	240	А	
		T_C = 150 °C, t_P = 10 ms	168		
Non-Repetitive Peak Forward Surge Current	I _{F,max}	T _C = 25 °C, t _P = 10 μs	2000	А	
i ² t Value	∫i² dt	T_C = 25 °C, t_P = 10 ms	800	A ² s	
Non-Repetitive Avalanche Energy	E _{AS}	L = 0.5 mH, I _{AS} = 50 A	600	mJ	
Diode Ruggedness	dV/dt	V _R = 0 ~ 960 V	200	V/ns	
Power Dissipation	P _{tot}	T _C = 25 °C	625	W	
Operating and Storage Temperature	T _j , T _{stg}		-55 to 175	°C	

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Electrical Characteristics

Danamatan	Comple of	Conditions		Values			11
Parameter	Symbol			Min.	Тур.	Max.	Unit
Diode Forward Voltage	V	I _F = 50 A, T _j = 25 °C			1.5	1.8	V
	V_{F}	$I_F = 50 \text{ A}, T_j = 175 ^{\circ}\text{C}$			2	2.4	
Reverse Current	I-	V _R = 1200 V, T _j = 25 °C		•	5	25	μA
	I _R	$V_R = 1200 \text{ V}, T_j$		50	250		
Total Capacitive Charge	0		V _R = 400 V		78		nC
	Q_{C}	$I_F \le I_{F,MAX}$	$V_{R} = 800 \text{ V}$		110		
Switching Time	4	$dI_F/dt = 200 A/\mu s$ $T_j = 175 °C$	V _R = 400 V		< 10		ns
	t_s		$V_{R} = 800 \text{ V}$				
Total Capacitance	С	$V_R = 1 V, f = 1 MHz$			226		
	C	$V_R = 800 \text{ V}, f = 1 \text{ MHz}$			163		pF

Thermal / Mechanical Characteristics

Thermal Resistance, Junction - Case	R _{thJC}		0.24	°C/W
Weight	W_{T}		6	g
Mounting Torque	T _M	M3 Screw	1	I.1 Nm

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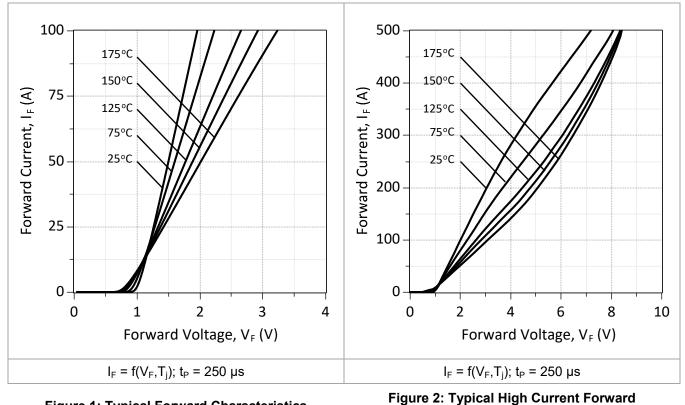


Figure 1: Typical Forward Characteristics

 10^{-4} 600 **10**⁻⁵ 500 Reverse Current, I_{R} (A) Power Dissipated (W) 400 10^{-6} 300 10^{-7} 200 10^{-8} 100 10^{-9} 0 25 50 75 100 125 150 175 400 600 800 1000 200 1200 Case Temperature, T_C (°C) Reverse Voltage, V_R (V) $I_R = f(V_R, T_j)$ $P_{tot} = f(T_C); T_j = 175 °C$

Figure 3: Typical Reverse Characteristics

Figure 4: Power Derating Curve

Characteristics

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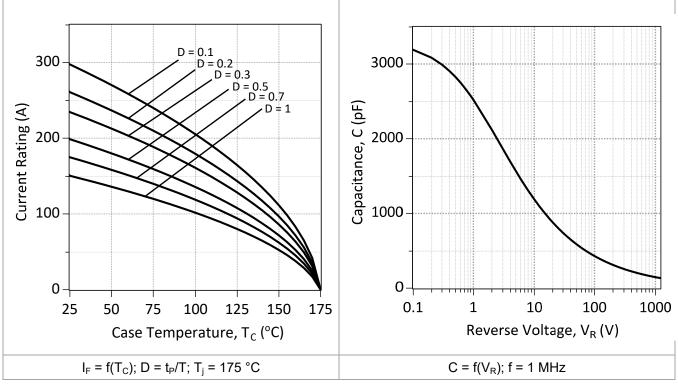


Figure 5: Current Derating Curves

150 (2) 120 90 60 0 300 600 900 1200 Reverse Voltage, V_R (V)

Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics

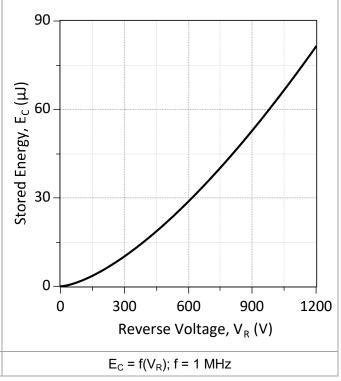


Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics

Figure 8: Typical Capacitive Energy vs Reverse Voltage Characteristics

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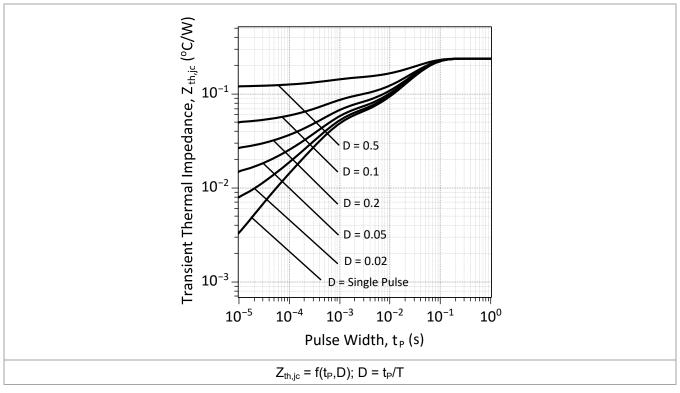


Figure 9: Transient Thermal Impedance

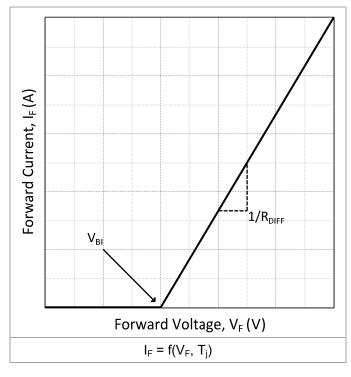


Figure 10: Forward Curve Model

$$I_F = (V_F - V_{BI})/R_{DIFF}$$
 (A)

Built-In Voltage (V_{BI}):

$$V_{BI}(T_j) = m^*T_j + n (V),$$

 $m = -1.47e-03, n = 1.08$

Differential Resistance (RDIFF):

$$R_{DIFF}(T_j) = a^*T_j^2 + b^*T_j + c (\Omega);$$

 $a = 2.87e-07, b = 3.40e-05, c = 0.0076$

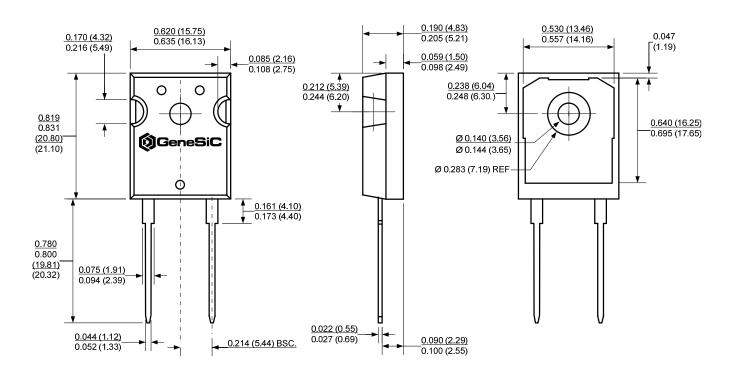
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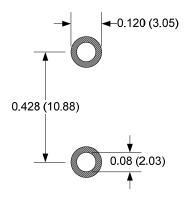
Package Dimensions

TO-247-2

Package Outline



Recommended Solder Pad Layout



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

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RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

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Related Links

- SPICE Models: https://www.genesicsemi.com/schottky-mps
- Evaluation Boards: https://www.genesicsemi.com/technical-support
- Quality Manual: https://www.genesicsemi.com/technical-support/quality-manual
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