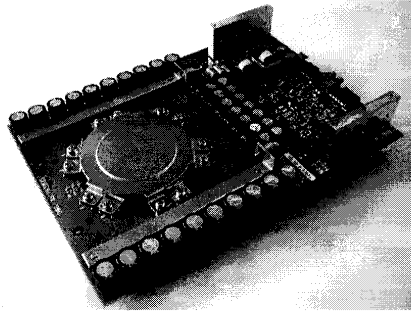


MITSUBISHI GCT(Gate Commutated Turn-off) THYRISTOR UNIT

**GCU15BA-130**

HIGH POWER INVERTER USE  
PRESS PACK TYPE

GCU15BA-130



- Symmetrical GCT unit
- GCT and gate driver are connected
- $I_{TQRM}$  : Repetitive controllable on state current...1500A
- $I_{T(AV)}$  : Average on-state current.....500A
- $V_{DRM}$  : Repetitive peak off state voltage .....6500V
- $V_{RRM}$  : Repetitive peak reverse voltage .....6500V
- $T_j$  : Operation junction temperature.....125deg

**APPLICATION**

Current source inverters, DC choppers, Induction heaters, DC to DC converter

**GCT PART(Type name: FGC1500B-130DS)**

Symbol	Parameter	Condition	Voltage class	Unit
$V_{RRM}$	Repetitive peak reverse voltage	-	6500	V
$V_{RSM}$	Non-repetitive peak reverse voltage	-	6500	V
$V_{DRM}$	Repetitive peak off state voltage	$V_{GK}=-2V$	6500	V
$V_{DSM}$	Non-repetitive peak off state voltage	$V_{GK}=-2V$	6500	V
$V_{LTD5}$	Long term DC stability voltage	$V_{GK}=-2V, \lambda=100Fit$	3600	V

Symbol	Parameter	Condition	Ratings	Unit
$I_{TQRM}$	Repetitive controllable on state current	$V_{DM}=3/4V_{DRM}, V_D=3000V, T_j=25/125deg$ $L_C=0.3\mu H, \text{ With GU-D15 (See Fig.1,3)}$	1500	A
$I_{T(RMS)}$	RMS on-state current	Applied for all condition angles	780	A
$I_{T(AV)}$	Average on-state current	$f=60Hz, \text{ sinewave } \theta=180^\circ, T_f=66deg$	500	A
$I_{TSM}$	Surge on-state current	One half cycle at 60Hz, $T_j=125deg$ start	8	kA
$I^2t$	Current-squared, time integration		$2.7 \times 10^5$	A <sup>2</sup> s
$di/dt$	Critical rate of rise of on state current	$I_T=1500A, V_D=3000V, T_j=25/125deg$ $C_s=0.2\mu F, R_s=5ohm, f=60Hz$ With GU-D15 (See Fig.1,2)	1000	A/ $\mu s$
$di/dt$	Critical rate of rise of reverse recovery current	$I_T=1500A, V_R=3000V, T_j=25/125deg$ $C_s=0.2\mu F, R_s=5ohm$ (See Fig.4,5)	1000	A/ $\mu s$
$V_{FGM}$	Peak forward gate voltage		10	V
$V_{RGM}$	peak reverse gate voltage		21	V
$I_{FGM}$	Peak forward gate current		900	A
$I_{RGM}$	Peak reverse gate current		1500	A
$P_{FGM}$	Peak forward gate power dissipation		9	kW
$P_{RGM}$	Peak reverse gate power dissipation		32	kW
$P_{FG(AV)}$	Average forward gate power dissipation		180	W
$P_{RG(AV)}$	Average reverse gate power dissipation		230	W
$T_j$	Operation junction temperature		-20~125	°C
$T_{stg}$	Storage temperature		-20~150	°C
-	Mounting force required	(Recommended value 20 kN)	18 ~ 24	kN
-	Weight	Typical value 760g	-	g

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Symbol	Parameter	Condition	Limits			Unit
			Min	Typ	Max	
V <sub>TM</sub>	On-state voltage	I <sub>T</sub> =800A, T <sub>J</sub> =125deg	—	—	6	V
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>RM</sub> =6500V, T <sub>J</sub> =125deg	—	—	300	mA
I <sub>DRM</sub>	Repetitive peak off state current	V <sub>DM</sub> =6500V, V <sub>GK</sub> =-2V, T <sub>J</sub> =125deg	—	—	150	mA
I <sub>GRM</sub>	Reverse gate current	V <sub>RG</sub> =21V, T <sub>J</sub> =125deg	—	—	100	mA
dv/dt	Critical rate of rise of off state voltage	V <sub>D</sub> =3000V, V <sub>GK</sub> =-2V, T <sub>J</sub> =125deg (Expo. wave)	3000	—	—	V/μs
t <sub>gt</sub>	Turn-on time	I <sub>T</sub> =1500A, V <sub>D</sub> =3000V, di/dt=1000A/μs	—	—	5	μs
t <sub>d</sub>	Turn-on delay time	C <sub>S</sub> =0.2μF, R <sub>S</sub> =5ohm, T <sub>J</sub> =125deg With GU-D15 (See Fig.1.2)	—	—	1	μs
E <sub>on</sub>	Turn-on switching energy	I <sub>T</sub> =800A, V <sub>D</sub> =3000V, di/dt=1000A/μs C <sub>S</sub> =0.2μF, R <sub>S</sub> =5ohm, T <sub>J</sub> =125deg With GU-D15 (See Fig.1.2)	—	—	1.3	J/P
t <sub>s</sub>	Storage time	I <sub>T</sub> =1500A, V <sub>DM</sub> =3/4V <sub>DRM</sub> , V <sub>D</sub> =3000V C <sub>S</sub> =0.2μF, R <sub>S</sub> =5ohm, T <sub>J</sub> =125deg With GU-D15 (See Fig.1.5)	—	—	3	μs
E <sub>off</sub>	Turn-off switching energy	I <sub>T</sub> =800A, V <sub>DM</sub> =4000V, V <sub>D</sub> =3000V C <sub>S</sub> =0.2μF, R <sub>S</sub> =5ohm, T <sub>J</sub> =125deg With GU-D15 (See Fig.1.5)	—	—	5.2	J/P
Q <sub>RR</sub>	Reverse recovery charge	I <sub>T</sub> =800A, V <sub>R</sub> =3000V, di/dt=1000A/μs	—	—	2000	μC
E <sub>rec</sub>	Reverse recovery energy	C <sub>S</sub> =0.2μF, R <sub>S</sub> =5ohm, T <sub>J</sub> =125deg (See Fig.4.5)	—	—	7.4	J/P
I <sub>GT</sub>	Gate trigger current	V <sub>D</sub> =24V, R <sub>L</sub> =0.1ohm, T <sub>J</sub> =25deg	—	—	0.75	A
V <sub>GT</sub>	Gate trigger voltage	DC method	—	—	1.5	V
R <sub>th(j-f)</sub>	Thermal resistance	Junction to Fin	—	—	0.016	K/W

MITSUBISHI GCT(Gate Commutated Turn-off) THYRISTOR UNIT

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HIGH POWER INVERTER USE  
PRESS PACK TYPE

GATE DRIVER PART(Type name: GU-D15)

Parameter	Symbol	Unit	Ratings			Conditions
			Min	Typ	Max	
Power supply (NOTE 1)	Vc	V	19	20	21	DC power supply
Power consumption (NOTE 2)	P	W	2.5	-	-	-
Control signal (NOTE 3)	-	-	-	-	-	I <sub>T</sub> =830Arms, f=780Hz duty=0.33
Frequency	f	Hz	-	-	780	Optical fiber data link Transmitter: HFBR-1521(HP) Receiver:HFBR-2521(HP)
Delay time of on gate current	tfd	μs	-	-	3.0	I <sub>T</sub> =830Arms, duty=0.33
Delay time of off gate current	trd	μs	-	-	3.0	
Critical rate of rise of on gate current	di <sub>o</sub> /dt	A/μs	50	-	-	
Peak on gate current	I <sub>GM</sub>	A	-	90	-	
Width of on high gate current	tw	μs	3	-	-	
On gate current	I <sub>G</sub>	A	1.9	-	-	T <sub>j</sub> ≥-10deg
Critical rate of rise of off gate current	di <sub>o</sub> q/dt	A/μs	-	2250	-	V <sub>RG</sub> =20V
Maximum duty	Dmax	%	-	-	100	
Temperature	Ta	deg.	-10	-	+60	Operation temperature (Recommend≤40deg)
Weight	-	g	-	1560	-	With FGC1500B-130DS
Status signal (NOTE 4)	-	-	-	-	-	



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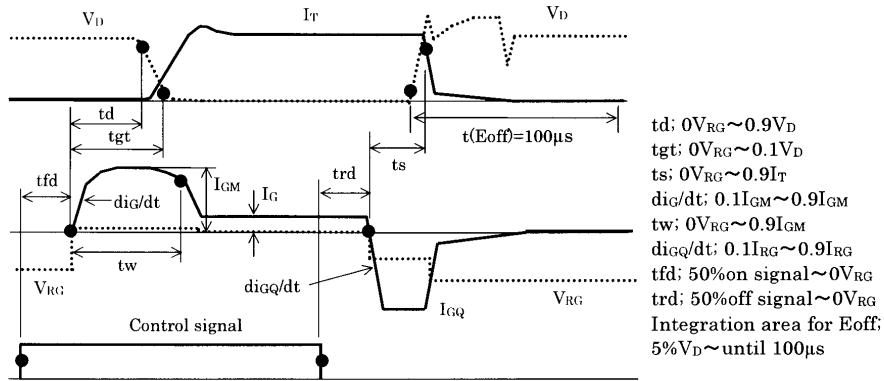


Fig.1: Turn on and turn off waveform

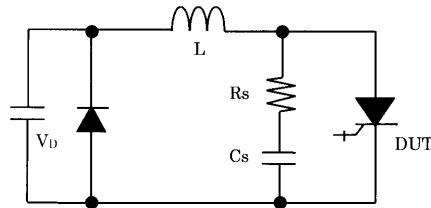


Fig.2: Turn-on test circuit

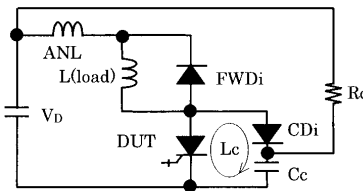


Fig.3: Turn-off test circuit (With clamp circuit)

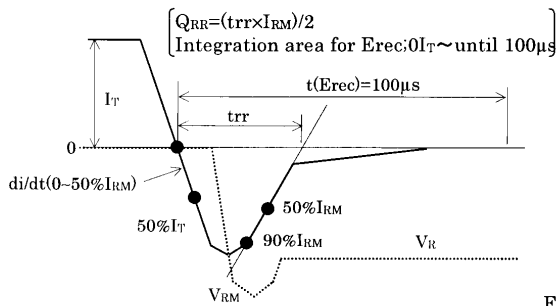


Fig.4: Reverse recovery waveform

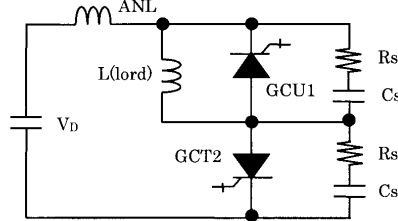


Fig.5: Turn off and Reverse recovery test circuit

Without the GDU and  $I_G = 0.75A(DC)$  is supplied to GCT 1 at reverse recovery test.

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- (NOTE 1): (1)Guaranteed power supply voltage for operation is 19V minimum and 21V maximum.  
 (2)When over voltage occurs, GDU voltage bus (G-K voltage for GCT) is clamped to be 21V if maximum voltage for input gate voltage from power supply is 25V and maximum duration of over voltage higher than 21V from power supply is 0.4 sec when control signal is on(Please see Fig.8).  
 (3)When over voltage occurs, GDU voltage bus is clamped to be 21V with no time limitation if maximum voltage from power supply is 25V and control signal is off(Please see Fig.8).

(NOTE 2): GCT is off state and no gate signal is supplied for gate driver. No leakage current flows between gate and cathode of GCT.

(NOTE 3): Optic fiber data link HFBR-1521 and HFBR-2521 are interlocked each other.

(NOTE 4): 1. Status signal from LED

(1)Status signal

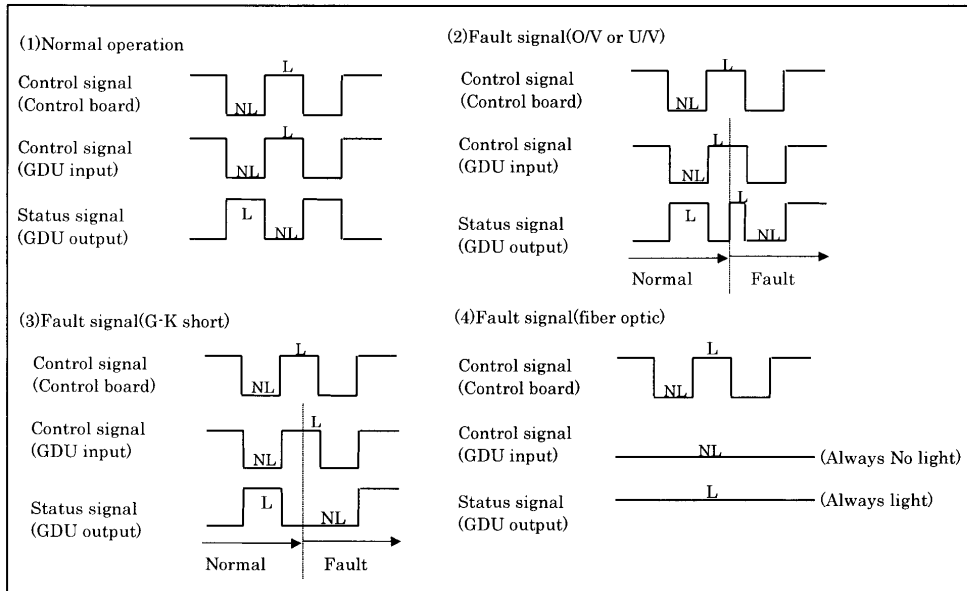
Status of GCT	LED 1 (Red)	LED 2 (Yellow)
On state	OFF	ON
Off state	ON	OFF

(2)Fault signal

Status	G-K	PS	PS LED (LED 4)	G-K LED (LED 3)
Normal	Normal	20±1V	On	On
Fault	Normal	Voltage down	Off, V<17.5V(Typ.)	Off(If V>12.5V(Typ.) then LED 3 is on)
Fault	G-K short	20±1V	On	Off
Fault	G-K short	Voltage down	Off, V<17.5V(Typ.)	Off

2. Status signal from Transmitter(NOTE 5)

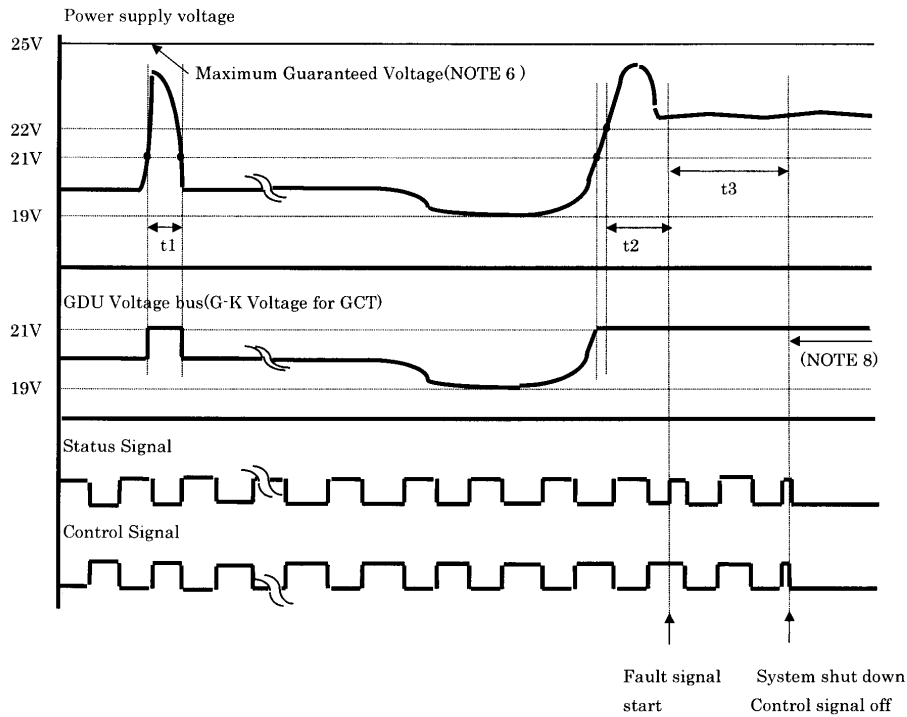
(L: Light NL: No light)



(NOTE 5):About over voltage fault signal, please see Fig.6.

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 HIGH POWER INVERTER USE  
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Fig.6: Over voltage fault signal timing chart



- 1)  $t1 < 1.0 \text{ ms}$   
Max. repetition rate; 1 pulse/100 ms
- 2)  $t2 \geq 1.0 \text{ ms}$
- 3)  $t3 < 0.4 \text{ sec}$

(NOTE 6): Maximum peak voltage of GDU input voltage from power supply should be lower than 25V.

(NOTE 7): If the period for over voltage  $< 1.0 \text{ ms}$ (period  $t1$ ),no fault signal is sent.

If the period for over voltage  $\geq 1.0 \text{ ms}$ , fault signal starts after period  $t2$  from 22V of power supply voltage.

System should be shut down(Control signal should be off) within period  $t3$  from fault signal start.

(NOTE 8):GDU Voltage bus(G-K Voltage for GCT) is clamped to be 21V if power supply voltage is higher than 21V after system shut down(control signal off).

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HIGH POWER INVERTER USE  
PRESS PACK TYPE

(NOTE 9): Additional support for vibration test  
Additional support is necessary for vibration test of GCU15BA-130.  
Fig.7 shows detailed figure about connection method between gate driver and heat sink by additional support.

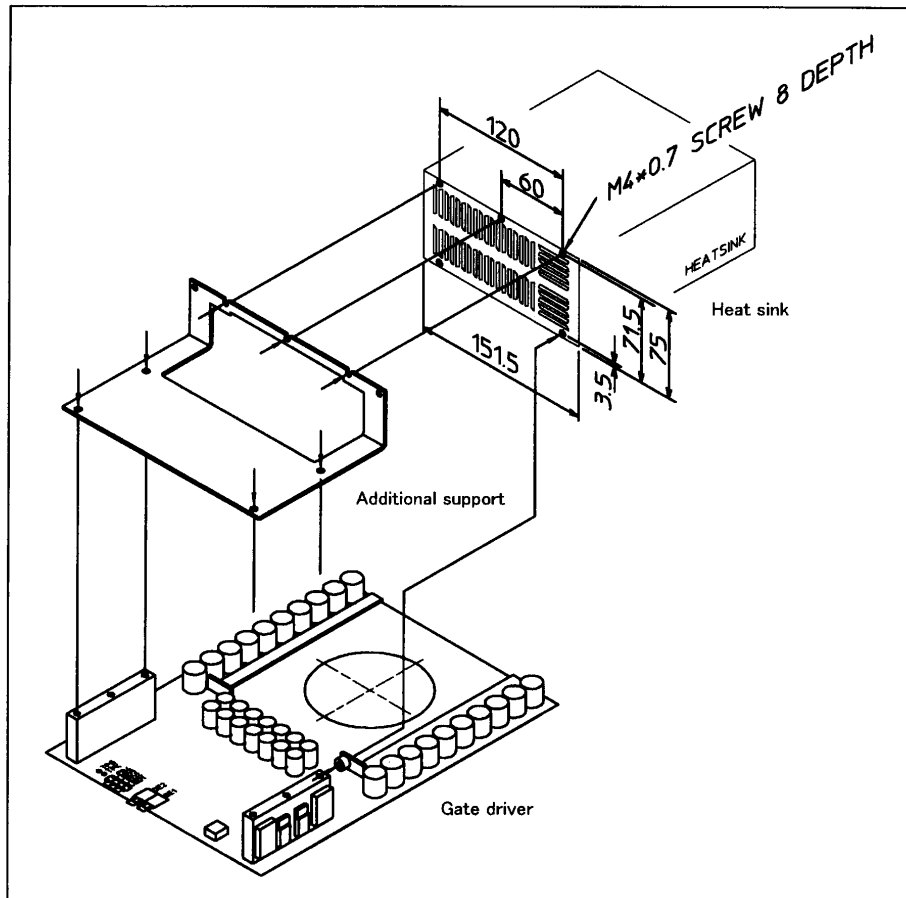
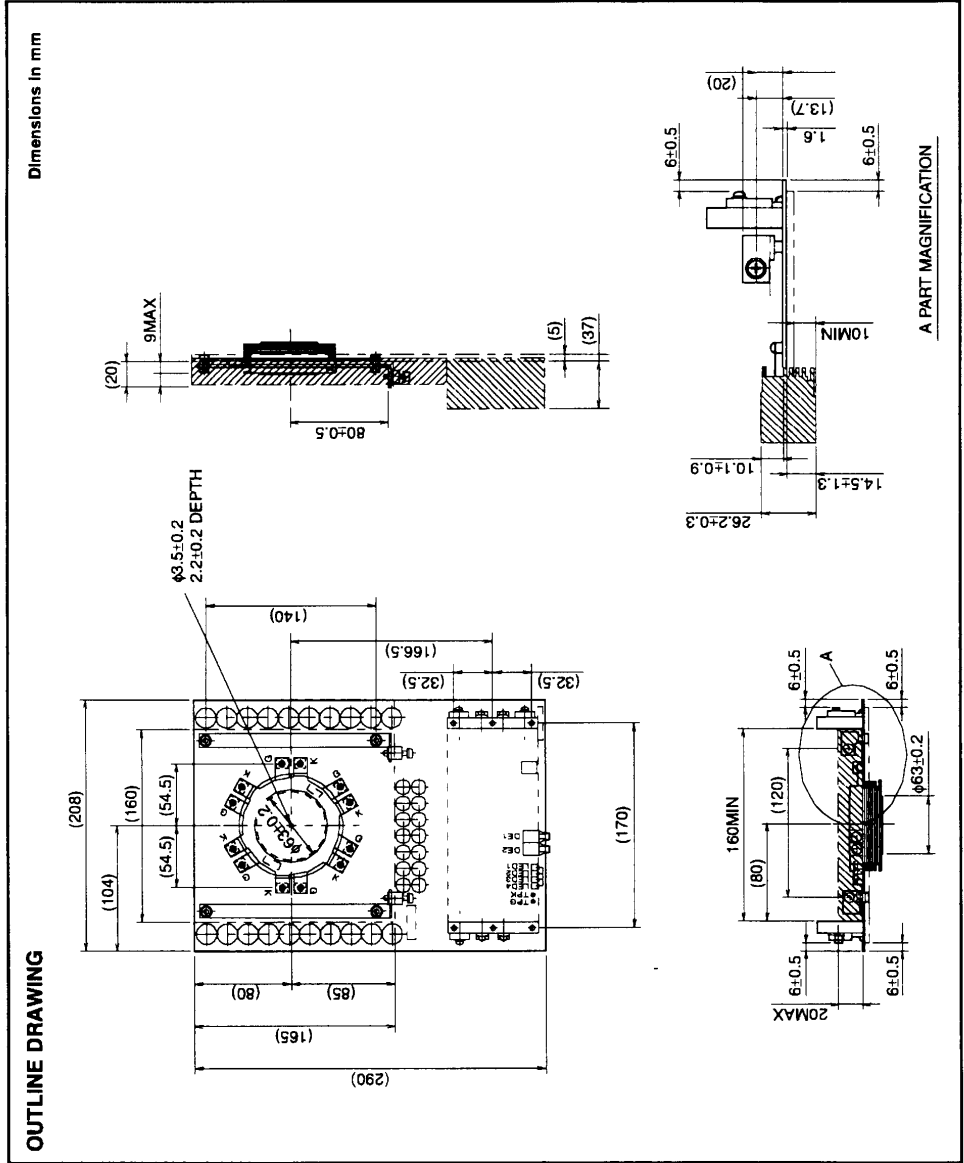


Fig.7: connection method between gate driver and heat sink by additional support

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HIGH POWER INVERTER USE  
PRESS PACK TYPE



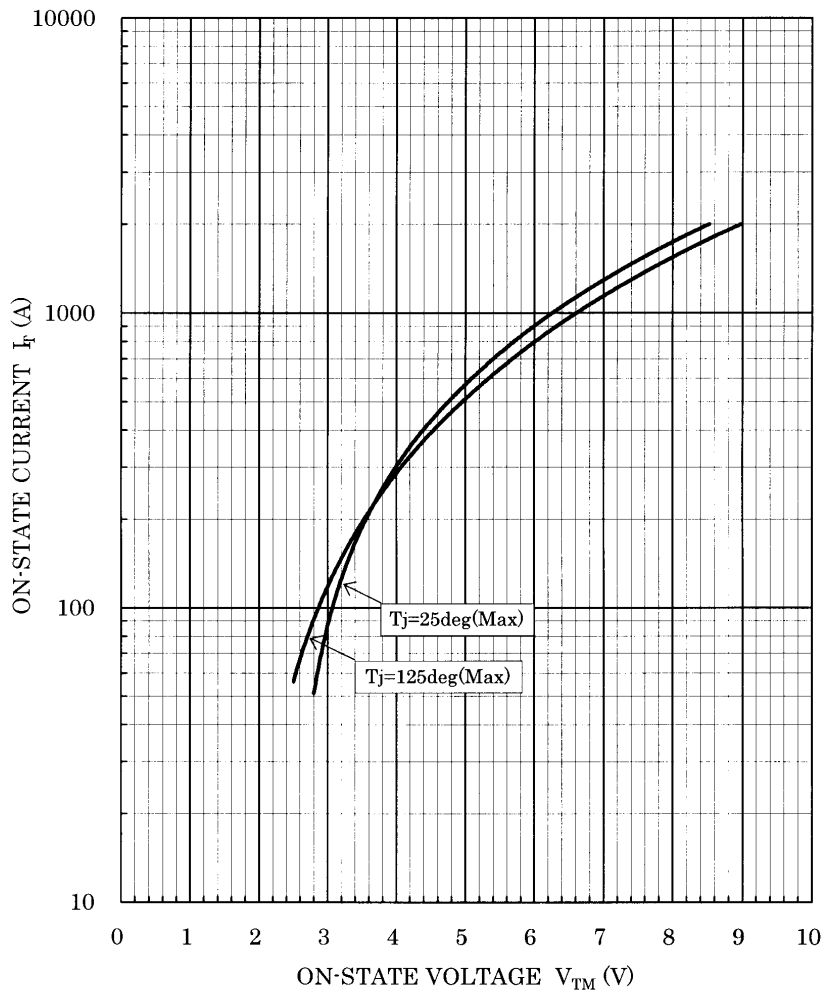


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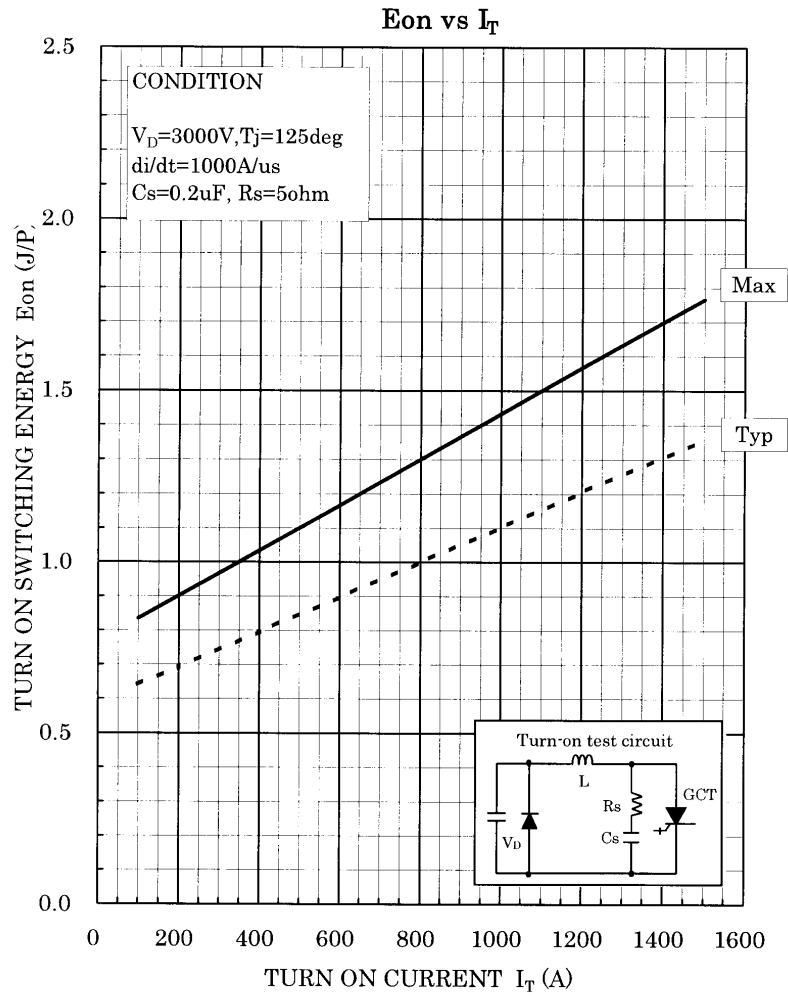
HIGH POWER INVERTER USE  
PRESS PACK TYPE

MAXIMUM ON STATE CHARACTERISTIC



MITSUBISHI  
ELECTRIC

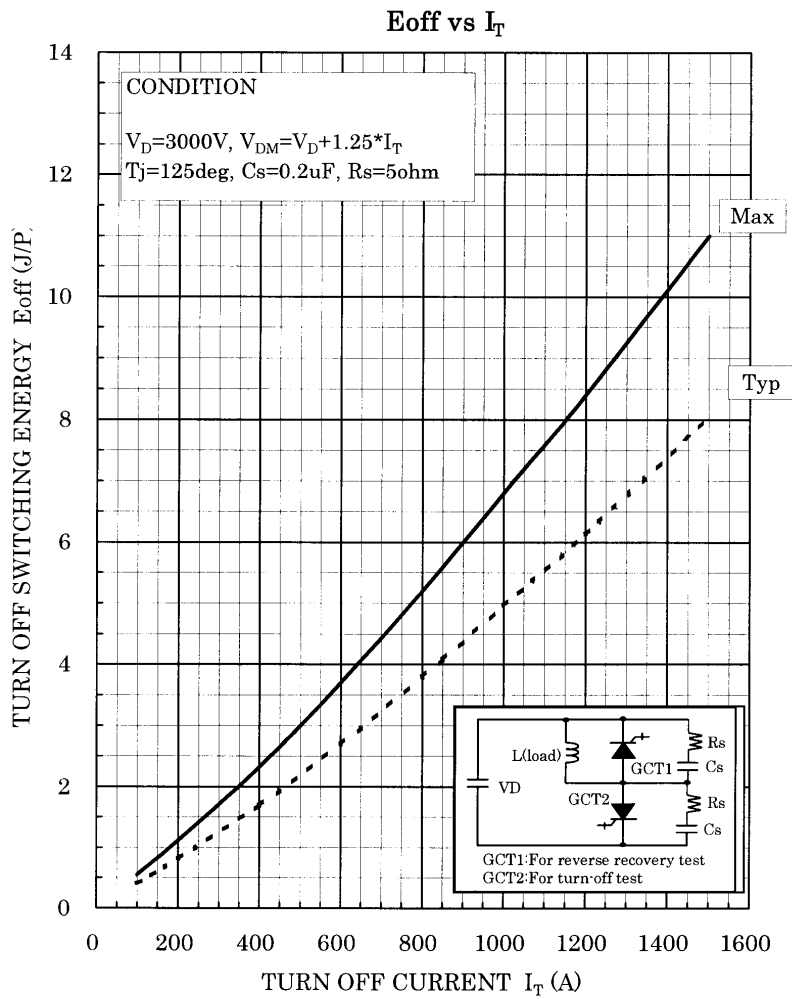
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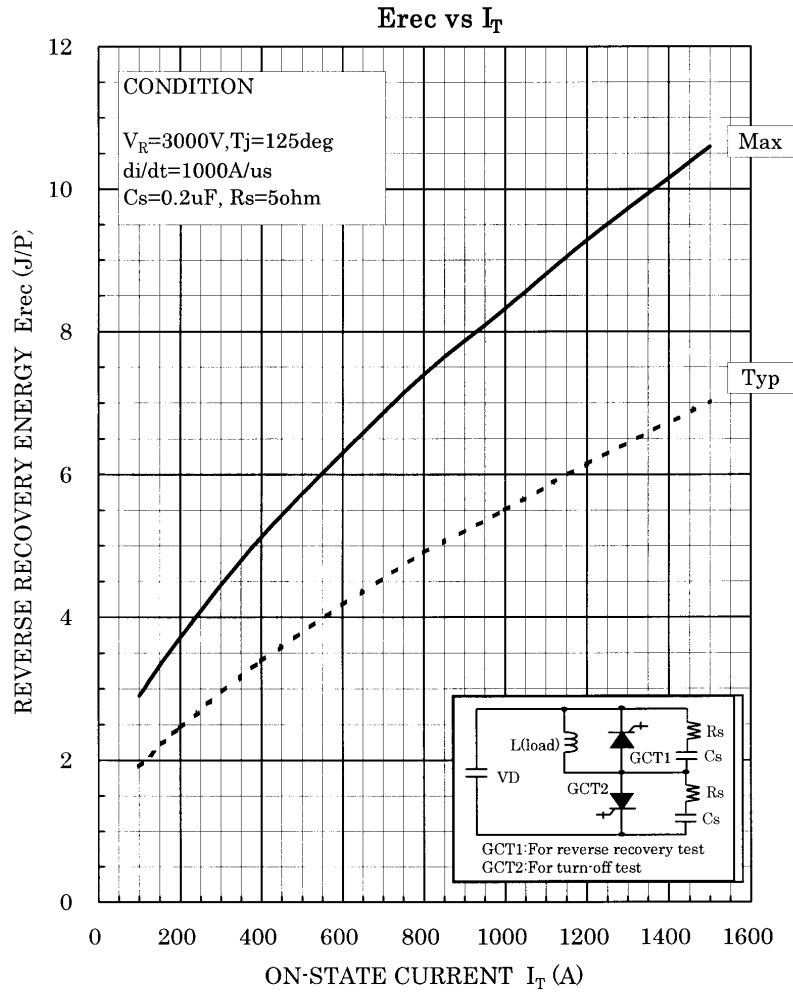
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