

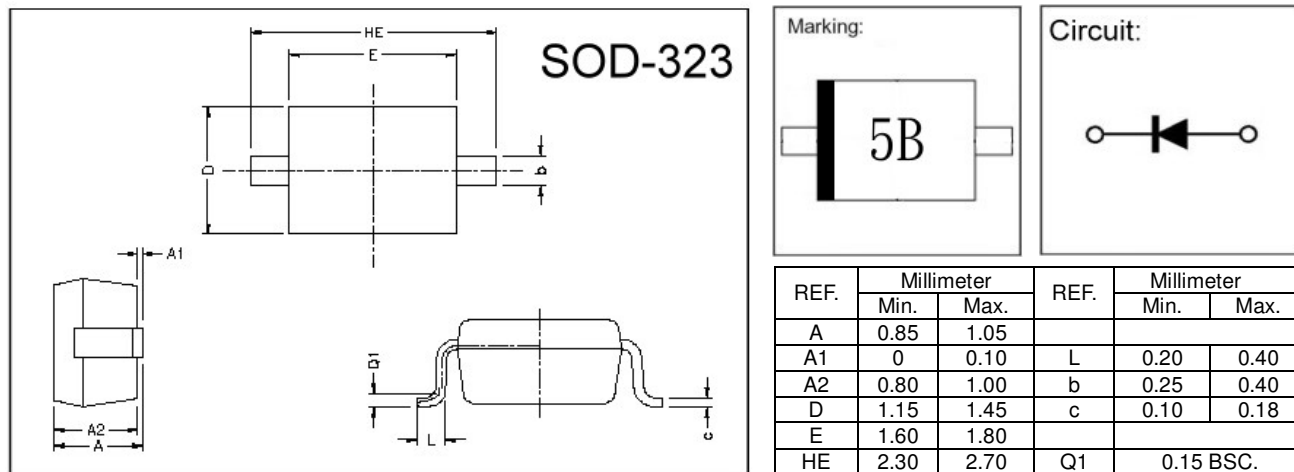
## GD501SD

**SURFACE MOUNT, SCHOTTKY BARRIER DIODE**  
**VOLTAGE 45V, CURRENT 0.1A**

### Description

The GD501SD is designed for low power rectification and high reliability.

### Package Dimensions



### Absolute Maximum Ratings at Ta = 25°C

| Parameter   | Symbol           | Ratings    | Unit |
|---|------------------|------------|------|
| Junction Temperature  | T <sub>j</sub>   | +125       | °C   |
| Storage Temperature   | T <sub>stg</sub> | -40 ~ +125 | °C   |
| Maximum Peak Repetitive Reverse Voltage                     | V <sub>RRM</sub> | 45         | V    |
| Maximum RMS Voltage   | V <sub>RMS</sub> | 32         | V    |
| Maximum DC Blocking Voltage                                 | V <sub>DC</sub>  | 40         | V    |
| Peak Forward Surge Current at 8.3mSec single half sine-wave | I <sub>FSM</sub> | 1.0        | A    |
| Typical Junction Capacitance between Terminal (Note 1)      | C <sub>J</sub>   | 6.0        | pF   |
| Maximum Average Forward Rectified Current                   | I <sub>o</sub>   | 0.1        | A    |
| Total Power Dissipation                                     | PD               | 225        | mW   |

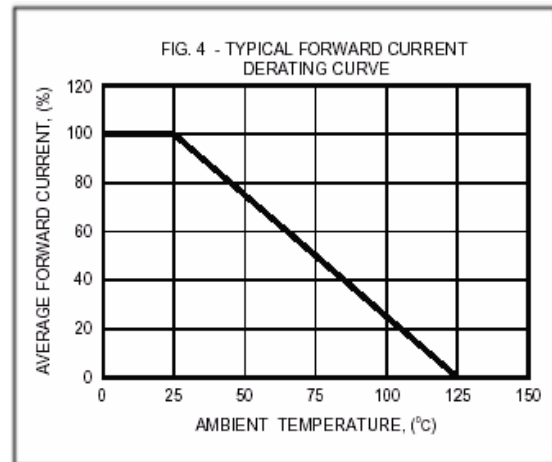
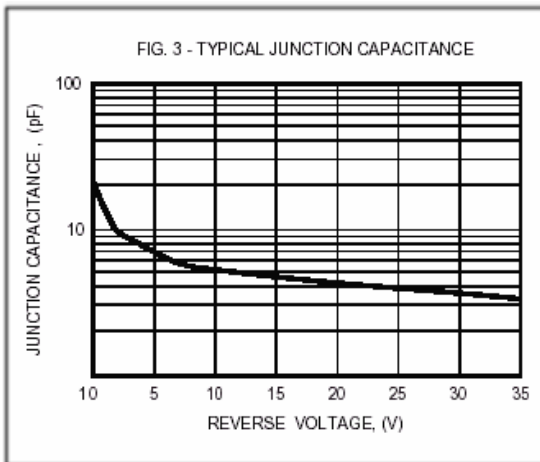
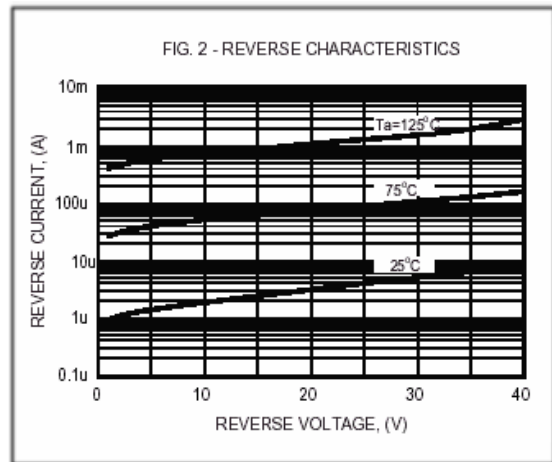
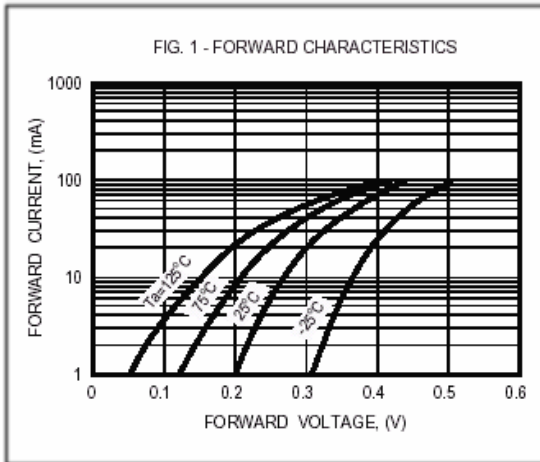
### Characteristics at Ta = 25°C

| Characteristics                       | Symbol            | Max  | Unit | Test Condition         |
|---------------------------------------|-------------------|------|------|------------------------|
| Maximum Instantaneous Forward Voltage | V <sub>F(1)</sub> | 0.55 | V    | I <sub>F</sub> = 100mA |
| Maximum Instantaneous Forward Voltage | V <sub>F(2)</sub> | 0.34 | V    | I <sub>F</sub> = 10 mA |
| Maximum Average Reverse Current       | I <sub>R</sub>    | 30.0 | uA   | V <sub>R</sub> = 10V   |

Notes: 1. Measured at 1.0 MHz and applied reverse voltage of 10.0 volt.

2. ESD sensitive product handling required.

## Characteristics Curve



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