



2 Watts Twin Output DC - DC Converter

FEATURES

- ♦ Efficiency up to 85%
- ♦ SIP Package
- ♦ No Heat Sink Required
- ♦ 1KVDC Isolation
- ♦ Industry Standard Pinout
- ♦ Internal SMD Construction
- ♦ No external Component Required

SPECIFICATIONS

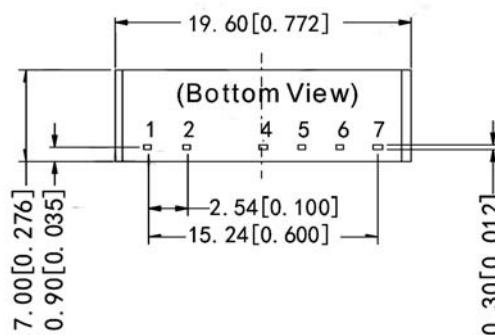
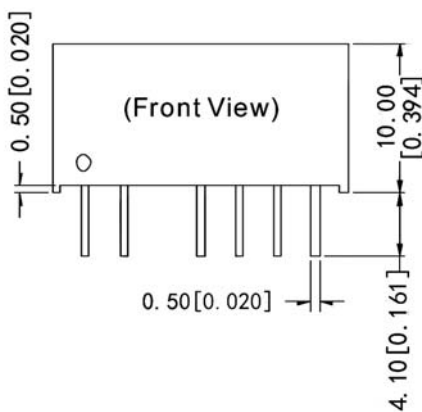
Input Voltage Range.....See model list
 Line Regulation¹..... ± 1.2% max.
 Load Regulation².....5V: 12.8%; 9V: 8.3%
 12V: 6.8%; 15V: 6.3% Typ.
 Temperature Drift, 100% load..... ± 0.03%/°C Max.
 Ripple & Noise (20MHz BW).....150mVp-p Max.
 Short Circuit Protection³.....1 second
 Isolation Voltage⁴.....1000VDC
 Isolation Resistance⁵.....1000MΩ Min.
 Switching Frequency.....100KHz Min.
 MTBF.....3500K hours
 Storage Humidity Range.....95% Max.
 Operating Temperature Range.....-40°C ~ +85°C
 Storage Temperature Range.....-55°C ~ +125°C
 Temperature Rise at Full Load.....25°C(Max)
 Lead Temperature⁶.....300°C
 Cooling.....Free Air Convection
 Case Material.....Plastic (UL94-V0)
 Weight.....2.8g

Note:

- (1) For Vin change of 1%
 (2) 10% to 100% Load
 (3) Supply vol. must be discontinued at the end of short circuit duration
 (4) Tested for 1 minute and 1mA max. (Vin/Vout & Vo1/Vo2)
 (5) Test at 500VDC (Vin/Vout & Vo1/Vo2)
 (6) 1.5mm from case for 10 seconds

Model Number	Input Voltage (VDC)		Output			Eff. % (Typ.)
			Vo1,Vo2 (VDC)	Io1, Io2 (mA)		
	Nominal	Range			Max	
GI02-04	5	4.5~5.5	5/5	200/200	20/20	80
GI02-04D	5	4.5~5.5	9/9	111/111	12/12	82
GI02-05	5	4.5~5.5	12/12	83/83	9/9	82
GI02-06	5	10.8~13.2	15/15	67/67	7/7	83
GI02-64	12	10.8~13.2	5/5	200/200	20/20	80
GI02-64D	12	10.8~13.2	9/9	111/111	12/12	83
GI02-65	12	10.8~13.2	12/12	83/83	9/9	85
GI02-66	12	21.6~26.4	15/15	67/67	7/7	83
GI02-74	24	21.6~26.4	5/5	200/200	20/20	81
GI02-74D	24	21.6~26.4	9/9	111/111	12/12	82
GI02-75	24	21.6~26.4	12/12	83/83	9/9	84
GI02-76	24	21.6~26.4	15/15	67/67	7/7	84

MECHANICAL DRAWING (Unit: mm)



Note:
 Unit: mm[inch]
 Pin section tolerances: ± 0.10mm[±0.004inch]
 General tolerances: ± 0.50mm[±0.020inch]

FOOTPRINT DETAILS	
Pin	Function
1	Vin
2	GND
4	0V1
5	+Vo1
6	0V2
7	+Vo2