

600V/100A

2-PACK IGBT MODULE (Half - Bridge)

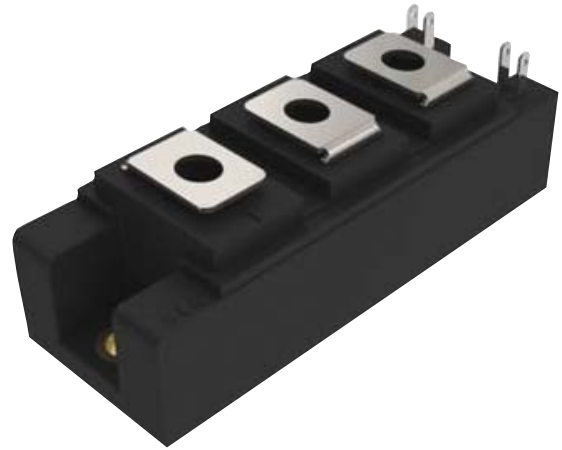
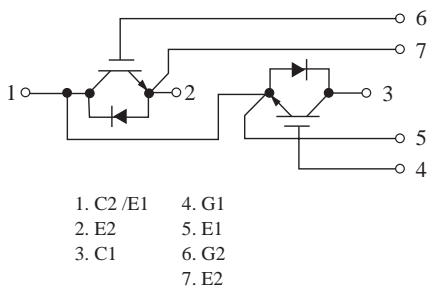
FEATURES

- Trench NPT Technology
- Low $V_{CE(sat)}$
- Low Turn-off loss
- Short tail current
- Positive temperature coefficient
- 10us Short Circuit Capability
- UL Recognized. File No. E305401

APPLICATION

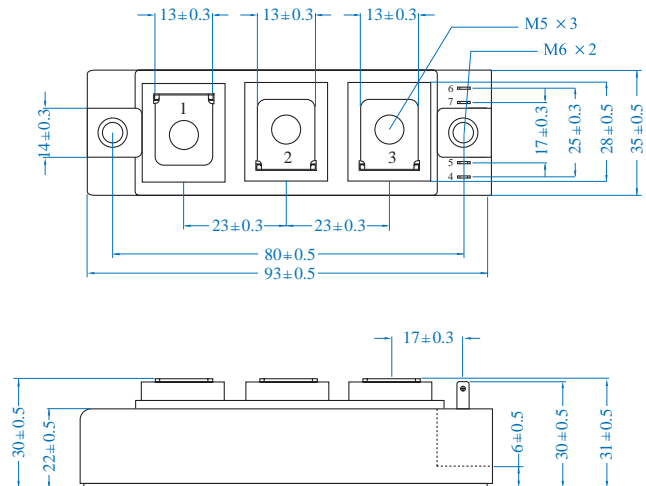
- Motor Controls
- General purpose inverters
- Servo Controls

INTERNAL CIRCUIT



OUTLINE DRAWING

Unit : mm



MAXIMUM RATING (Ta=25 °C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-to-Emitter Voltage	V_{CES}	600	V
Gate-Emitter Voltage	V_{GES}	± 20	V
Continuous Collector Current	I_C	@T _C =25	130
		@T _C =80	100
Pulsed Collector Current	I_{CP}	200	A
Power Dissipation	P_D	550	W
Diode Continuous Forward Current	I_F	@T _C =25	130
		@T _C =80	100
Isolation Voltage test	V_{iso}	2500	V
Junction Temperature	T_j	-40 ~ +150	
Storage Temperature	T_{stg}	-40 ~ +125	
Weight	Weight	190 ± 5	g
Mounting Torque (M6)	M	5	N.m
Terminal Connection torque (M5)	M	4	N.m

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ELECTRICAL CHARACTERISTICS (Ta=25)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Static							
Collector-Emitter Breakdown Voltage	BV_{CES}	$V_{GE}=0V, I_C=1mA$	600	-	-	V	
Collector Cut-off Current	I_{CES}	$V_{GE}=0V, V_{CE}=600V$	-	-	1.0	mA	
Gate Leakage Current	I_{GES}	$V_{CE}=0V, V_{GE}= \pm 20V$	-	-	± 100	nA	
Gate Threshold Voltage	$V_{GE(th)}$	$V_{GE}=V_{CE}, I_C=1mA$	4.5	-	7.0	V	
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE}=15V, I_C=100A$	-	2.1	2.5	V	
		$V_{GE}=15V, I_C=100A, T_C = 125$	-	2.2	-	V	
		$V_{GE}=15V, I_C=200A$	-	3.0	-	V	
Dynamic							
Total Gate Charge	Q_g	$V_{CE}=300V, V_{GE}= \pm 15V, I_C= 100A$	-	460	-	nC	
Gate-Emitter Charge	Q_{ge}		-	76	-	nC	
Gate-Collector Charge	Q_{gc}		-	235	-	nC	
Turn-On Delay Time	$t_{d(on)}$	$V_{CE}=300V, I_C=100A, V_{GE}= \pm 15V, R_G=10$ Inductive Load, $T_C = 25$	-	106	-	ns	
Rise Time	t_r		-	421	-	ns	
Turn-Off Delay Time	$t_{d(off)}$		-	282	-	ns	
Fall Time	t_f		-	112	-	ns	
Turn-On Switching Loss	E_{on}		-	3.33	-	mJ	
Turn-Off Switching Loss	E_{off}		-	3.0	-	mJ	
Total Switching Loss	E_{ts}		-	6.33	-	mJ	
Turn-On Delay Time	$t_{d(on)}$		$V_{CE}=300V, I_C=100A, V_{GE}= \pm 15V, R_G=10$ Inductive Load, $T_C = 125$	-	93	-	ns
Rise Time	t_r			-	251	-	ns
Turn-Off Delay Time	$t_{d(off)}$			-	309	-	ns
Fall Time	t_f	-		114	-	ns	
Turn-On Switching Loss	E_{on}	-		5.38	-	mJ	
Turn-Off Switching Loss	E_{off}	-		4.6	-	mJ	
Total Switching Loss	E_{ts}	-		9.98	-	mJ	
Input Capacitance	C_{ies}	$V_{CE}=30V, V_{GE}=0V, f=1MHz$	-	8540	-	pF	
Output Capacitance	C_{oes}		-	670	-	pF	
Reverse Transfer Capacitance	C_{res}		-	780	-	pF	
Short Circuit Current	I_{SC}	$V_{CC}=300V, V_{GE}= \pm 15V,$ $t_{psc} 10\mu s$	-	430	-	A	

THERMAL CHARACTERISTIC

CHARACTERISTIC	SYMBOL	Max	UNIT
Junction to Case (IGBT Part, Per 1/2 Module)	$R_{th(j-c)}$	0.45	/W
Junction to Case (Diode Part, Per 1/2 Module)	$R_{th(j-c)}$	0.50	

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ELECTRICAL CHARACTERISTIC OF DIODE

CHARACTERISTIC	SYMBOL	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Diode Forward Voltage	V_F	$I_F = 100A$	$T_C=25$	-	1.4	1.6	V
			$T_C=125$	-	1.4	-	
Diode Reverse Recovery Time	t_{rr}		$T_C=25$	-	266	315	ns
			$T_C=125$	-	300	-	
Diode Peak Reverse Recovery Current	I_{rr}	$I_F=100A,$ $R_G=10$	$T_C=25$	-	11	14	A
			$T_C=125$	-	20	-	
Diode Reverse Recovery Charge	Q_{rr}		$T_C=25$	-	1650	1980	nC
			$T_C=125$	-	4100	-	

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Fig 1. Saturation Voltage Characteristics

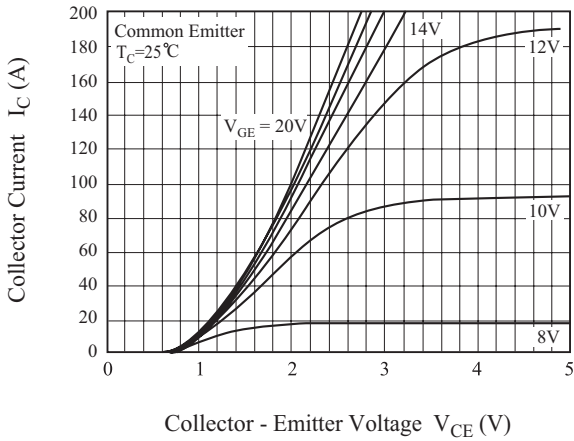


Fig 2. Saturation Voltage Characteristics

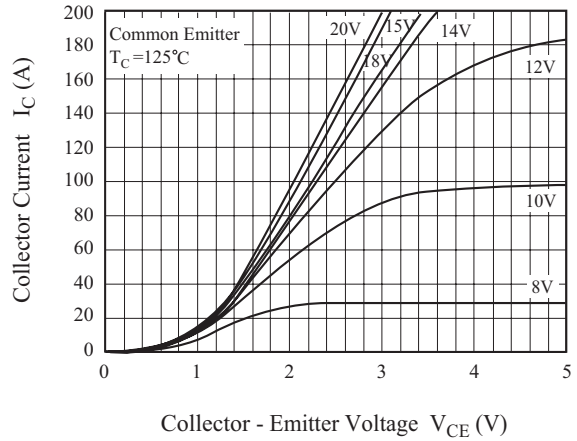


Fig 3. Saturation Voltage Characteristics

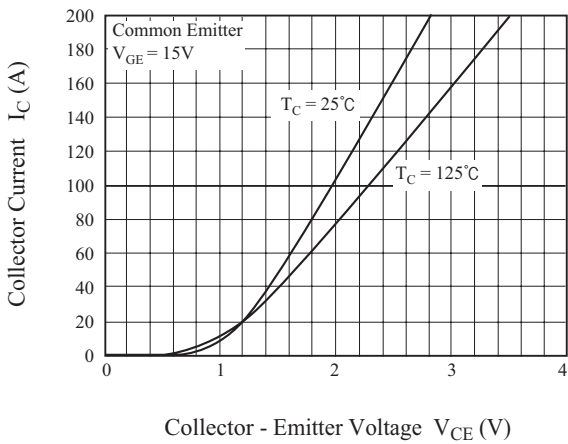


Fig 4. Saturation Voltage vs. V_{GE}

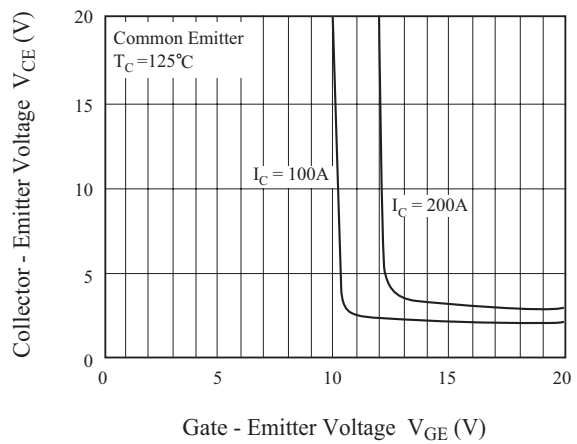


Fig 5. Capacitance Characteristics

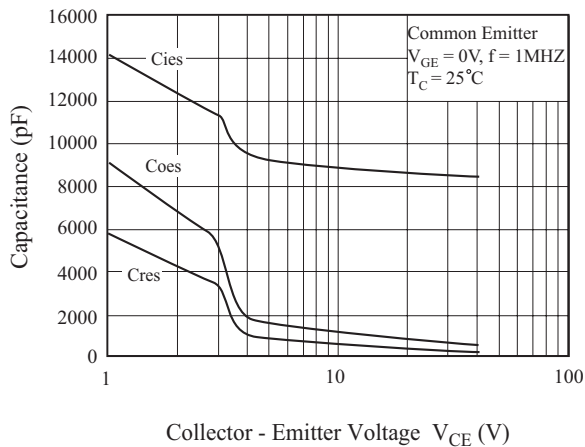
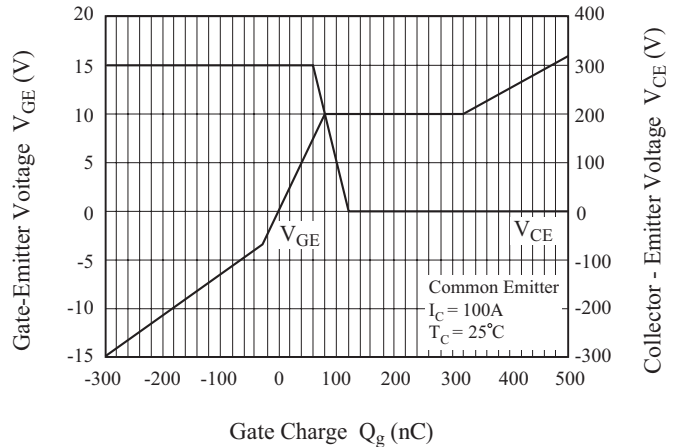


Fig 6. Gate Charge Characteristics



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Fig 7. Turn-On Characteristics vs. Gate Resistance

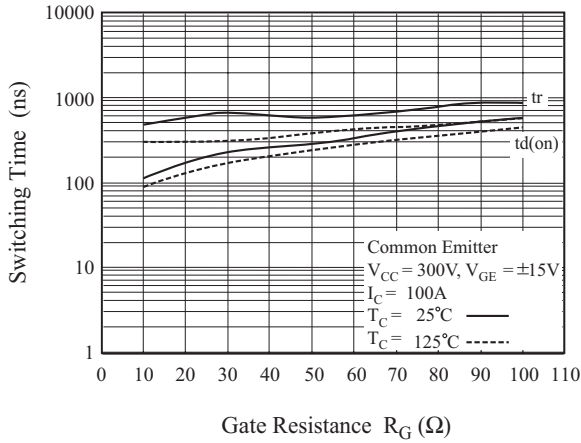


Fig 8. Turn-Off Characteristics vs. Gate Resistance

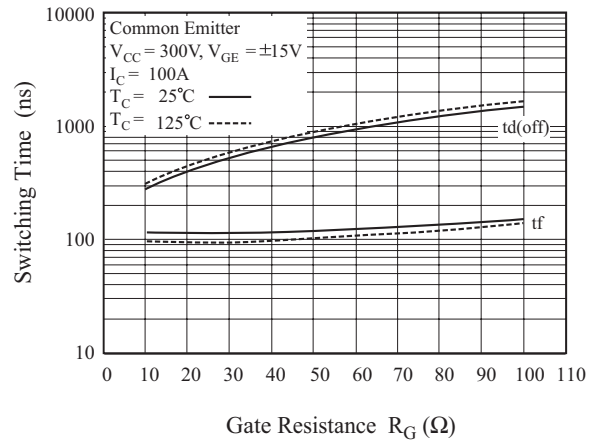


Fig 9. Switching Loss vs. Gate Resistance

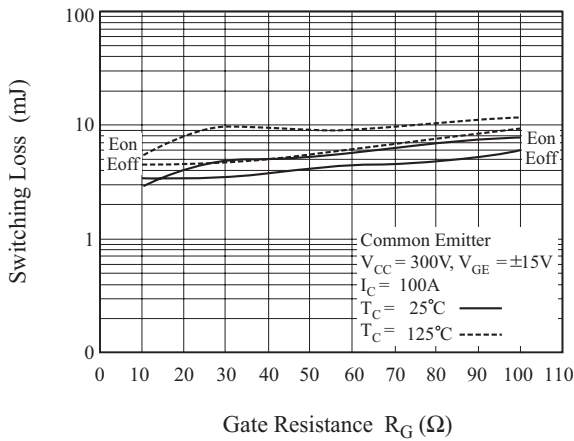


Fig 10. Turn-On Characteristics vs. Collector Current

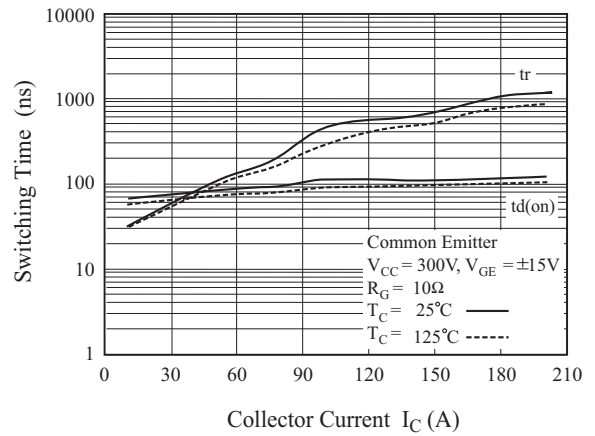


Fig 11. Turn-Off Characteristics vs. Collector Current

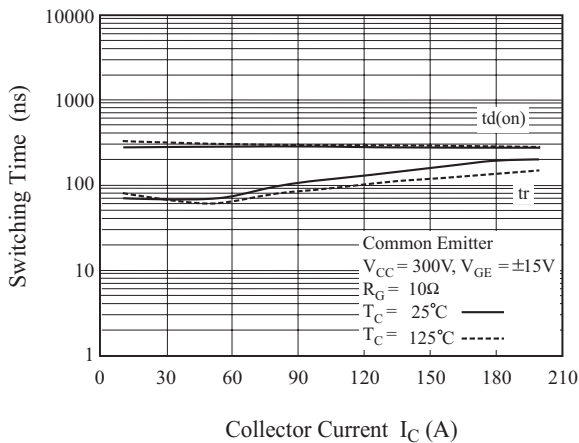
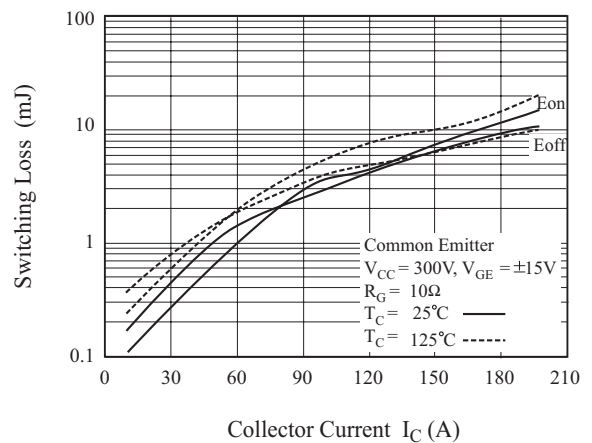


Fig 12. Switching Loss vs. Collector Current



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Fig 13. Forward Characteristics

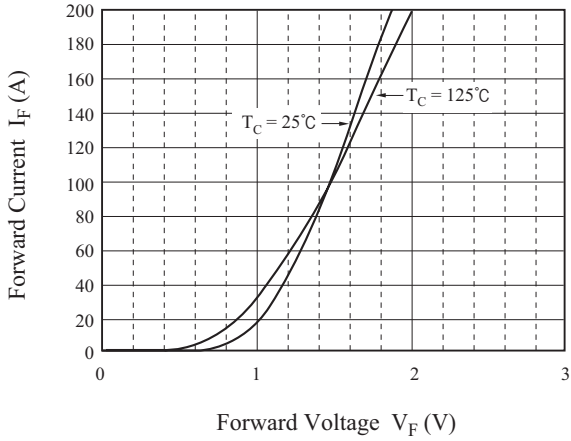


Fig 14. Reverse Recovery Current

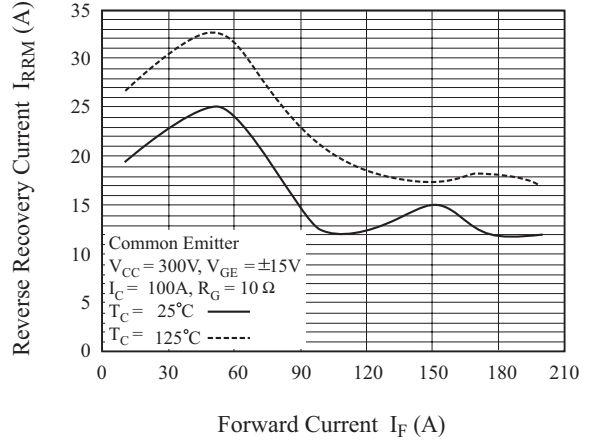


Fig 15. Reverse Recovery Time

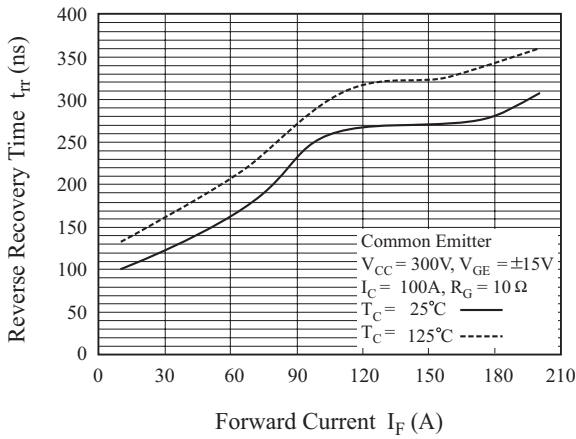


Fig 16. Reverse Bias Safe Operating Area (Max)

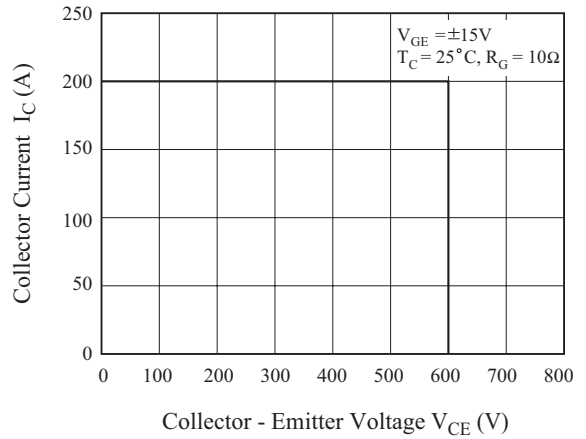


Fig17. Transient Thermal Response Curve

