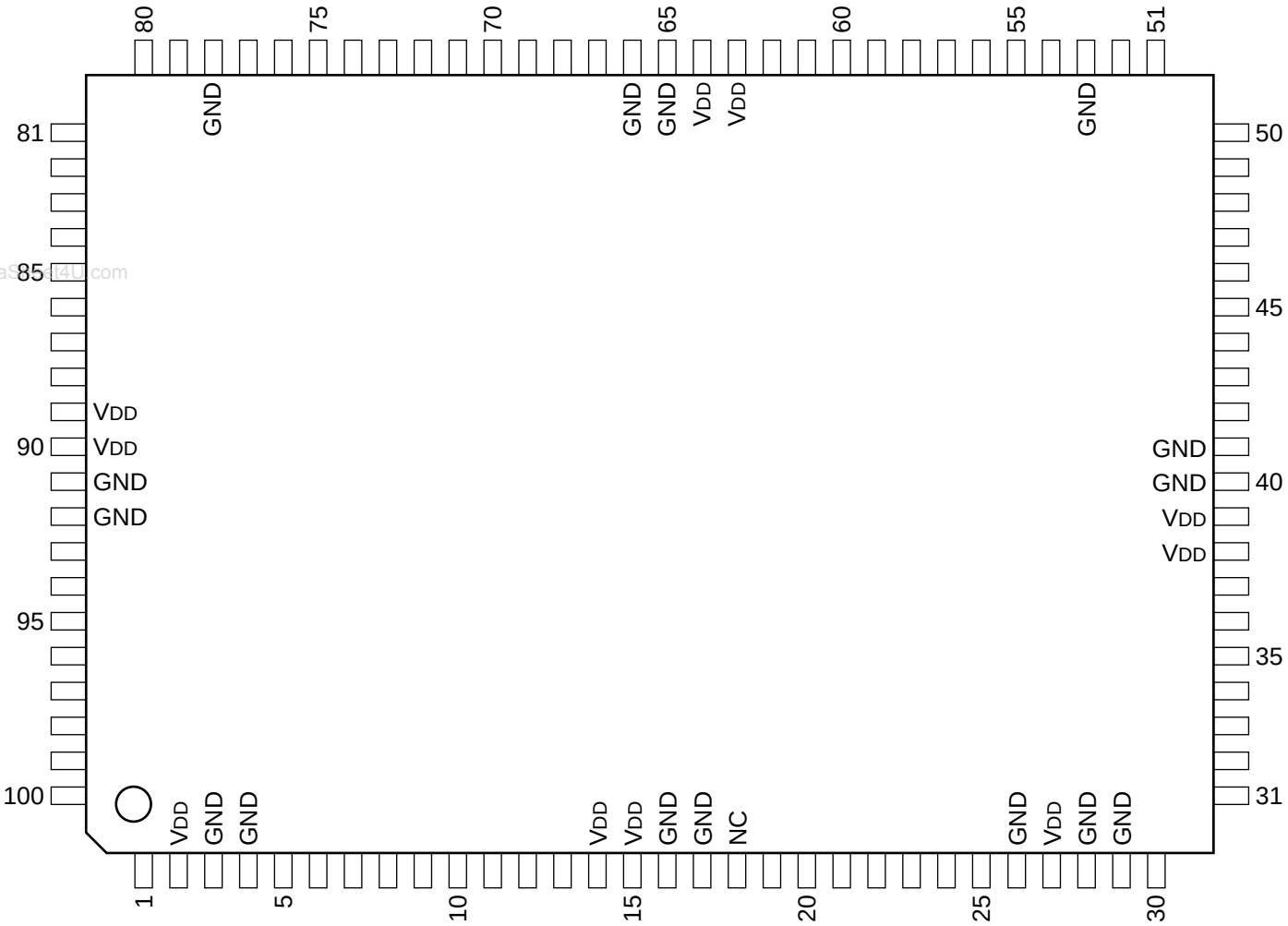
TWO-CHANNEL DIGITAL IMAGE SCALER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	YIN7	26	—	GND	51	O	UVOUT1	76	O	YOUT4
2	—	V_{DD}	27	—	V_{DD}	52	O	UVOUT2	77	O	YOUT3
3	—	GND	28	—	GND	53	—	GND	78	—	GND
4	—	GND	29	—	GND	54	O	UVOUT3	79	O	YOUT2
5	O	$\overline{YRL}$	30	I	UVIN7	55	O	UVOUT4	80	O	YOUT1
6	I/O	AD7	31	I	UVIN6	56	O	UVOUT5	81	O	YOUT0
7	I/O	AD6	32	I	UVIN5	57	O	UVOUT6	82	I	$\overline{YOE}$
8	I/O	AD5	33	I	UVIN4	58	O	UVOUT7	83	O	$\overline{YHF}$
9	I/O	AD4	34	I	UVIN3	59	O	GOUT0	84	O	$\overline{YEF}$
10	I/O	AD3	35	I	UVIN2	60	O	GOUT1	85	I	$\overline{YRD}$
11	I/O	AD2	36	I	$\overline{UVDV}$	61	O	GOUT2	86	I	RGB
12	I/O	AD1	37	O	$\overline{UVPREQ}$	62	I	CLK	87	I	$\overline{YFR}$
13	I/O	AD0	38	—	V_{DD}	63	—	V_{DD}	88	I	YIN0
14	—	V_{DD}	39	—	V_{DD}	64	—	V_{DD}	89	—	V_{DD}
15	—	V_{DD}	40	—	GND	65	—	GND	90	—	V_{DD}
16	—	GND	41	—	GND	66	—	GND	91	—	GND
17	—	GND	42	I	UVIN1	67	I	FCLK	92	—	GND
18	—	NC	43	I	UVIN0	68	O	GOUT3	93	O	$\overline{YPREQ}$
19	I	$\overline{CS}$	44	I	$\overline{UVFR}$	69	O	GOUT4	94	I	$\overline{YDV}$
20	I	$\overline{DS}$	45	I	$\overline{UVRD}$	70	O	GOUT5	95	I	YIN1
21	I	A/$\overline{D}$	46	O	$\overline{UVEF}$	71	O	GOUT6	96	I	YIN2
22	I	R/$\overline{W}$	47	O	$\overline{UVHF}$	72	O	GOUT7	97	I	YIN3
23	I	$\overline{RESET}$	48	I	$\overline{UVOE}$	73	O	YOUT7	98	I	YIN4
24	I	ODD	49	I	$\overline{GOE}$	74	O	YOUT6	99	I	YIN5
25	O	$\overline{UVRL}$	50	O	UVOUT0	75	O	YOUT5	100	I	YIN6

INPUT

A/$\overline{D}$	;	HOST INTERFACE ADDRESS/DATA SELECT (1 = SELECTS HOST INTERFACE ADDRESS POINTER) (0 = SELECTS HOST INTERFACE DATA REGISTERS)
CLK	;	MASTER CLOCK
$\overline{CS}$	;	HOST INTERFACE CHIP SELECT CONTROL
$\overline{DS}$	;	HOST INTERFACE DATA STROBE
FCLK	;	FIFO READ CLOCK
$\overline{GOE}$	;	OUTPUT ENABLE FOR GOUT DATA BUS
ODD	;	INDICATES THE ODD/EVEN FIELD STATUS (1 = ODD FIELD, 0 = EVEN FIELD)
$\overline{RESET}$	;	POWER-ON RESET
RGB	;	SELECTS RGB OUTPUT (1 = RGB OUTPUT DATA PRODUCED) (0 = OUTPUT DATA IN SAME COLOR SPACE AS INPUT)
R/$\overline{W}$	;	HOST INTERFACE READ/WRITE CONTROL (1 = SELECTS HOST INTERFACE READ) (0 = SELECTS HOST INTERFACE WRITE)
$\overline{UVDV}$	;	UV CHANNEL DATA VALID
$\overline{UVFR}$	;	UV CHANNEL FIELD/FRAME RESET
UVIN7 - UVIN0	;	UV CHANNEL DATA (UVIN0 = LSB)
$\overline{UVOE}$	;	OUTPUT ENABLE FOR UVOUT BUS
$\overline{UVRD}$	;	UV FIFO READ ENABLE
$\overline{YDV}$	;	Y CHANNEL DATA VALID
$\overline{YFR}$	;	Y CHANNEL FIELD/FRAME RESET
YIN7 - YIN0	;	Y CHANNEL DATA (YIN0 = LSB)
$\overline{YOE}$	;	OUTPUT ENABLE FOR YOUT BUS
$\overline{YRD}$	;	Y FIFO READ ENABLE

OUTPUT

GOUT7 - GOUT0	;	GREEN DATA (GOUT0 = LSB)
$\overline{UVEF}$	;	UV FIFO EMPTY FLAG
$\overline{UVHF}$	;	UV FIFO HALF FULL FLAG
UVOUT7 - UVOUT0	;	UV/BLUE DATA (UVOUT0 = LSB)
$\overline{UVPREQ}$	;	UV CHANNEL PIXEL REQUEST
$\overline{UVRL}$	;	UV CHANNEL REPEAT LINE REQUEST
$\overline{YEF}$	;	Y FIFO EMPTY FLAG
$\overline{YHF}$	;	Y FIFO HALF FULL FLAG
YOUT7 - YOUT0	;	Y/RED DATA (YOUT0 = LSB)
$\overline{YPREQ}$	;	Y CHANNEL PIXEL REQUEST
$\overline{YRL}$	;	Y CHANNEL REPEAT LINE REQUEST

INPUT/OUTPUT

AD7 - AD0	;	HOST INTERFACE ADDRESS/DATA BUS (AD0 = LSB)