GP400DDS12

Powerline N-Channel Dual Switch IGBT Module

DS5341-1.1 February 2000

The GP400DDS12 is a dual switch 1200V, robust n channel enhancement mode insulated gate bipolar transistor (IGBT) module. Designed for low power loss, the module is suitable for a variety of high voltage applications in motor drives and power conversion. The high impedance gate simplifies gate drive considerations enabling operation directly from low power control circuitry.

Fast switching times allow high frequency operation making the device suitable for the latest drive designs employing pwm and high frequency switching. The IGBT has a wide reverse bias safe operating area (RBSOA) for ultimate reliability in demanding applications.

These modules incorporate electrically isolated base plates and low inductance construction enabling circuit designers to optimise circuit layouts and utilise earthed heat sinks for safety.

The powerline range of high power modules includes dual and single switch configurations with a range of current and voltage capabilities to match customer system demands.

Typical applications include dc motor drives, ac pwm drives, main traction drives and auxiliaries, large ups systems and resonant inverters.

FEATURES

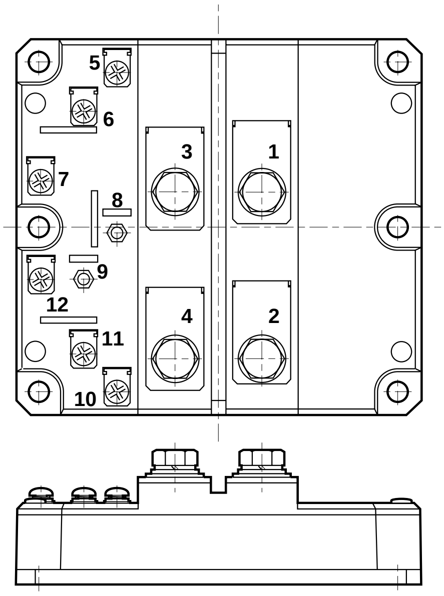
- n - Channel
- Enhancement Mode
- High Input Impedance
- Optimised For High Power High Frequency Operation
- Isolated Base
- Full 1200V Capability
- 400A Per Arm

APPLICATIONS

- High Power Switching
- Motor Control
- Inverters
- Traction Systems

KEY PARAMETERS

V_{CES}		1200V
$V_{CE(sat)}$	(typ)	2.7V
I_C	(max)	400A
$I_{C(PK)}$	(max)	800A



Outline type code: D

(See package details for further information)

Fig. 1 Electrical connections - (not to scale)

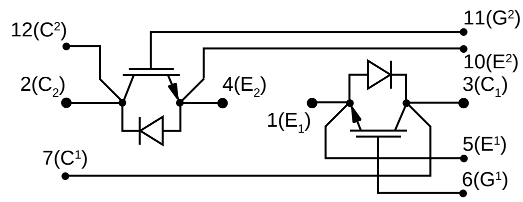


Fig.2 Dual switch circuit diagram

ORDERING INFORMATION

Order As: **GP400DDS12**

Note: When ordering, please use the whole part number.

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may inlcude potentially hazardous rupture of the package. Appropriate safety precautions should always be followed.

T_{case} = 25˚C unless stated otherwise.

Symbol	Parameter	Test Conditions	Max.	Units
V_{CES}	Collector-emitter voltage	V_{GE} = 0V	1200	V
V_{GES}	Gate-emitter voltage	-	±20	V
I_C	Collector current	DC, T_{case} = 25˚C	600	A
		DC, T_{case} = 75˚C	400	A
$I_{C(PK)}$		1ms, T_{case} = 75˚C	800	A
P_{max}	Maximum power dissipation	T_{case} = 25˚C (Transistor)	3750	W
V_{isol}	Isolation voltage	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	2500	V

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Conditions	Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance - transistor (per arm)	DC junction to case	-	35	°C/kW
$R_{th(j-c)}$	Thermal resistance - diode (per arm)	DC junction to case -	-	70	°C/kW
$R_{th(c-h)}$	Thermal resistance - Case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	8	°C/kW
T_j	Junction temperature	Transistor	-	150	°C
		Diode	-	125	°C
T_{stg}	Storage temperature range	-	–40	125	°C
-	Screw torque	Mounting - M6	-	5	Nm
		Electrical connections - M4	-	2	Nm
		Electrical connections - M8	-	10	Nm

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ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
I_{CES}	Collector cut-off current	V_{GE} = 0V, V_{CE} = V_{CES}	-	-	1	mA
		V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125°C	-	-	50	mA
I_{GES}	Gate leakage current	V_{GE} = ±20V, V_{CE} = 0V	-	-	±4	μA
$V_{GE(TH)}$	Gate threshold voltage	I_C = 120mA, V_{GE} = V_{CE}	4	-	7.5	V
$V_{CE(SAT)}$	Collector-emitter saturation voltage	V_{GE} = 15V, I_C = 400A	-	2.7	3.5	V
		V_{GE} = 15V, I_C = 400A, T_{case} = 125°C	-	3.2	4.0	V
I_F	Diode forward current	DC	-	-	400	A
I_{FM}	Diode maximum forward current	t_p = 1ms	-	-	800	A
V_F	Diode forward voltage	I_F = 400A	-	2.5	3.0	V
		I_F = 400A, T_{case} = 125°C	-	2.4	2.9	V
C_{ies}	Input capacitance	V_{CE} = 25V, V_{GE} = 0V, f = 1MHz	-	45	-	nF
L_M	Module inductance	-	-	20	-	nH

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INDUCTIVE SWITCHING CHARACTERISTICS

For definition of switching waveforms, refer to figure 3 and 4.

T_{case} = 25˚C unless stated otherwise

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
$t_{d(off)}$	Turn-off delay time	I_C = 400A V_{GE} = ±15V V_{CE} = 600V $R_{G(ON)}$ = $R_{G(OFF)}$ = 3.3Ω L ~ 100nH	-	1100	1300	ns
t_f	Fall time		-	150	200	ns
E_{OFF}	Turn-off energy loss		-	130	170	mJ
$t_{d(on)}$	Turn-on delay time		-	800	900	ns
t_r	Rise time		-	320	400	ns
E_{ON}	Turn-on energy loss		-	90	130	mJ
Q_{rr}	Diode reverse recovery charge	I_F = 400A V_R = 50%V_{CES}, dI_F/dt = 2000A/µs	-	30	50	µC

T_{case} = 125˚C unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
$t_{d(off)}$	Turn-off delay time	I_C = 400A V_{GE} = ±15V V_{CE} = 600V $R_{G(ON)}$ = $R_{G(OFF)}$ = 3.3Ω L ~ 100nH	-	1300	1500	ns
t_f	Fall time		-	200	250	ns
E_{OFF}	Turn-off energy loss		-	170	250	mJ
$t_{d(on)}$	Turn-on delay time		-	950	1200	ns
t_r	Rise time		-	350	450	ns
E_{ON}	Turn-on energy loss		-	150	200	mJ
Q_{rr}	Diode reverse recovery charge	I_F = 400A V_R = 50%V_{CES}, dI_F/dt = 2000A/µs	-	50	70	µC

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

SWITCHING DEFINITIONS

$$E_{on} = \int_{t_1}^{t_4 + 5\mu s} V_{ce}.I_c dt$$

$$t_{d(on)} = t_2 - t_1$$

$$t_r = t_3 - t_2$$

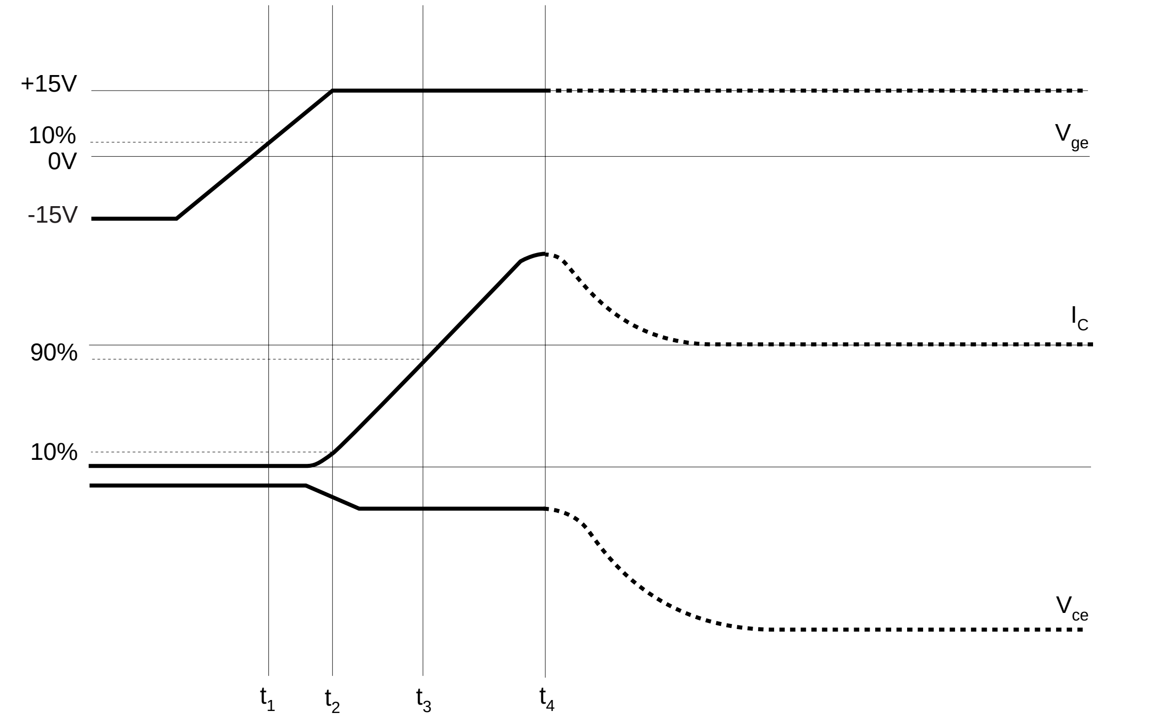

Fig.3 Definition of turn-on switching times

$$E_{off} = \int_{t_5}^{t_7 + 5\mu s} V_{ce}.I_c dt$$

$$t_{d(off)} = t_6 - t_5$$

$$t_f = t_7 - t_6$$

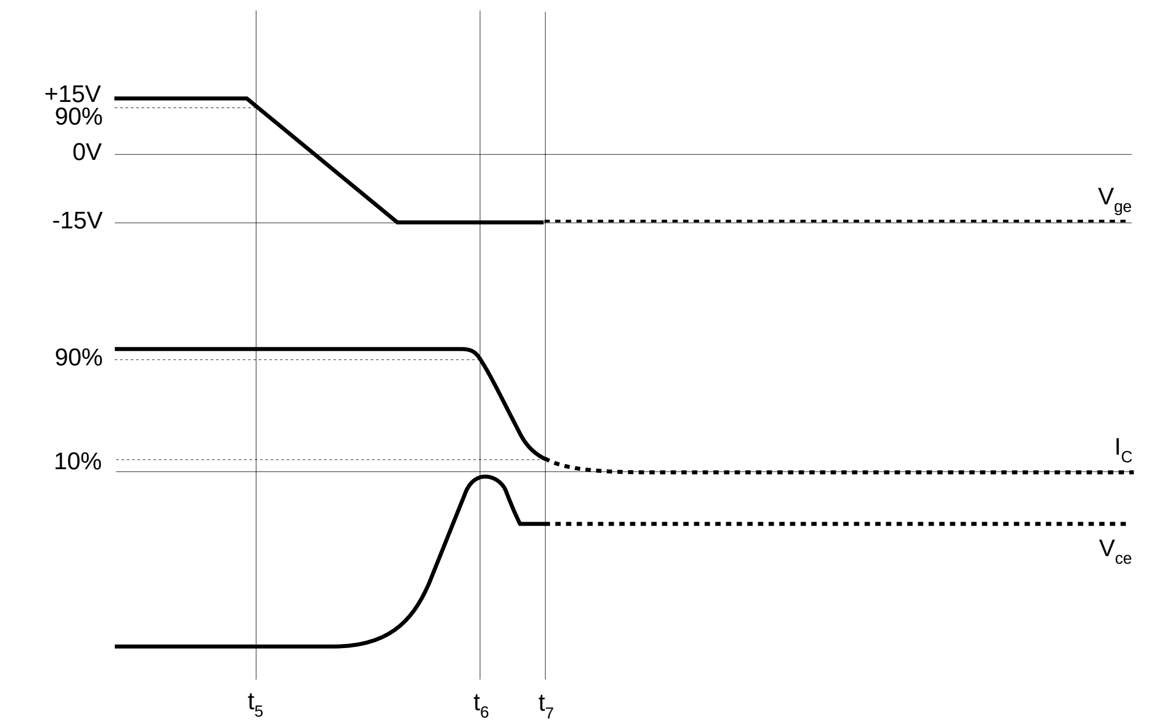

Fig.4 Definition of turn-off switching times

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

CURVES

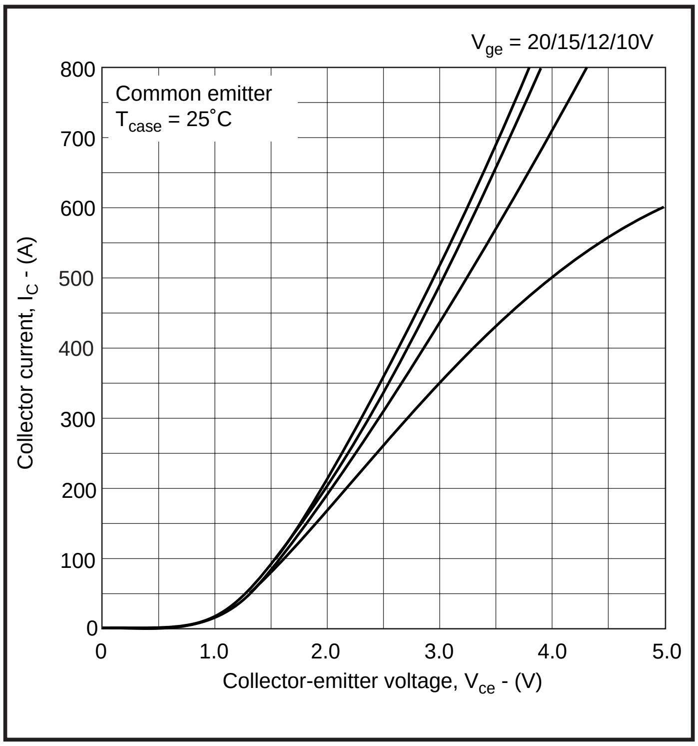

Fig.5 Typical output characteristics

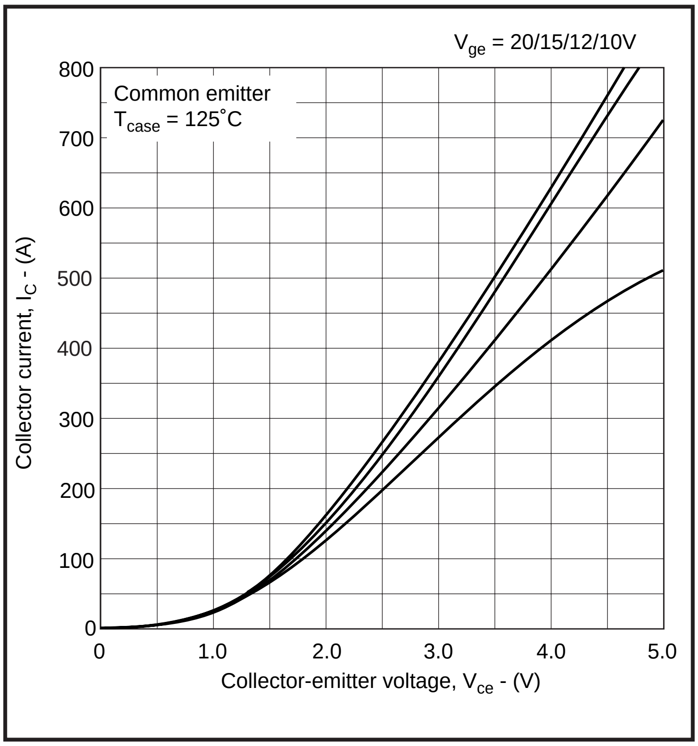

Fig.6 Typical output characteristics

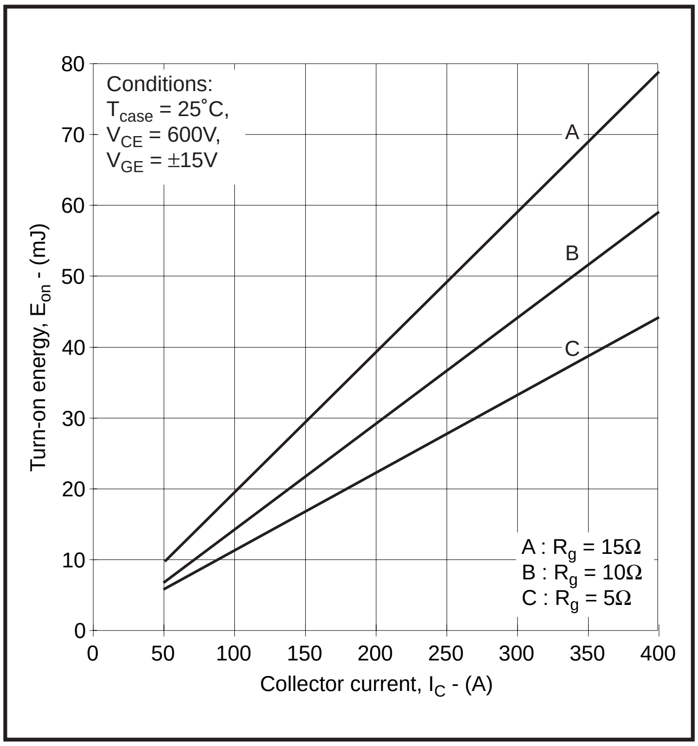

Fig.7 Typical turn-on energy vs collector current

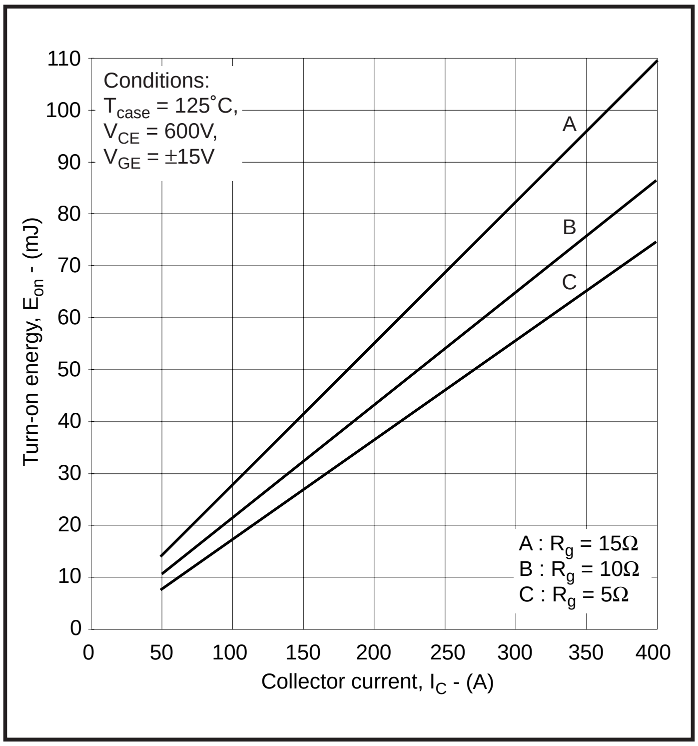

Fig.8 Typical turn-on energy vs collector current

Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures.

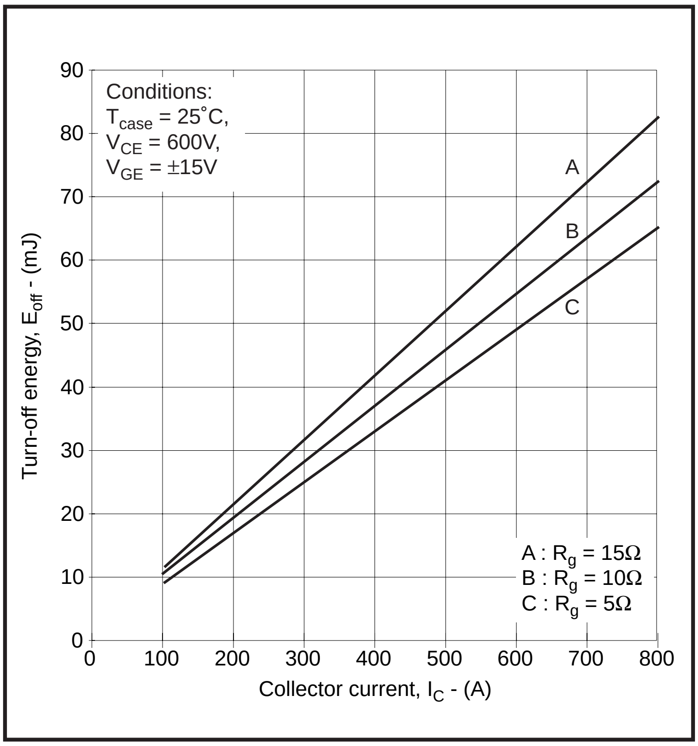

Fig.9 Typical turn-off energy vs collector current

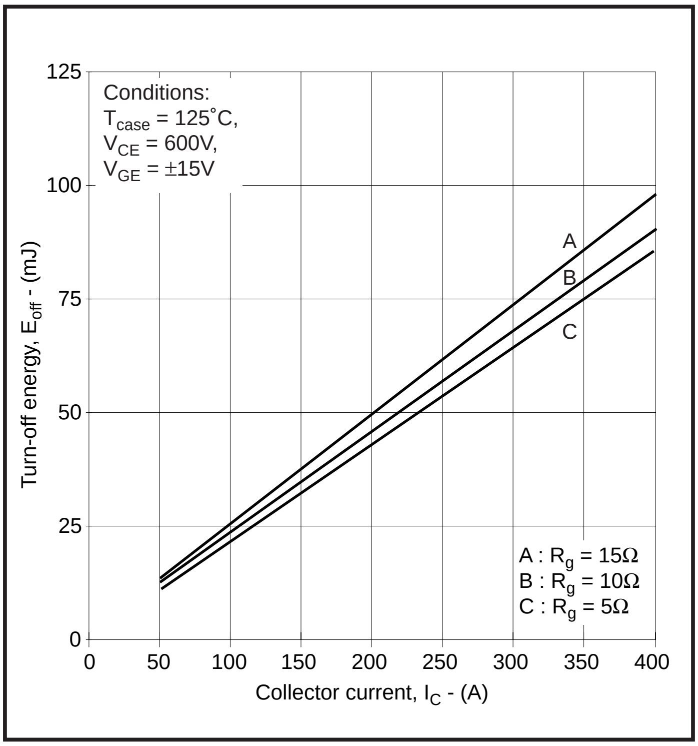

Fig.10 Typical turn-off energy vs collector current

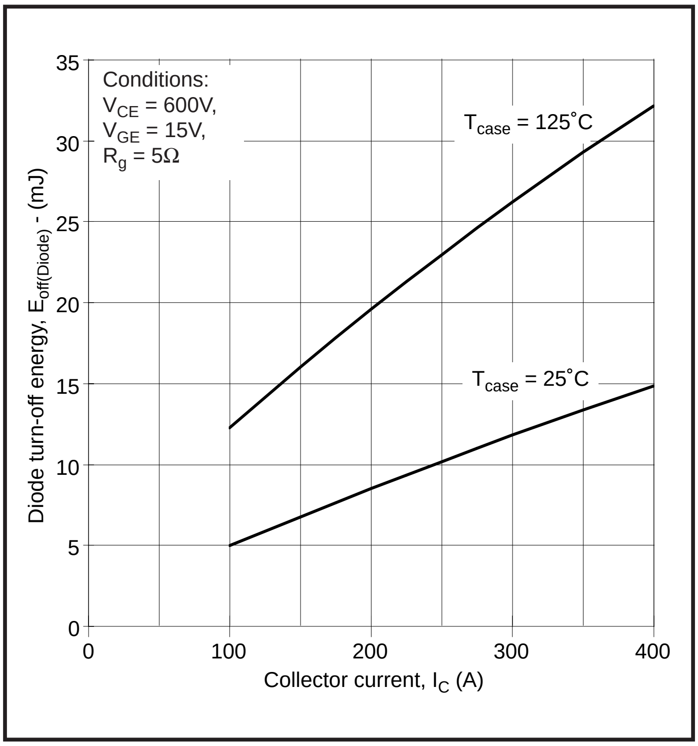

Fig.11 Typical diode reverse recovery charge vs collector current

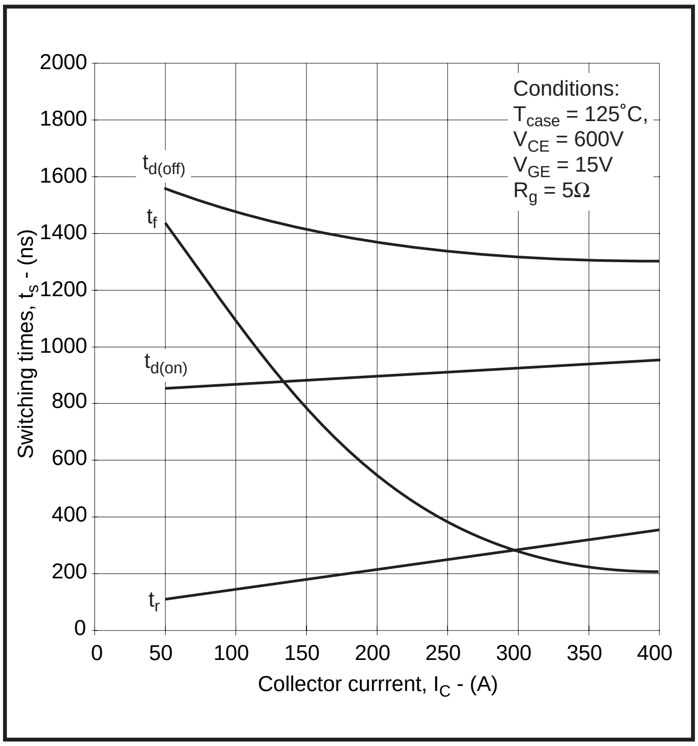

Fig.12 Typical switching characteristics

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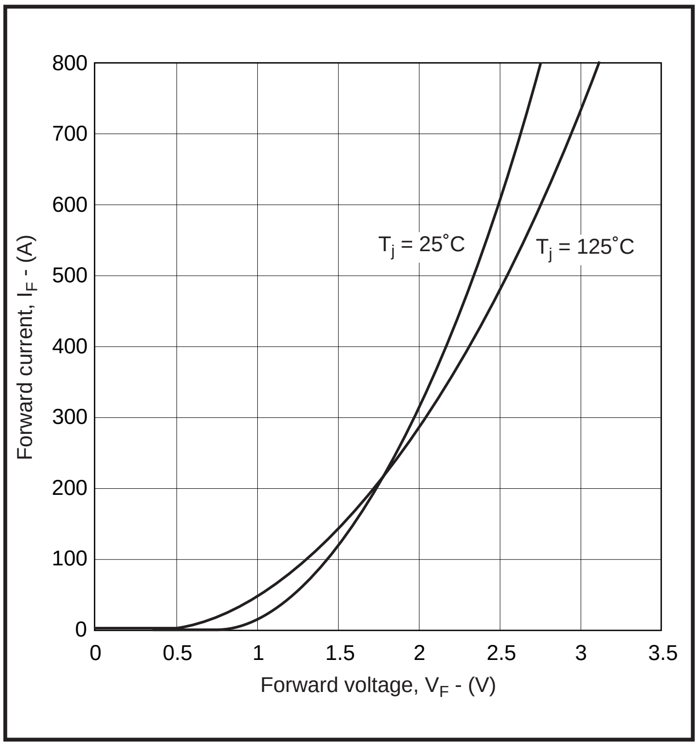

Fig.13 Diode typical forward characteristics

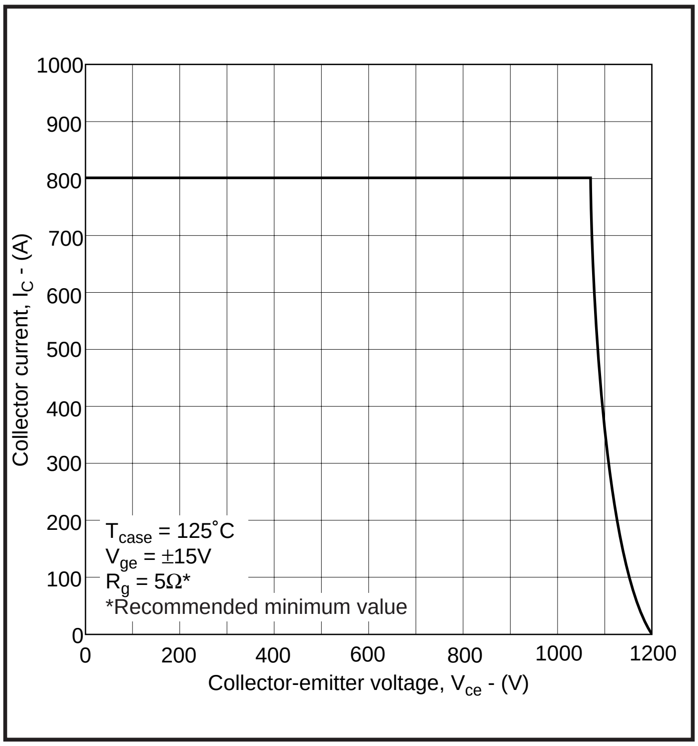

Fig.14 Reverse bias safe operating area

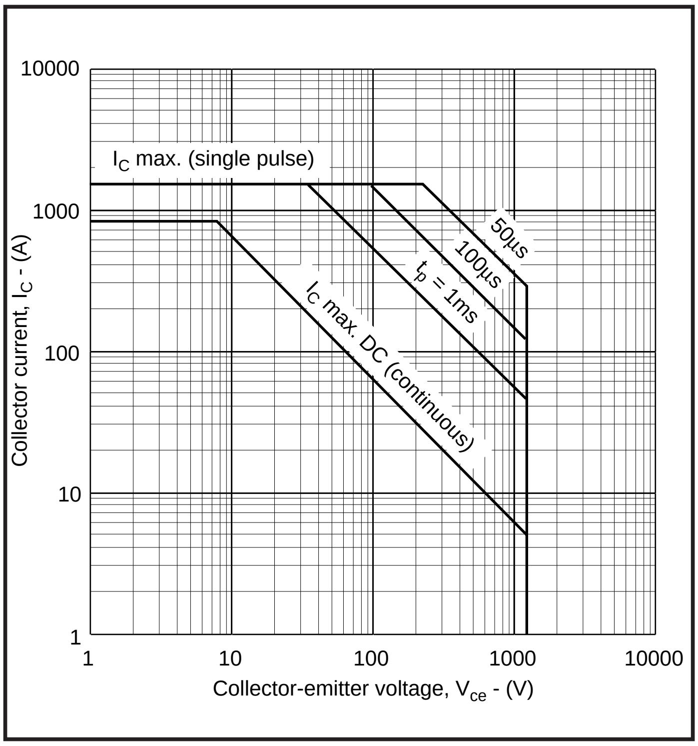

Fig.15 Forward bias safe operating area

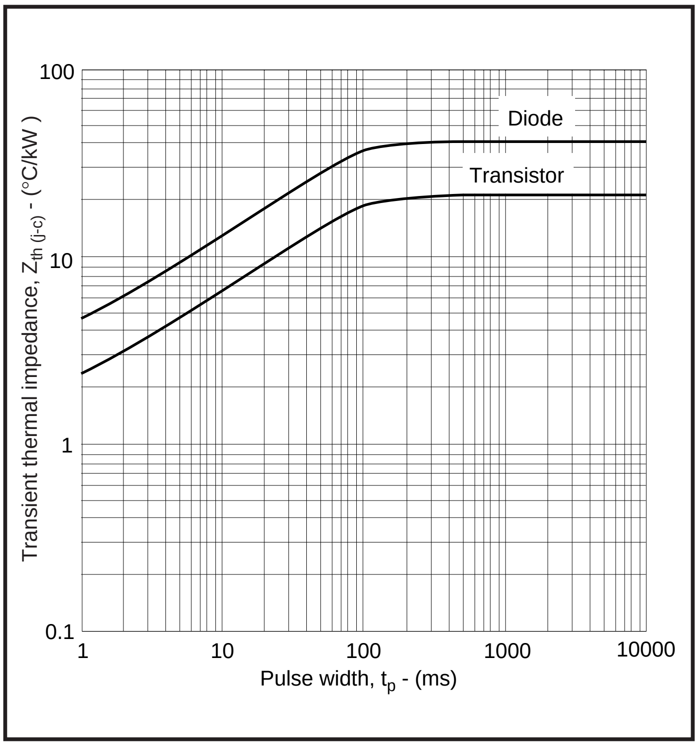

Fig.16 Transient thermal impedance

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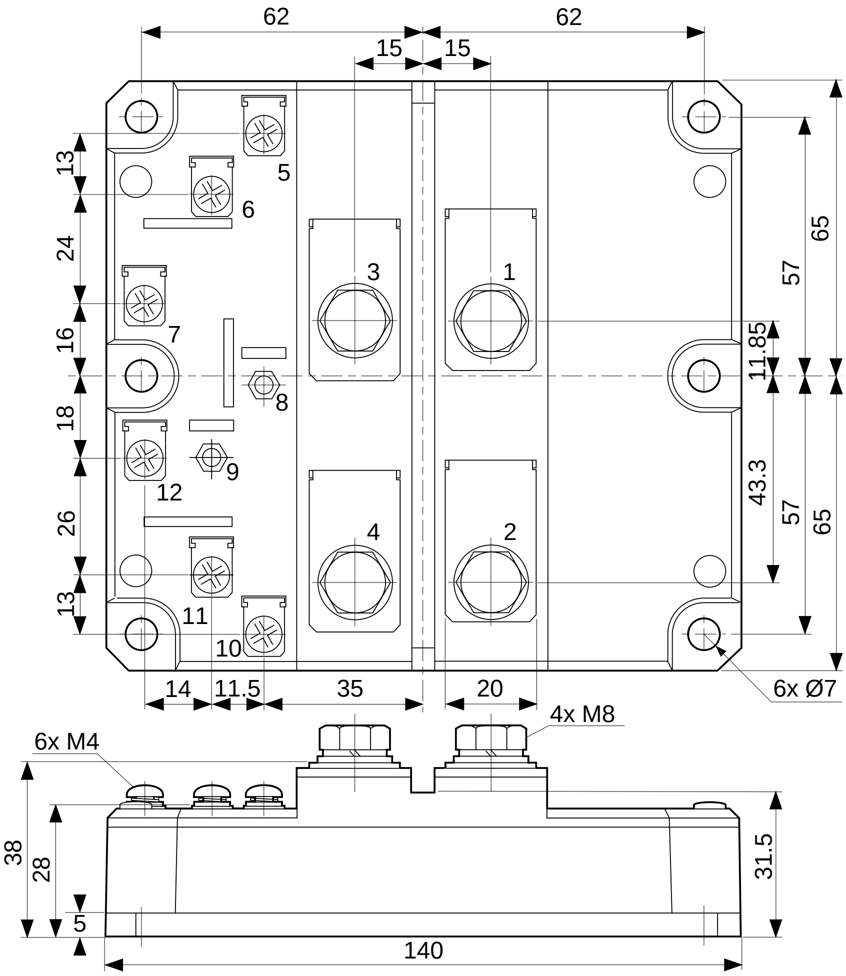
PACKAGE DETAILS

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Nominal weight: 1600g

Module outline type code: D

ASSOCIATED PUBLICATIONS

Title	Application Note Number
Electrostatic handling precautions	AN4502
An introduction to IGBTs	AN4503
IGBT ratings and characteristics	AN4504
Heatsink requirements for IGBT modules	AN4505
Calculating the junction temperature of power semiconductors	AN4506
Gate drive considerations to maximise IGBT efficiency	AN4507
Parallel operation of IGBTs – punch through vs non-punch through characteristics	AN4508
Guidance notes for formulating technical enquiries	AN4869
Principle of rating parallel connected IGBT modules	AN5000
Short circuit withstand capability in IGBTs	AN5167
Driving high power IGBTs with concept gate drivers	AN5190

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POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the up to date CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete solution (PACs).

HEATSINKS

Power Assembly has it's own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance or our semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest Sales Representative or the factory.

DYNEX SEMICONDUCTOR

http://www.dynexsemi.com

e-mail: power_solutions@dynexsemi.com

HEADQUARTERS OPERATIONS
DYNEX SEMICONDUCTOR LTD
Doddington Road, Lincoln.
Lincolnshire. LN6 3LF. United Kingdom.
Tel: 00-44-(0)1522-500500
Fax: 00-44-(0)1522-500550

DYNEX POWER INC.
Unit 7 - 58 Antares Drive,
Nepean, Ontario, Canada K2E 7W6.
Tel: 613.723.7035
Fax: 613.723.1518
Toll Free: 1.888.33.DYNEX (39639)

CUSTOMER SERVICE CENTRES
France, Benelux, Italy and Spain Tel: +33 (0)1 69 18 90 00. Fax: +33 (0)1 64 46 54 50
North America Tel: 011-800-5554-5554. Fax: 011-800-5444-5444
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SALES OFFICES
France, Benelux, Italy and Spain Tel: +33 (0)1 69 18 90 00. Fax: +33 (0)1 64 46 54 50
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UK, Germany, Scandinavia & Rest Of World Tel: +44 (0)1522 500500. Fax: +44 (0)1522 500020
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Target Information: This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand.

No Annotation: The product parameters are fixed and the product is available to datasheet specification.

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