

#### FEATURES

- Non Punch Through Silicon
- Isolated Copper Baseplate
- Low Inductance Internal Construction

#### APPLICATIONS

- High Power Inverters
- Motor Controllers
- Induction Heating
- Resonant Converters

The Powerline range of high power modules includes dual and single switch configurations covering voltages from 600V to 3300V and currents up to 4800A.

The GP400DDS18 is a dual switch 1800V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) ensuring reliability in demanding applications.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise earthed heat sinks for safety.

#### ORDERING INFORMATION

Order As:

**GP400DDS18**

Note: When ordering, please use the whole part number.

#### KEY PARAMETERS

$V_{CES}$		<b>1800V</b>
$V_{CE(sat)}$	(typ)	<b>3.5V</b>
$I_C$	(max)	<b>400A</b>
$I_{C(PK)}$	(max)	<b>1600A</b>

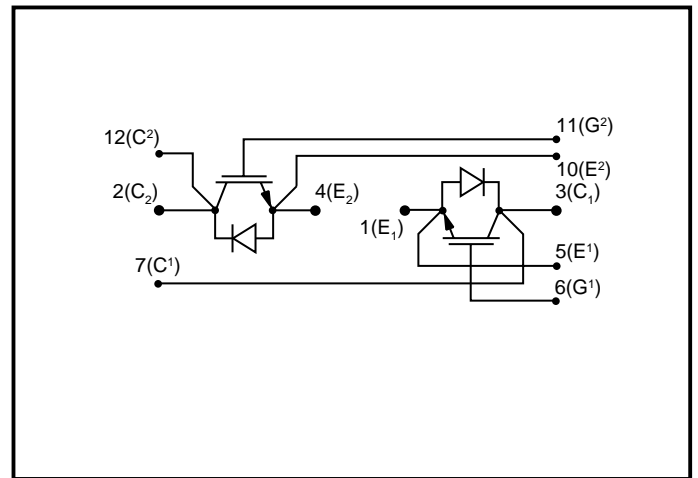


Fig. 1 Dual switch circuit diagram

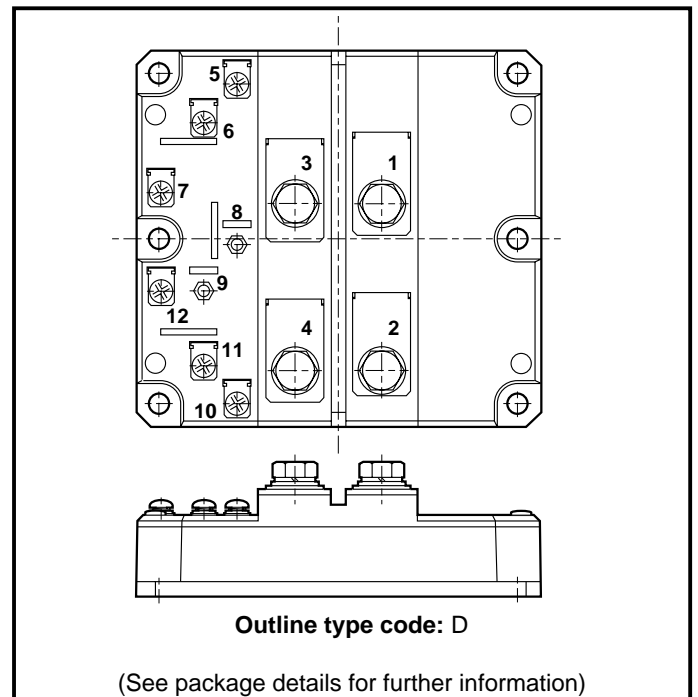


Fig. 2 Electrical connections - (not to scale)

## ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

$T_{case} = 25^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
$V_{CES}$	Collector-emitter voltage	$V_{GE} = 0\text{V}$	1800	V
$V_{GES}$	Gate-emitter voltage	-	$\pm 20$	V
$I_C$	Continuous collector current	$T_{case} = 65^{\circ}\text{C}$	400	A
$I_{C(PK)}$	Peak collector current	1ms, $T_{case} = 105^{\circ}\text{C}$	800	A
$P_{max}$	Max. transistor power dissipation	$T_{case} = 25^{\circ}\text{C}$ , $T_j = 150^{\circ}\text{C}$	3000	W
$V_{isol}$	Isolation voltage	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V

## THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance - transistor (per arm)	Continuous dissipation - junction to case	-	42	$^{\circ}\text{C}/\text{kW}$
$R_{th(j-c)}$	Thermal resistance - diode (per arm)	Continuous dissipation - junction to case	-	80	$^{\circ}\text{C}/\text{kW}$
$R_{th(c-h)}$	Thermal resistance - case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	8	$^{\circ}\text{C}/\text{kW}$
$T_j$	Junction temperature	Transistor	-	150	$^{\circ}\text{C}$
		Diode	-	125	$^{\circ}\text{C}$
$T_{stg}$	Storage temperature range	-	-40	125	$^{\circ}\text{C}$
-	Screw torque	Mounting - M6	-	5	Nm
		Electrical connections - M4	-	2	Nm
		Electrical connections - M8	-	10	Nm

**ELECTRICAL CHARACTERISTICS**
**T<sub>case</sub> = 25°C unless stated otherwise.**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I <sub>CES</sub>	Collector cut-off current	V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub>	-	-	1	mA
		V <sub>GE</sub> = 0V, V <sub>CE</sub> = V <sub>CES</sub> , T <sub>case</sub> = 125°C	-	-	12	mA
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = ±20V, V <sub>CE</sub> = 0V	-	-	2	μA
V <sub>GE(TH)</sub>	Gate threshold voltage	I <sub>C</sub> = 20mA, V <sub>GE</sub> = V <sub>CE</sub>	4.5	5.5	6.5	V
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 400A	-	3.5	4	V
		V <sub>GE</sub> = 15V, I <sub>C</sub> = 400A, T <sub>case</sub> = 125°C	-	4.3	5	V
I <sub>F</sub>	Diode forward current	DC	-	400	-	A
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms	-	800	-	A
V <sub>F</sub>	Diode forward voltage	I <sub>F</sub> = 400A	-	2.2	2.5	V
		I <sub>F</sub> = 400A, T <sub>case</sub> = 125°C	-	2.3	2.6	V
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz	-	45	-	nF
L <sub>M</sub>	Module inductance	-	-	20	-	nH

**ELECTRICAL CHARACTERISTICS**

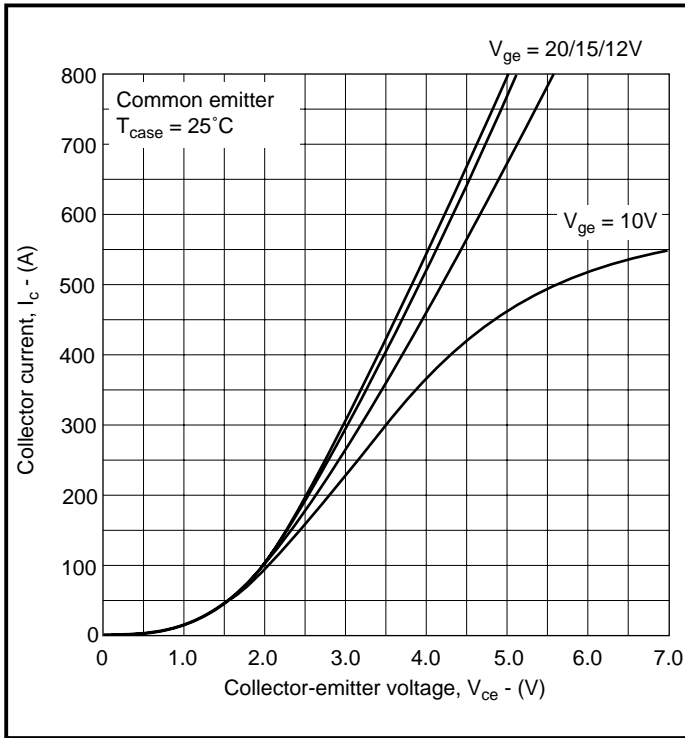
T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
t <sub>d(off)</sub>	Turn-off delay time	$I_C = 400A$ $V_{GE} = \pm 15V$ $V_{CE} = 900V$ $R_{G(ON)} = R_{G(OFF)} = 2.2\Omega$ $L \sim 100nH$	-	900	1100	ns
t <sub>f</sub>	Fall time		-	280	350	ns
E <sub>OFF</sub>	Turn-off energy loss		-	80	100	mJ
t <sub>d(on)</sub>	Turn-on delay time		-	500	650	ns
t <sub>r</sub>	Rise time		-	200	400	ns
E <sub>ON</sub>	Turn-on energy loss		-	140	180	mJ
Q <sub>rr</sub>	Diode reverse recovery charge	$I_F = 400A, V_R = 50\% V_{CES}$ $di_F/dt = 3000A/\mu s$	-	80	100	μC
I <sub>rr</sub>	Diode reverse current		-	250	-	A
E <sub>REC</sub>	Diode reverse recovery energy		-	70	-	mJ

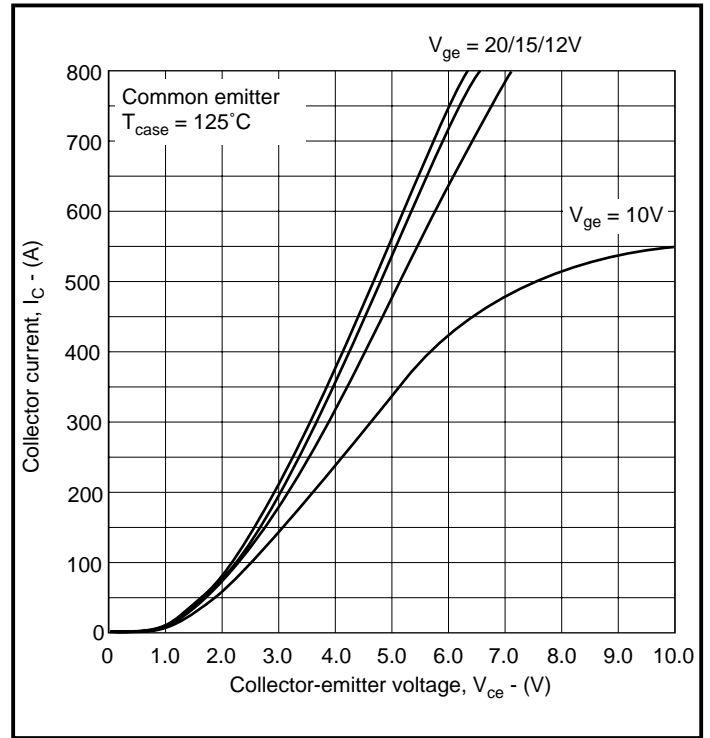
T<sub>case</sub> = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
t <sub>d(off)</sub>	Turn-off delay time	$I_C = 400A$ $V_{GE} = \pm 15V$ $V_{CE} = 900V$ $R_{G(ON)} = R_{G(OFF)} = 2.2\Omega$ $L \sim 100nH$	-	1010	1200	ns
t <sub>f</sub>	Fall time		-	390	500	ns
E <sub>OFF</sub>	Turn-off energy loss		-	100	150	mJ
t <sub>d(on)</sub>	Turn-on delay time		-	660	800	ns
t <sub>r</sub>	Rise time		-	310	400	ns
E <sub>ON</sub>	Turn-on energy loss		-	200	270	mJ
Q <sub>rr</sub>	Diode reverse recovery charge	$I_F = 400A, V_R = 50\% V_{CES}$ $di_F/dt = 2500A/\mu s$	-	110	150	μC
I <sub>rr</sub>	Diode reverse current		-	300	-	A
E <sub>REC</sub>	Diode reverse recovery energy		-	70	-	mJ

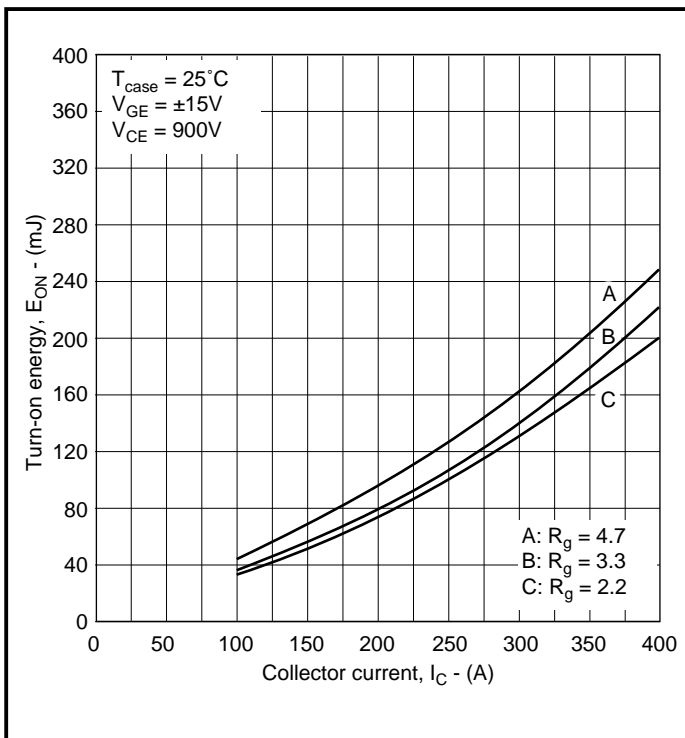
**TYPICAL CHARACTERISTICS**



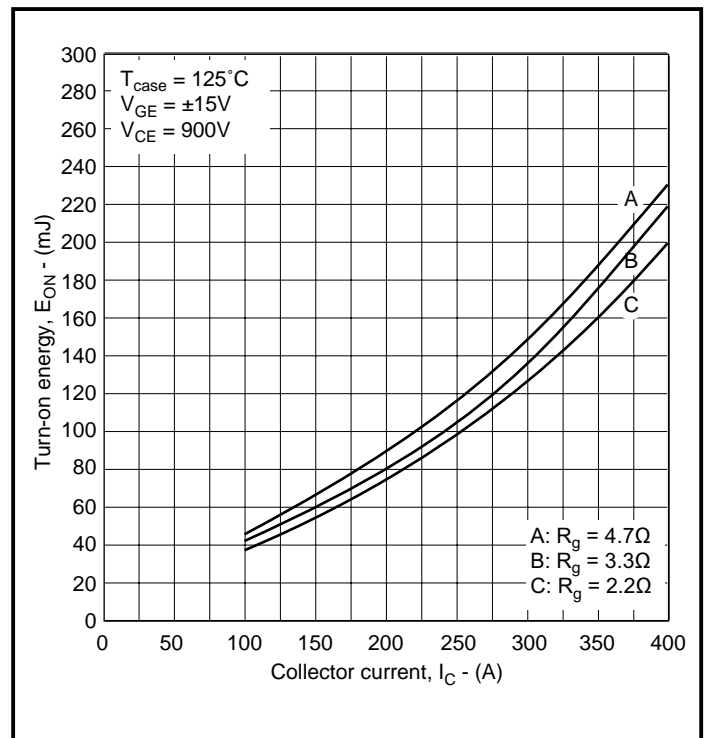
**Fig. 3 Typical output characteristics**



**Fig. 4 Typical output characteristics**



**Fig. 5 Typical turn-off energy vs collector current**



**Fig. 6 Typical turn-off energy vs collector current**

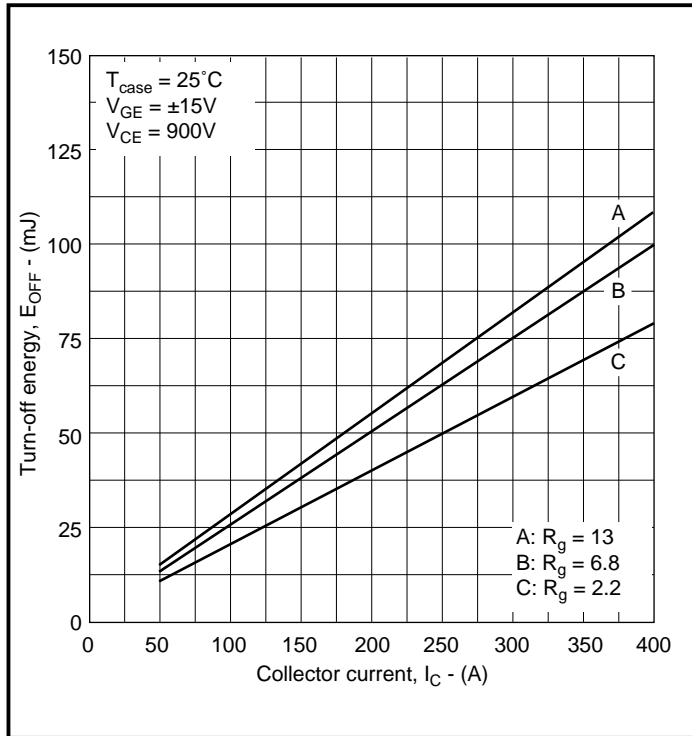


Fig. 7 Typical turn-off energy vs collector current

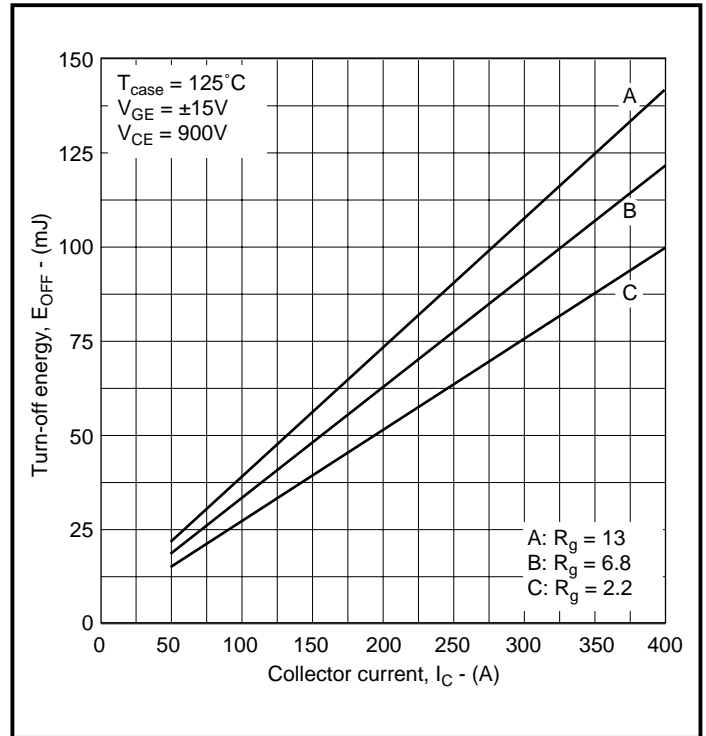


Fig. 8 Typical turn-off energy vs collector current

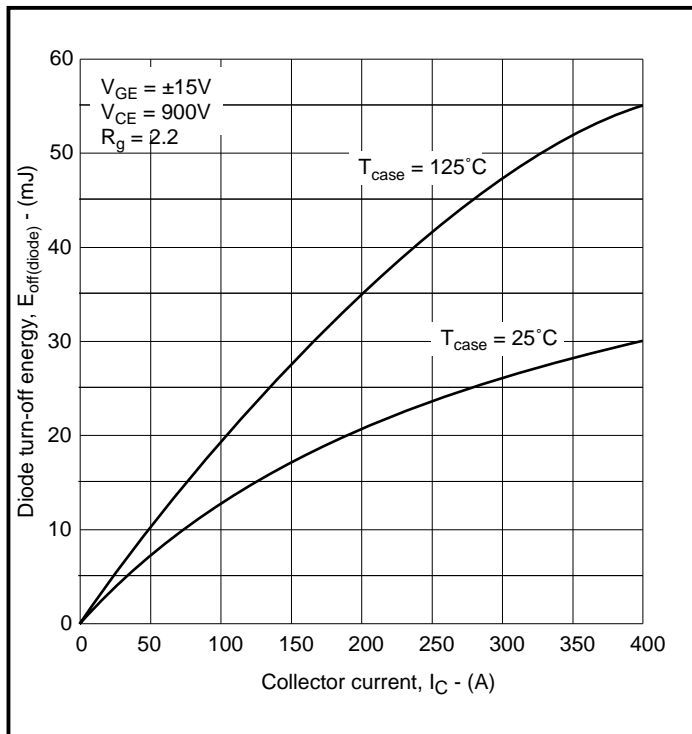


Fig. 9 Typical diode turn-off energy vs collector current

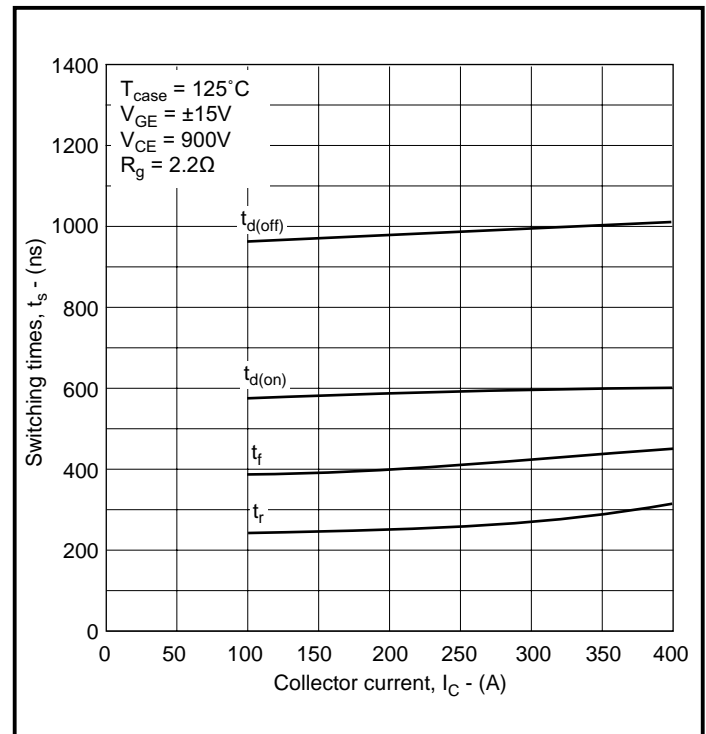
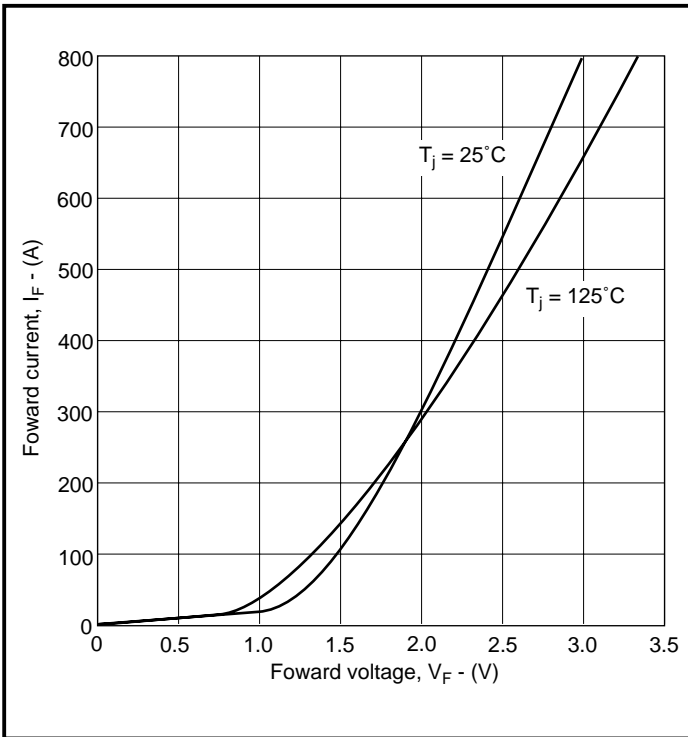
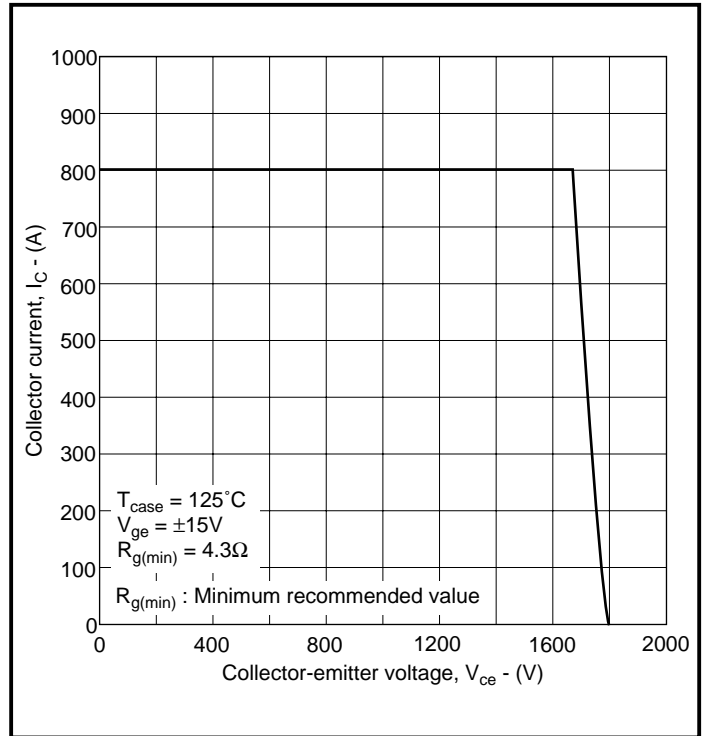


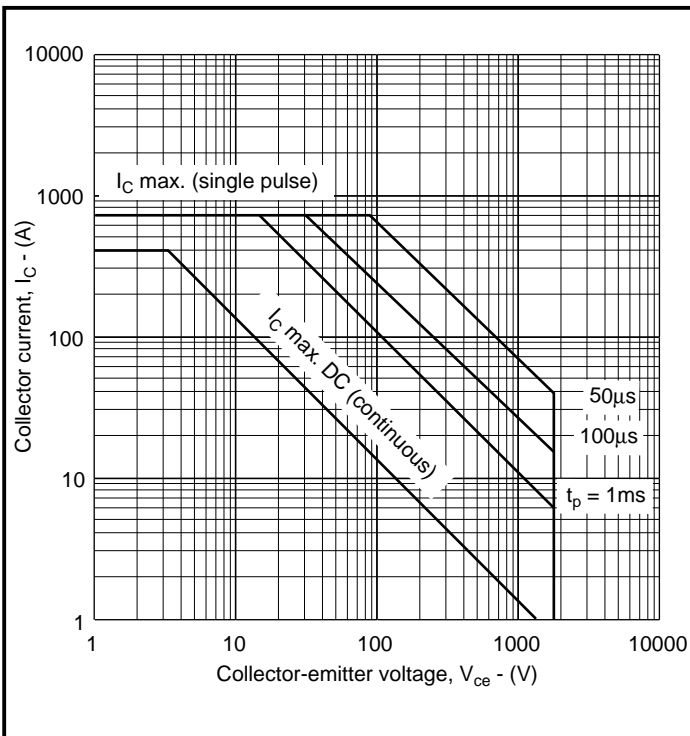
Fig. 10 Typical switching times



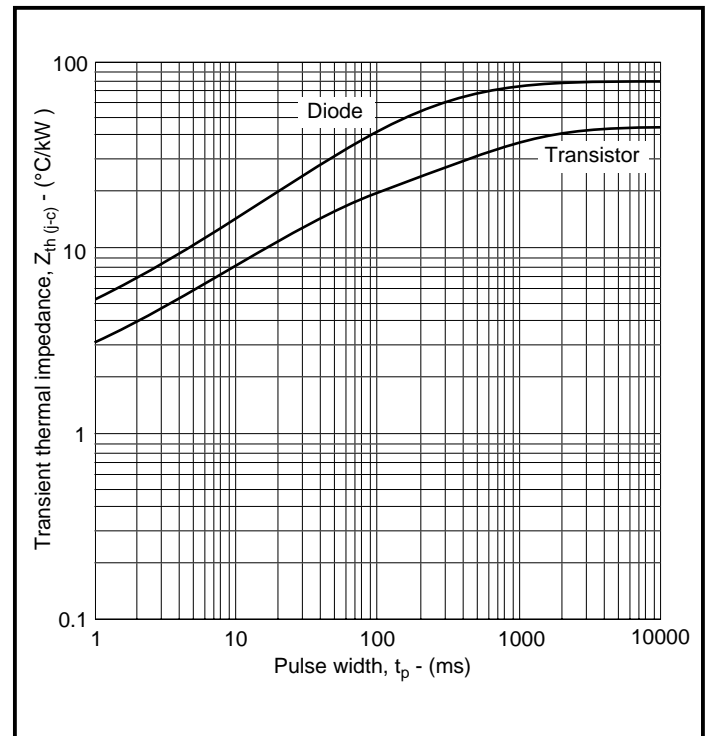
**Fig. 11 Diode typical forward characteristics**



**Fig. 12 Reverse bias safe operating area**



**Fig. 13 Forward bias safe operating area**



**Fig. 14 Transient thermal impedance**

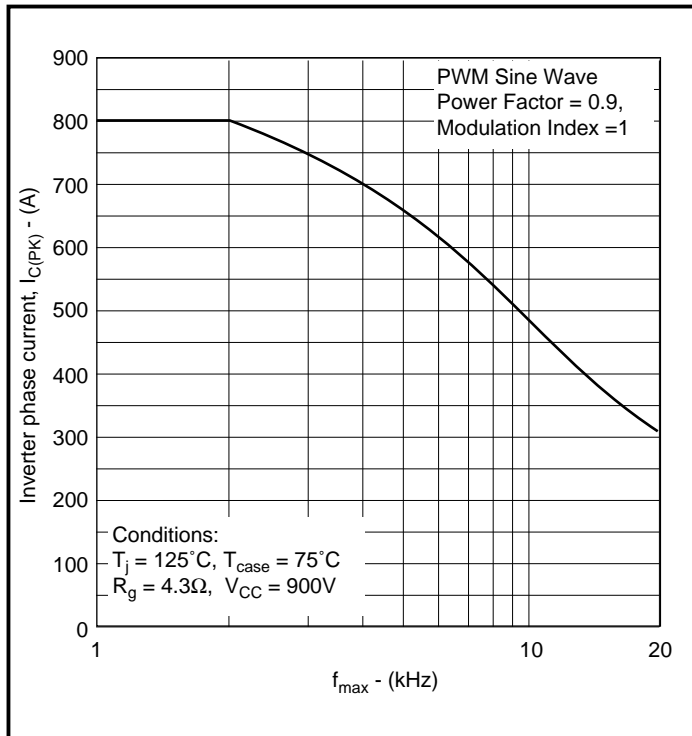


Fig.15 3-Phase inverter operating frequency

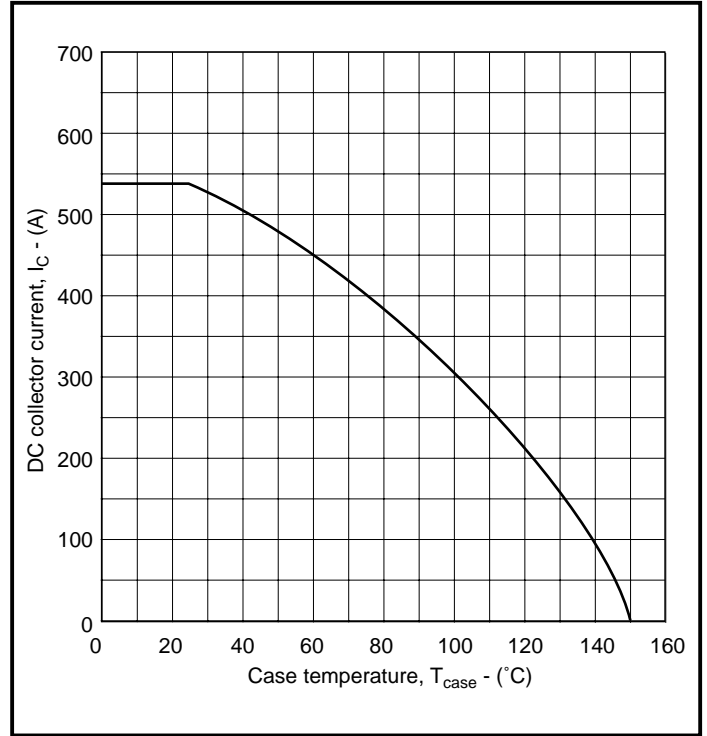
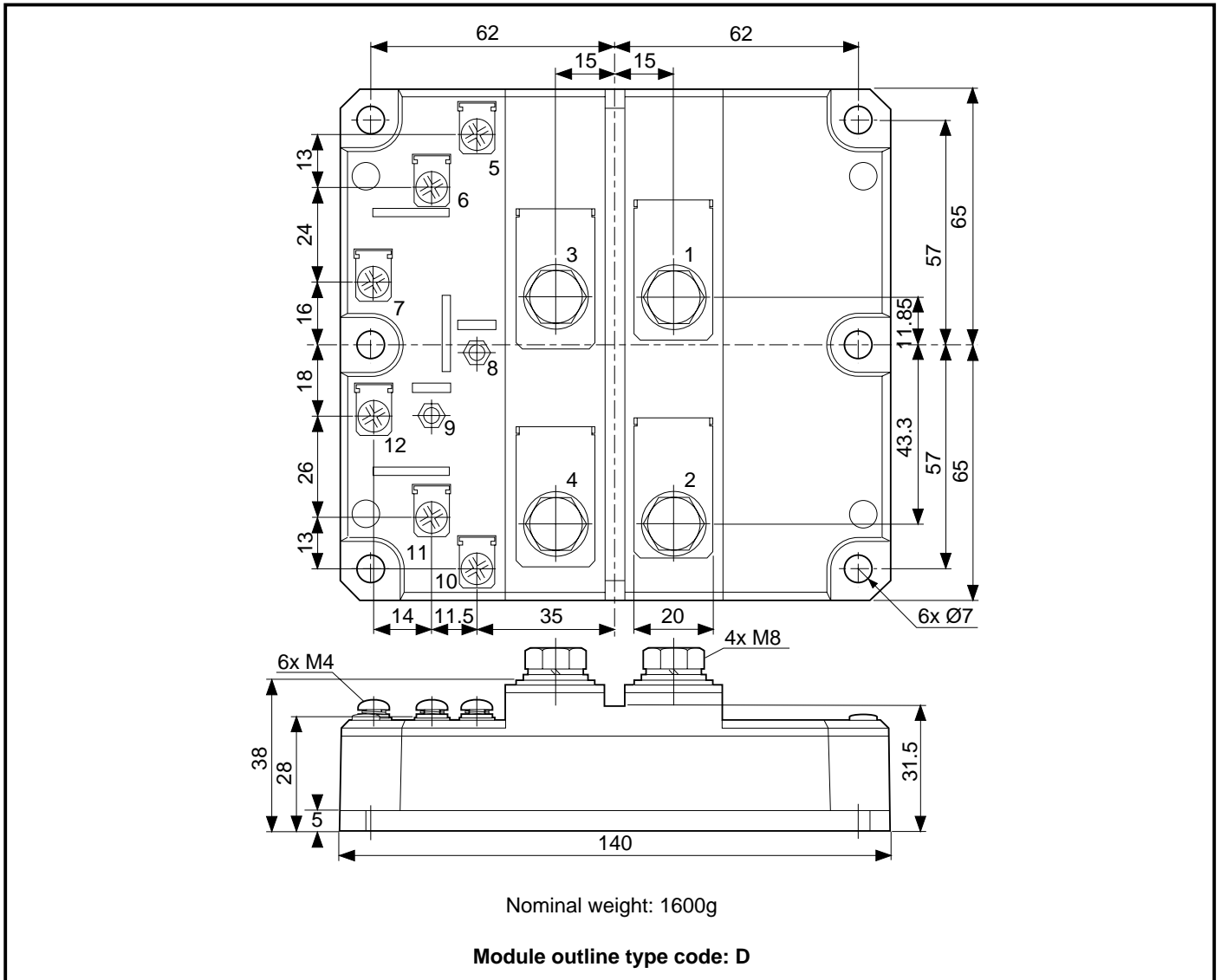


Fig.16 DC current rating vs case temperature



**PACKAGE DETAILS**

For further package information, please visit our website or contact your nearest Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



## ASSOCIATED PUBLICATIONS

Title	Application Note Number
Electrostatic handling precautions	AN4502
An introduction to IGBTs	AN4503
IGBT ratings and characteristics	AN4504
Heatsink requirements for IGBT modules	AN4505
Calculating the junction temperature of power semiconductors	AN4506
Gate drive considerations to maximise IGBT efficiency	AN4507
Parallel operation of IGBTs – punch through vs non-punch through characteristics	AN4508
Guidance notes for formulating technical enquiries	AN4869
Principle of rating parallel connected IGBT modules	AN5000
Short circuit withstand capability in IGBTs	AN5167
Driving high power IGBTs with Concept gate drivers	AN5190

## POWER ASSEMBLY CAPABILITY

The Power Assembly group provides support for those customers requiring more than the basic semiconductor switch. Using CAD design tools the group has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of Dynex semiconductors.

An extensive range of air and liquid cooled assemblies is available covering the range of circuit designs in general use today.

## HEATSINKS

The Power Assembly group has a proprietary range of extruded aluminium heatsinks. These were designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or customer service office.



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