



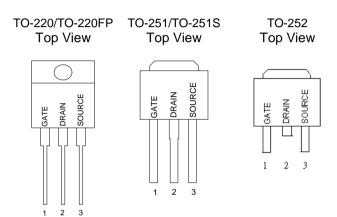
### **GENERAL DESCRIPTION**

This advanced high voltage MOSFET is designed to withstand high energy in the avalanche mode and switch efficiently. This hew high energy device also offers a drain-to-source diode with fast recovery time. Designed for high voltage, high speed switching applications such as power supplies, converters, power motor controls and bridge circuits. ◆

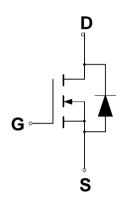
### **FEATURES**

- Higher Current Rating
- ◆ Lower Rds(on)
- ◆ Lower Capacitances
- ◆ Lower Total Gate Charge
  - Tighter VSD Specifications
  - Avalanche Energy Specified

#### PIN CONFIGURATION



#### **SYMBOL**



N-Channel MOSFET

### **ABSOLUTE MAXIMUM RATINGS**

Rating		Symbol	Value	Unit
Drain to Current — Continuous		I <sub>D</sub>	1.5	А
– Pul	sed	I <sub>DM</sub>	4.5	
Gate-to-Source Voltage	- Continue	V <sub>GS</sub>	±30	V
Total Power Dissipation	TO-251/ TO-251S/TO-252	P <sub>D (TC)</sub>	39	W
	TO-220		50	
	TO-220FP		20	
Derate above 25	TO-251/ TO-251S/TO-252		0.3	W/°C
	TO-220		0.38	
	TO-220FP		0.15	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	$^{\circ}\!\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}$ C				
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 1.4A, L = 10mH, R_G = 25)$		E <sub>AS</sub>	9.8	mJ
Thermal Resistance — Junction to Case TO-251/ TO-251S/TO-252		JC	3.2	°C/W
	TO-220		2.87	
	TO220FP		6	
<ul> <li>Junction to Ambient TO-251/TO-251S/TO-252</li> <li>TO-220, TO-220FP</li> </ul>		JA	120	
			62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		TL	260	$^{\circ}\!\mathbb{C}$





## Power Field Effect Transistor

### **ORDERING INFORMATION**

Part Number	Package
GPT02N60AGN220*	TO-220
GPT02N60AGN220FP*	TO-220 Full Package
GPT02N60AGN251*	TO-251
GPT02N60AGN251S*	TO-251S
GPT02N60AGN252*	TO-252

<sup>\*</sup>Note: G : Suffix for Pb Free Product

### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

			GPT02N60A		A	
Characteristic		Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		V <sub>(BR)DSS</sub>	600			V
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$						
Drain-Source Leakage Current		I <sub>DSS</sub>			1	uA
$(V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Fo	orward	$I_{GSSF}$			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Re	everse	$I_{GSSR}$			100	nA
$(V_{gsr} = -30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	2.5	3.5	4.5	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$						
Static Drain-Source On-Resistand	ce ( $V_{GS} = 10 \text{ V}, I_D = 1.0 \text{A}$ ) *	R <sub>DS(on)</sub>			6.8	
Forward Transconductance (V <sub>DS</sub> = 15 V, I <sub>D</sub> = 1.0 A) *		<b>g</b> FS		1		S
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	C <sub>iss</sub>		200.7		pF
Output Capacitance	f = 1.0  MHz	Coss		21.4		pF
Reverse Transfer Capacitance	1 = 1.0 Wil 12)	$C_{rss}$		1.88		pF
Turn-On Delay Time	(\/ 200 \/ \ 1.5 A	t <sub>d(on)</sub>		11.5		ns
Rise Time	$(V_{DD} = 300 \text{ V}, I_D = 1.5 \text{ A},$ $V_{GS} = 10 \text{ V}.$	t <sub>r</sub>		10.5		ns
Turn-Off Delay Time	$V_{GS} = 10 \text{ V},$ $R_{G} = 9.1 \text{ ) *}$	t <sub>d(off)</sub>		24.3		ns
Fall Time	NG - 9.1 )	t <sub>f</sub>		12		ns
Total Gate Charge	()/ 400 \/ 1 4.5.4	$Q_g$		7.32		nC
Gate-Source Charge	$(V_{DS} = 480 \text{ V}, I_{D} = 1.5 \text{ A},$ $V_{GS} = 10 \text{ V})^*$	$Q_{gs}$		1.54		nC
Gate-Drain Charge		$Q_gd$		3.68		nC
SOURCE-DRAIN DIODE CHARA	ACTERISTICS					
Forward On-Voltage(1)	(I <sub>S</sub> = 1.5 A,	V <sub>SD</sub>			1.5	V
Forward Turn-On Time		t <sub>on</sub>		**		ns
Reverse Recovery Time	$d_{IS}/d_t = 100A/\mu s)$	t <sub>rr</sub>		122.7		ns

<sup>\*</sup> Pulse Test: Pulse Width  $\leq$ 300 $\mu$ s, Duty Cycle  $\leq$ 2%

<sup>\*\*</sup> Negligible, Dominated by circuit inductance





### TYPICAL ELECTRICAL CHARACTERISTICS

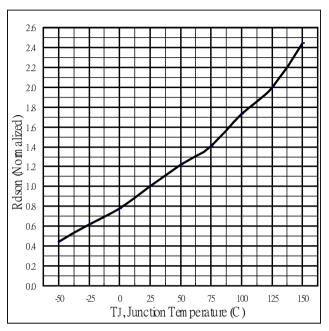


Fig 1. On-Resistance Variation with vs. Temperature

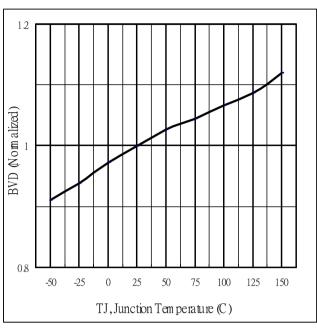


Fig.2 Breakdown Voltage Variation vs. Temperature

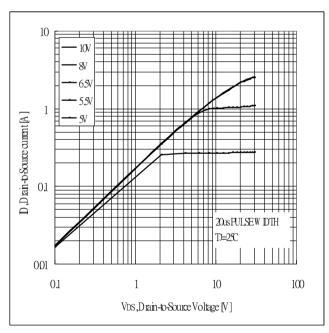


Fig 3. Typical Output Characteristics

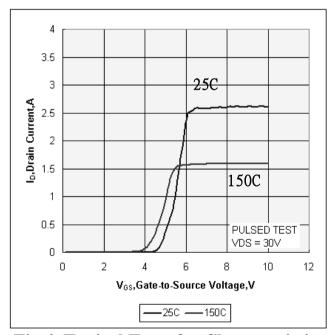


Fig 4. Typical Transfer Characteristics





## POWER FIELD EFFECT TRANSISTOR

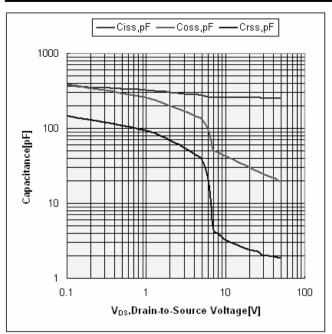


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

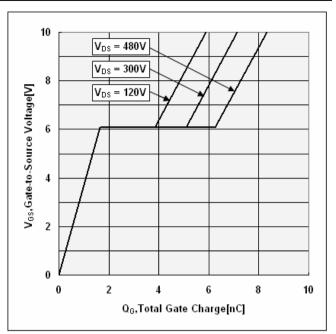


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

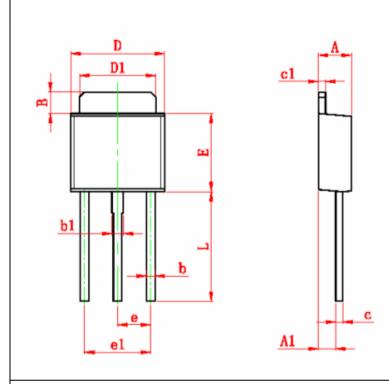




## **PACKAGE DIMENSION**

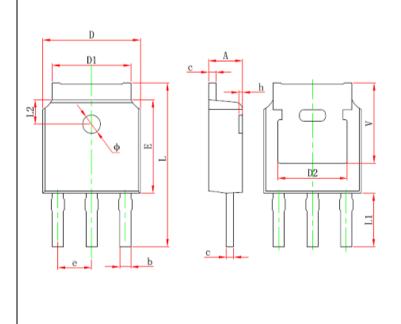
TO-251





Carran la a l	Dimensions In Millimeters		
Symbol	Min.	Max	
A	2.10	2.50	
A1	0.90	1.35	
В	0.90	1.65	
Ъ	0.45	0.75	
b1	0.65	0.95	
С	0.40	0.60	
c1	0.40	0.60	
D	6.30	6.80	
D1	5.00	5.50	
E	5.40	6.30	
е	2.3 TYP.		
e1	4.40	4.80	
L	7.40	8.00	

### TO-251S

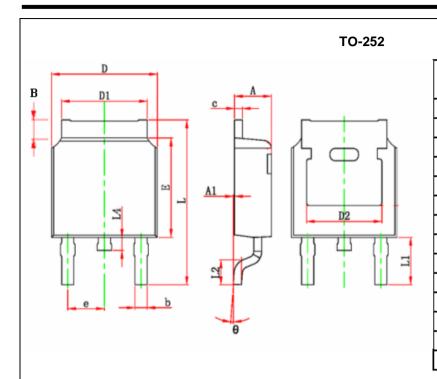


Crembal	Dimensions In Millimeters		
Symbol	Min.	Max	
А	2.10	2.50	
Ъ	0.64	0.90	
С	0.44	0.60	
D	6.30	6.90	
D1	5.00	5.50	
D2	4.83 TYP.		
E	5.80	6.40	
е	2.286 TYP.		
L1	3.5 TYP.		
L2	1.6 TYP.		
Ф	1.2 TYP.		
h	0.00	0.30	
V	5.35 TYP.		





# Power Field Effect Transistor



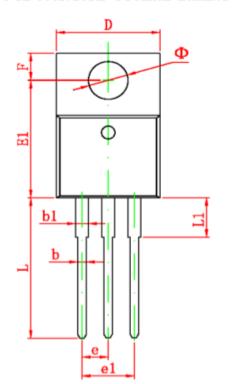
Symbol	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	2.10	2.50	
A1	0.90	1.35	
В	0.90	1.65	
Ь	0.45	0.90	
С	0.40	0.60	
D	6.30	6.80	
D1	5.00	5.50	
D2	4.83 TYP.		
E	5.90	6.30	
е	2.3 TYP.		
L	9.30	10.50	
L2	1.20	1.80	
L4	0.60	1.00	
θ	0.00	10.00	

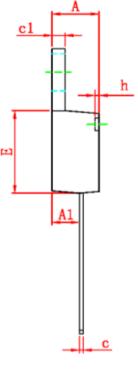




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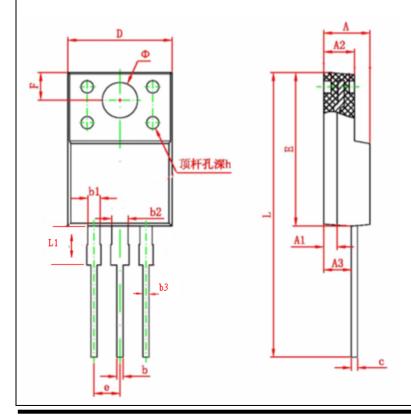






C b - 1	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	4.40	4.80	
A1	2.10	2.84	
ь	0.71	0.91	
b1	1.17	1.37	
С	0.30	0.60	
c1	1.17	1.47	
D	9.40	10.60	
E	8.40	9.60	
е	2.54 TYP.		
e1	4.90	5.60	
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	12.50	14.00	
L1	3.50	4.00	

### TO-220FP



Cross le a l	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	3.80	4.70	
A1	1.3 REF.		
A2	2.20	3.20	
А3	2.10	3.20	
Ь	0.30	0.95	
b1	1.00	1.75	
b2	1.00	1.75	
b3	0.50	0.80	
С	0.30	0.90	
D	9.90	10.40	
E	14.60	16.20	
e	2.54 TYP.		
F	3.00 REF.		
θ	3.50 REF.		
h	0.00	0.30	
L	28.00	30.00	
L1	3.20	3 <b>.</b> 55	



## POWER FIELD EFFECT TRANSISTOR

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