



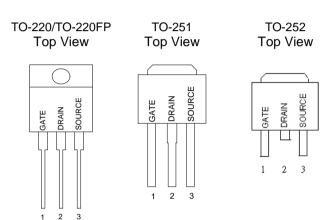
GENERAL DESCRIPTION

This advanced high voltage MOSFET is designed to withstand high energy in the avalanche mode and switch efficiently. This new high energy device also offers a drain-to-source diode with fast recovery time. Designed for high voltage, high speed switching applications such as power supplies, converters, power motor controls and bridge circuits. ◆

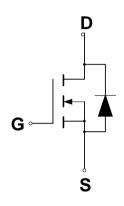
FEATURES

- Higher Current Rating
- ◆ Lower Rds(on)
- ◆ Lower Capacitances
- ◆ Lower Total Gate Charge
 - Tighter VSD Specifications
 - Avalanche Energy Specified

PIN CONFIGURATION



SYMBOL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS

	Rating	Symbol	Value	Unit
Drain to Current — Continuous		I _D	1.5	Α
– Pul	- Pulsed		4.5	
Gate-to-Source Voltage	- Continue	V_{GS}	±30	V
Total Power Dissipation	TO-251/TO-252	P _{D (TC)}	39	W
	TO-220		51	
	TO-220FP		21	
Derate above 25	TO-251/TO-252		0.29	W/°C
	TO-220		0.38	
	TO-220FP		0.15	
Operating and Storage	Temperature Range	T _J , T _{STG}	-55 to 150	$^{\circ}$ C
Single Pulse Drain-to-Source Avalanche Energy $-T_J=25^{\circ}\!\mathbb{C}$		E _{AS}	9.11	mJ
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 1.35A, L = 10mH, R_G = 25)$				
Thermal Resistance — Junction to Case TO-251/TO-252		JC	2.9	°C/W
	TO-220		2	
	TO220FP		5.8	
_	Junction to Ambient TO-251/TO-252	JA	120	
	TO-220, TO-220FP		62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		TL	260	$^{\circ}$ C





Power Field Effect Transistor

ORDERING INFORMATION

Part Number	Package
GPT02N70AGN220*	TO-220
GPT02N70AGN220FP*	TO-220 Full Package
GPT02N70AGN251*	TO-251
GPT02N70AGN252*	TO-252

^{*}Note: G : Suffix for Pb Free Product

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $T_J = 25^{\circ}C$.

			(GPT02N70	A	
Char	acteristic	Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		V _{(BR)DSS}	700			V
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$						
Drain-Source Leakage Current		I _{DSS}			1	uA
$(V_{DS} = 700 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Fo	rward	I_{GSSF}			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Reverse		I_{GSSR}			100	nA
$(V_{gsr} = -30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	2.5	3.5	4.5	V
$(V_{DS} = V_{GS}, I_{D} = 250 \ \mu A)$						
Static Drain-Source On-Resistanc	e ($V_{GS} = 10 \text{ V}, I_D = 1 \text{ A}$) *	R _{DS(on)}			11	
Forward Transconductance (V_{DS} =	= 15 V, I _D = 1 A) *	g FS		1.8		S
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	C _{iss}		226.9		pF
Output Capacitance	,,	C _{oss}		19.8		pF
Reverse Transfer Capacitance	f = 1.0 MHz)	C _{rss}		1.21		pF
Turn-On Delay Time	()/ 250 \/ 1 4.5 A	t _{d(on)}		12.4		ns
Rise Time	$(V_{DD} = 350 \text{ V}, I_D = 1.5 \text{ A},$	t _r		11.4		ns
Turn-Off Delay Time	$V_{GS} = 10 \text{ V},$	t _{d(off)}		57.6		ns
Fall Time	$R_G = 9.1$)*	t _f		52.8		ns
Total Gate Charge	0/ 500 // 1 4.5.4	Q_g		7.56		nC
Gate-Source Charge	$(V_{DS} = 560 \text{ V}, I_D = 1.5 \text{ A},$	Q _{gs}		1.35		nC
Gate-Drain Charge	$V_{GS} = 10 \text{ V})^*$	Q_gd		4.24		nC
SOURCE-DRAIN DIODE CHARA	CTERISTICS	•		•		•
Forward On-Voltage(1)	(1 4 5 A	V _{SD}			1.5	V
Forward Turn-On Time	$(I_S = 1.5 \text{ A},$	t _{on}		**		ns
Reverse Recovery Time	$d_{IS}/d_t = 100A/\mu s)$	t _{rr}		139.2		ns

^{*} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%

^{**} Negligible, Dominated by circuit inductance





TYPICAL ELECTRICAL CHARACTERISTICS

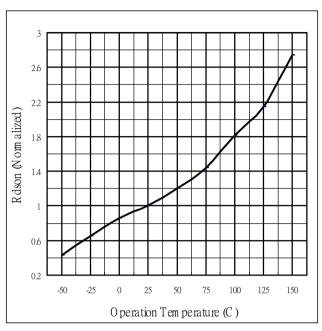


Fig 1. On-Resistance Variation with vs. Temperature

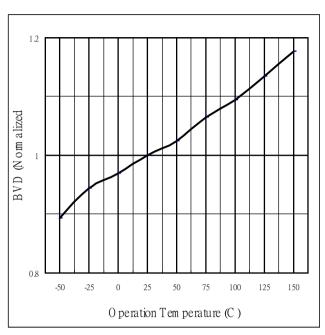


Fig.2 Breakdown Voltage Variation vs. Temperature

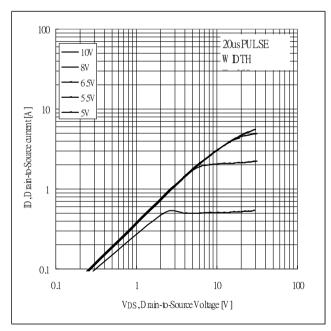


Fig 3. Typical Output Characteristics

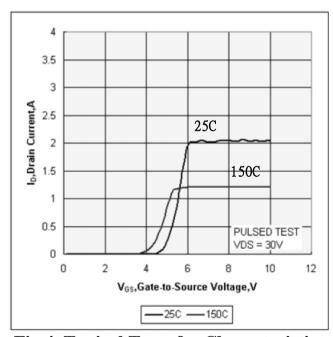


Fig 4. Typical Transfer Characteristics





GPT02N70A

POWER FIELD EFFECT TRANSISTOR

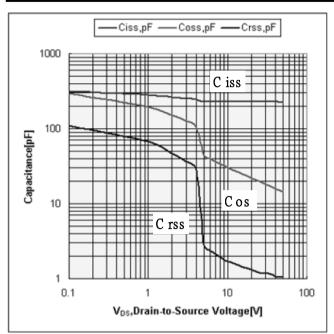


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

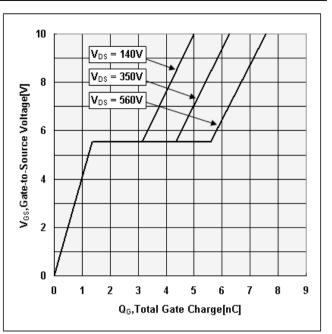


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

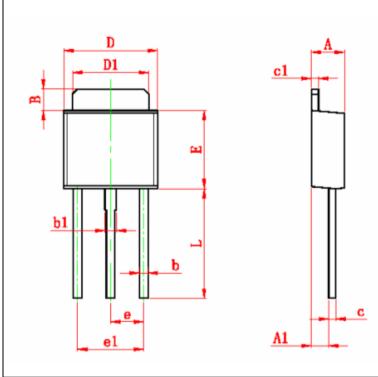




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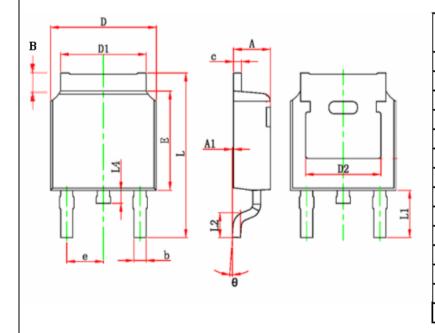
PACKAGE DIMENSION





Cross had	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	2.10	2.50	
A1	0.90	1.35	
В	0.90	1.65	
Ь	0.45	0.75	
b1	0.65	0.95	
С	0.40	0.60	
c1	0.40	0.60	
D	6.30	6.80	
D1	5.00	5.50	
E	5.40	6.30	
е	2.3 TYP.		
e1	4.40	4.80	
L	7.40	8.00	

TO-252



Cerrel al	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	2.10	2.50	
A1	0.90	1.35	
В	0.90	1.65	
Ъ	0.45	0.90	
O	0.40	0.60	
D	6.30	6.80	
D1	5.00	5.50	
D2	4.83 TYP.		
E	5.90	6.30	
е	2.3 TYP.		
L	9.30	10.50	
L2	1.20	1.80	
L4	0.60	1.00	
0	0.00	10.00	

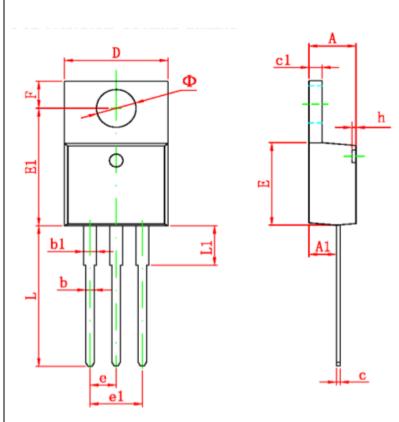




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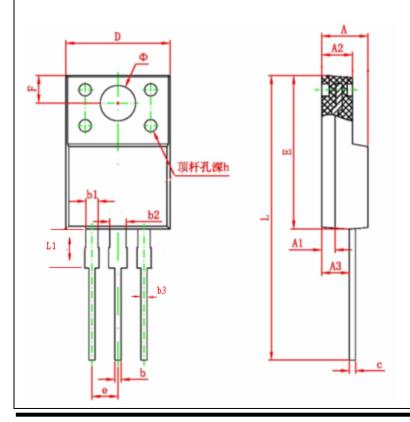
Power Field Effect Transistor





Cross b a l	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	4.40	4.80	
A1	2.10	2.84	
Ь	0.71	0.91	
b1	1.17	1.37	
С	0.30	0.60	
c1	1.17	1.47	
D	9.40	10.60	
E	8.40	9.60	
e	2.54 TYP.		
e1	4.90	5.60	
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	12.50	14.00	
L1	3.50	4.00	

TO-220FP



Symbol	Dimensions I	n Millimeters	
	Min.	Max	
Α	3.80	4.70	
A1	1.3 REF.		
A2	2.20	3.20	
А3	2.10	3.20	
Ь	0.30	0.95	
b1	1.00	1.75	
b2	1.00	1.75	
b3	0.50	0.80	
С	0.30	0.90	
D	9.90	10.40	
E	14.60	16.20	
e	2.54 TYP.		
F	3.00 REF.		
θ	3.50 REF.		
h	0.00	0.30	
L	28.00	30.00	
L1	3.20	3 . 55	



GPT02N70A

POWER FIELD EFFECT TRANSISTOR

IMPORTANT NOTICE

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