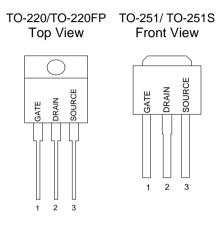
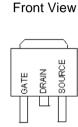


### **GENERAL DESCRIPTION**

This advanced high voltage MOSFET is designed to withstand high energy in the avalanche mode and switch efficiently. This new high energy device also offers a drain-to-source diode with fast recovery time. Designed for high voltage, high speed switching applications such as power supplies, converters, power motor controls and bridge circuits.

### **PIN CONFIGURATION**





2 3

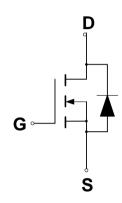
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TO-252

### FEATURES

- Higher Current Rating
- Lower Rds(on)
- Lower Capacitances
  - Lower Total Gate Charge
- Tighter VSD Specifications
- Avalanche Energy Specified

#### SYMBOL



N-Channel MOSFET

### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain to Current – Continuous	I <sub>D</sub>	3.5	А
- Pulsed	I <sub>DM</sub>	10.5	
Gate-to-Source Voltage – Continue	V <sub>GS</sub>	±30	V
Total Power Dissipation TO-251/ TO-251S /TO-252	P <sub>D</sub>	44	W
TO-220		70	
TO-220FP		23	W/°C
Derate above 25 $^\circ$ C TO-251/ TO-251S /TO-252		0.34	
TO-220		0.6	
TO-220FP		0.2	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}C$		36.45	
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 2.7A, L = 10mH, R_G = 25$ )			mJ
Thermal Resistance – Junction to Case TO-251/ TO-251S /TO-252	JC	2.7	°C <b>/W</b>
TO-220		1.8	
TO220FP		4.9	
<ul> <li>Junction to Ambient TO-251/ TO-251S /TO-252</li> </ul>	JA	120	
TO-220, TO-220FP		62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 sec	onds T <sub>L</sub>	260	°C



#### ORDERING INFORMATION

Part Number	Package
GPT04N60AGN220*	TO-220
GPT04N60AGN220FP*	TO-220 Full Package
GPT04N60AGN251*	TO-251
GPT04N60AGN251S*	TO-251S
GPT04N60AGN252*	TO-252

\*Note: G : Suffix for Pb Free Product

## **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J$  = 25  $^\circ\!\mathrm{C}$  .

				GPT04N60	Α	
Chai	racteristic	Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage	)	V <sub>(BR)DSS</sub>	600			V
$(V_{GS} = 0 V, I_D = 250 \mu A)$						
Drain-Source Leakage Current		I <sub>DSS</sub>			1	uA
$(V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Fo	orward	I <sub>GSSF</sub>			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Re	everse	I <sub>GSSR</sub>			100	nA
$(V_{gsr} = -30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		V <sub>GS(th)</sub>	2.5	3.5	4.5	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$						
Static Drain-Source On-Resistand	ce ( $V_{GS} = 10 \text{ V}, I_D = 2.0\text{A}$ ) *	R <sub>DS(on)</sub>			3.2	
Forward Transconductance (V <sub>DS</sub>	= 15 V, I <sub>D</sub> = 2.0 A) *	<b>g</b> fs		2.5		S
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	Ciss		480.4		pF
Output Capacitance	$(v_{DS} = 25 \text{ V}, v_{GS} = 0 \text{ V},$ f = 1.0 MHz)	C <sub>oss</sub>		44.7		pF
Reverse Transfer Capacitance	T = 1.0 (VII 12)	C <sub>rss</sub>		2.56		pF
Turn-On Delay Time	$(V_{DD} = 300 \text{ V}, I_D = 3.5 \text{ A},$ $V_{GS} = 10 \text{ V},$ $R_G = 9.1$ ) *	t <sub>d(on)</sub>		14.4		ns
Rise Time		tr		15.1		ns
Turn-Off Delay Time		t <sub>d(off)</sub>		30.4		ns
Fall Time		t <sub>f</sub>		13.9		ns
Total Gate Charge	$(V_{DS} = 480 \text{ V}, I_D = 3.5 \text{ A},$ $V_{GS} = 10 \text{ V})^*$	Qg		11.9		nC
Gate-Source Charge		Q <sub>gs</sub>		2.72		nC
Gate-Drain Charge		Q <sub>gd</sub>		5.57		nC
SOURCE-DRAIN DIODE CHAR	ACTERISTICS					
Forward On-Voltage(1)	(I <sub>S</sub> = 3.5 A, d <sub>IS</sub> /d <sub>t</sub> = 100A/µs)	V <sub>SD</sub>			1.5	V
Forward Turn-On Time		t <sub>on</sub>		**		ns
Reverse Recovery Time		t <sub>rr</sub>		200		ns

\* Pulse Test: Pulse Width  $\leq$ 300µs, Duty Cycle  $\leq$ 2%

\*\* Negligible, Dominated by circuit inductance



## **TYPICAL ELECTRICAL CHARACTERISTICS**

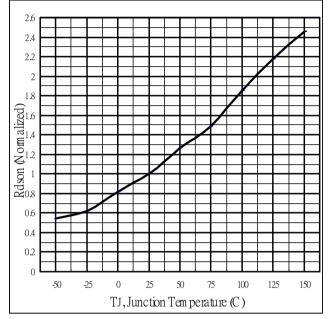


Fig 1. On-Resistance Variation with vs. Temperature

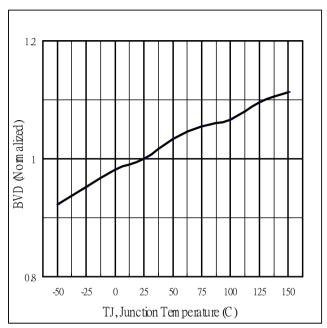


Fig.2 Breakdown Voltage Variation vs. Temperature

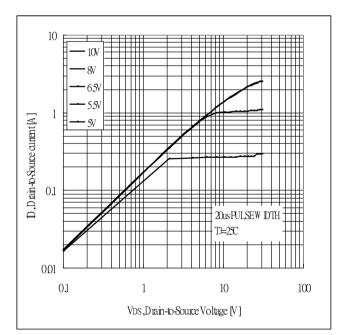
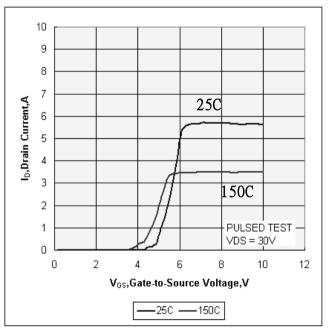


Fig 3. Typical Output Characteristics



**Fig 4. Typical Transfer Characteristics** 



**GPT04N60A** Power Field Effect Transistor

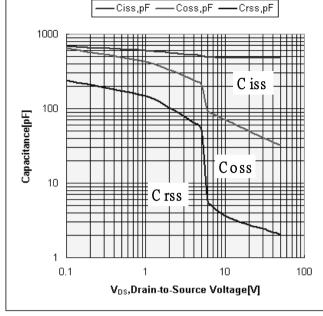


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

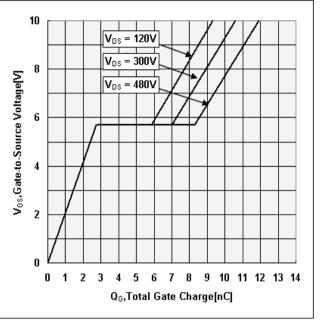
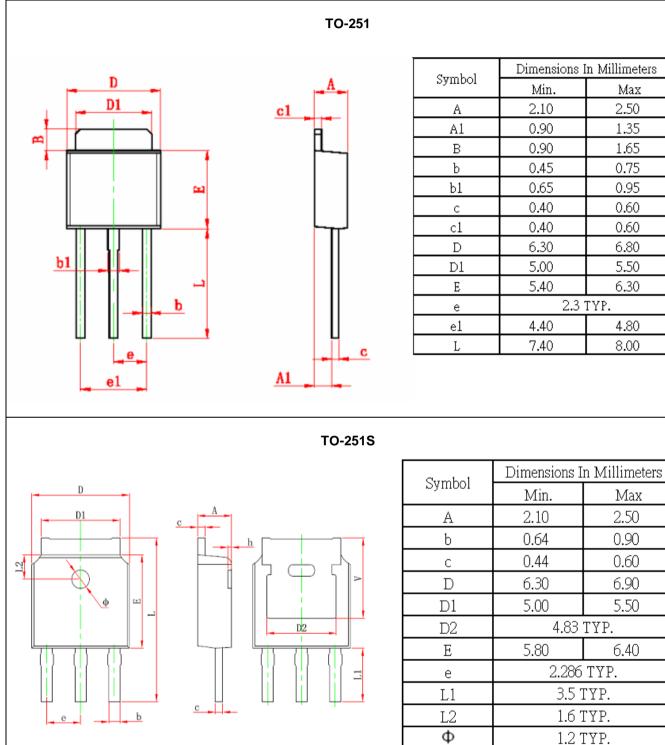


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage





#### PACKAGE DIMENSION



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V

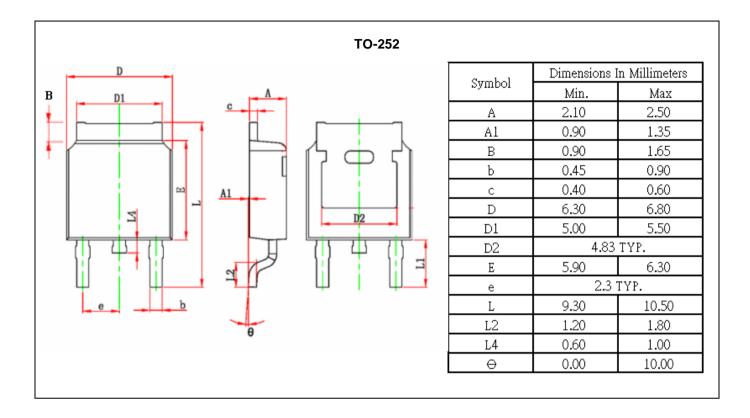
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0.30

5.35 TYP.









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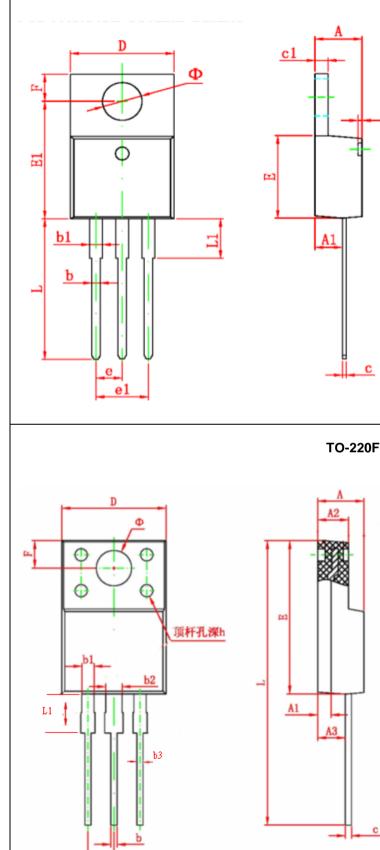
CHAMPION

虹冠電子

# GPT04N60A Power Field Effect Transistor

TO-220

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Cumbal	Dimensions In Millimeters		
Symbol	Min.	Мах	
А	4.40	4.80	
A1	2.10	2.84	
b	0.71	0.91	
b1	1.17	1.37	
с	0.30	0.60	
c1	1.17	1.47	
D	9.40	10.60	
E	8.40	9.60	
е	2.54 TYP.		
el	4.90	5.60	
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	12.50	14.00	
L1	3.50	4.00	

**TO-220FP** 

	Dimensional In Million stars		
Symbol	Dimensions In Millimeters		
bymbor	Min.	Max	
А	3.80	4.70	
A1	1.3 REF.		
A2	2.20	3.20	
A3	2.10	3.20	
b	0.30	0.95	
b1	1.00	1.75	
b2	1.00	1.75	
b3	0.50	0.80	
с	0.30	0.90	
D	9.90	10.40	
E	14.60	16.20	
е	2.54 TYP.		
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	28.00	30.00	
L1	3.20	3.55	



### **IMPORTANT NOTICE**

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