





## **GENERAL DESCRIPTION**

PIN CONFIGURATION

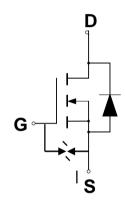
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

# TO-220F/TO220 Top View Front View Front View TO-252 Front View TO-252 Front View TO-252 Front View TO-253 Front View

#### **FEATURES**

- ◆ Robust High Voltage Termination
- ◆ Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- ◆ I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature

#### SYMBOL



N-Channel MOSFET

## **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain to Current — Continuous		4.5	Α
- Pulsed	I <sub>DM</sub>	13.5	
Gate-to-Source Voltage — Continue	$V_{GS}$	±30	V
Total Power Dissipation - TO251,252	P <sub>D</sub>	42	W
- TO220		76	
- TO220F		24	W/°C
Derate above 25℃ - TO251,252		0.32	
- TO220		0.61	
- TO220F		0.21	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	$^{\circ}\!\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}$ C		80	mJ
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 4A, L = 10mH, R_G = 25)$			
Thermal Resistance — Junction to Case - TO251,252	JC	2.8	°CW
- TO220		1.6	
- TO220F		4.8	
<ul> <li>Junction to Ambient - TO251,252</li> </ul>	JA	120	
- TO220, TO220F		62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	$^{\circ}\!\mathbb{C}$
ESD SENSITIVITY - HBM, C=100pF, R=1.5k	Vesd	2000	V





## **ORDERING INFORMATION**

Part Number	Package
GPT05N50GN220FP*	TO-220F
GPT05N50GN220*	TO-220
GPT05N50GN251*	TO-251
GPT05N50GN251S*	TO-251S
GPT05N50GN252*	TO-252

<sup>\*</sup>Note: G : Suffix for PB Free Product

## **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

				GPT05N50	0	
Char	acteristic	Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		V <sub>(BR)DSS</sub>	500			V
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$		, ,				
Drain-Source Leakage Current		I <sub>DSS</sub>			1	μΑ
$(V_{DS} = 500 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Fo	rward	$I_{GSSF}$			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Re	verse	$I_{GSSR}$			100	nA
$(V_{gsr} = -30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	2.5	3.5	4.5	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$						
Static Drain-Source On-Resistanc	e (V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.5A) *	R <sub>DS(on)</sub>			1.55	
Forward Transconductance (V <sub>DS</sub> =	= 20 V, I <sub>D</sub> = 2.5A) *	<b>g</b> fs		3		S
Input Capacitance	$(V_{DS} = 25 \text{ V. } V_{GS} = 0 \text{ V.}$	$C_{iss}$		528		pF
Output Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0  MHz)	C <sub>oss</sub>		52		pF
Reverse Transfer Capacitance	1 = 1.0 Wil 12)	C <sub>rss</sub>		4		pF
Turn-On Delay Time		t <sub>d(on)</sub>		14.3		ns
Rise Time	$(V_{DD} = 250 \text{ V}, I_D = 5 \text{ A},$	t <sub>r</sub>		15		ns
Turn-Off Delay Time	$R_G = 25$ ) *	$t_{d(off)}$		29.3		ns
Fall Time		t <sub>f</sub>		12.5		ns
Total Gate Charge	()/ 400 \/ 1 5 A	$Q_g$		13		nC
Gate-Source Charge	$(V_{DS} = 400 \text{ V}, I_D = 5 \text{ A}, V_{GS} = 10 \text{ V})^*$	$Q_gs$		3		nC
Gate-Drain Charge		Q <sub>gd</sub>		6.2		nC
SOURCE-DRAIN DIODE CHARA	CTERISTICS	-				
Forward On-Voltage(1)	(1. 5.4.)/ 0.)/	V <sub>SD</sub>			1.5	V
Forward Turn-On Time	$(I_S = 5 A, V_{GS} = 0 V, d_{IS}/d_t = 100A/\mu s)$	t <sub>on</sub>		**		ns
Reverse Recovery Time		t <sub>rr</sub>		213		ns

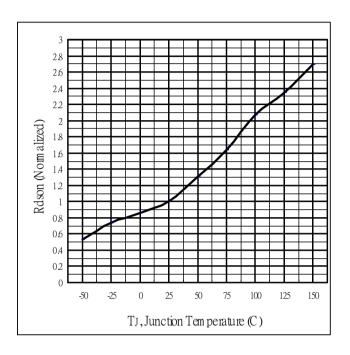
<sup>\*</sup> Pulse Test: Pulse Width  $\leq$ 300 $\mu$ s, Duty Cycle  $\leq$ 2%

<sup>\*\*</sup> Negligible, Dominated by circuit inductance





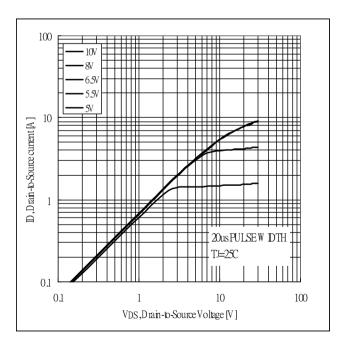
### TYPICAL ELECTRICAL CHARACTERISTICS



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Fig 1. On-Resistance Variation with vs. Temperature

Fig.2 Breakdown Voltage Variation vs. Temperature



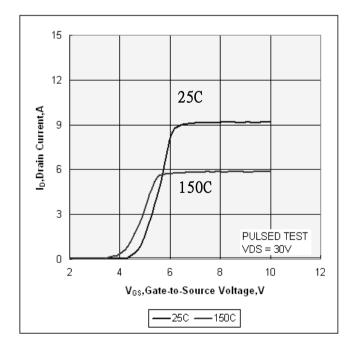


Fig 3. Typical Output Characteristics

Fig 4. Typical Transfer Characteristics







# POWER FIELD EFFECT TRANSISTOR

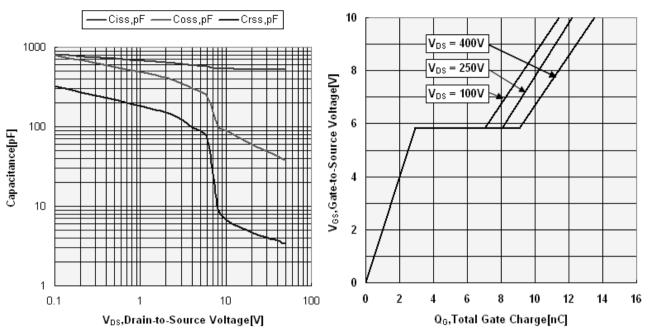


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

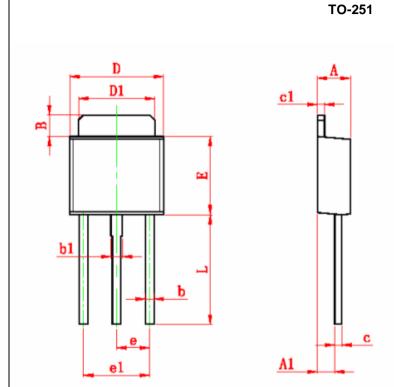
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage





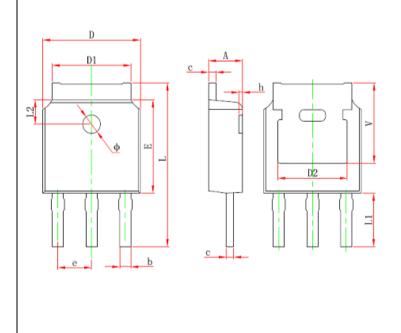


# **PACKAGE DIMENSION**



Creen had	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	2.10	2.50	
A1	0.90	1.35	
В	0.90	1.65	
ь	0.45	0.75	
b1	0.65	0.95	
С	0.40	0.60	
c1	0.40	0.60	
D	6.30	6.80	
D1	5.00	5.50	
E	5.40	6.30	
e	2.3 TYP.		
e1	4.40	4.80	
L	7.40	8.00	

### TO-251S



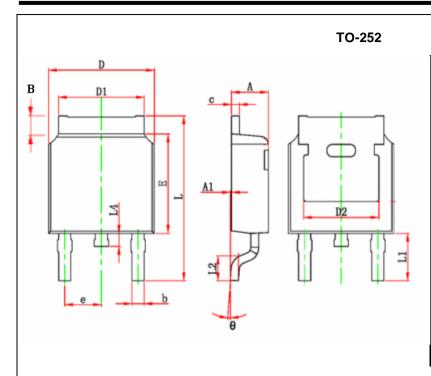
Cumbal	Dimensions In Millimeters		
Symbol	Min.	Max	
А	2.10	2.50	
Ъ	0.64	0.90	
С	0.44	0.60	
D	6.30	6.90	
D1	5.00	5.50	
D2	4.83 TYP.		
Е	5.80	6.40	
е	2.286 TYP.		
L1	3.5 TYP.		
L2	1.6 TYP.		
Ф	1.2 TYP.		
h	0.00	0.30	
V	5.35 TYP.		





# **GPT05N50**

# Power Field Effect Transistor



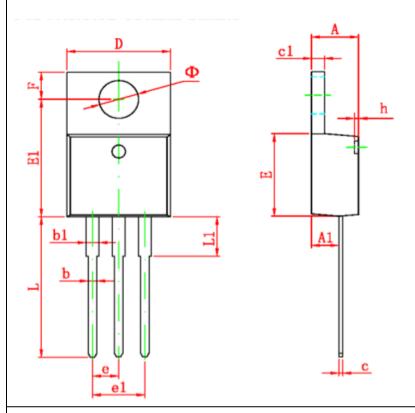
Creen had	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	2.10	2.50	
A1	0.90	1.35	
В	0.90	1.65	
ь	0.45	0.90	
С	0.40	0.60	
D	6.30	6.80	
D1	5.00	5.50	
D2	4.83 TYP.		
E	5.90	6.30	
e	2.3 TYP.		
L	9.30	10.50	
L2	1.20	1.80	
L4	0.60	1.00	
Ф	0.00	10.00	





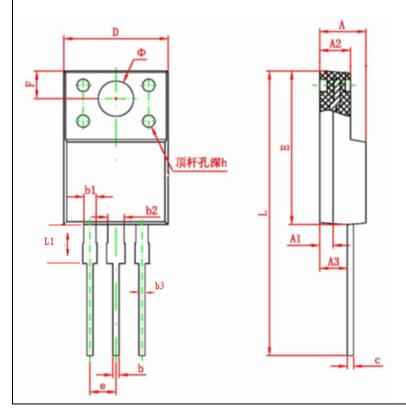
# Power Field Effect Transistor





Cross b a l	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	4.40	4.80	
A1	2.10	2.84	
b	0.71	0.91	
b1	1.17	1.37	
С	0.30	0.60	
c1	1.17	1.47	
D	9.40	10.60	
E	8.40	9.60	
е	2.54 TYP.		
e1	4.90	5.60	
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	12.50	14.00	
L1	3.50	4.00	

#### TO-220F



C l l	Dimensions In Millimeters			
Symbol	Min.	Max		
А	3.80	4.70		
A1	1.3 REF.			
A2	2.20	3.20		
A3	2.10	3.20		
Ъ	0.30	0.95		
b1	1.00	1.75		
b2	1.00	1.75		
b3	0.50	0.80		
C	0.30	0.90		
D	9.90	10.40		
E	14.60	16.20		
е	2.54 TYP.			
F	3.00 REF.			
θ	3.50 REF.			
h	0.00	0.30		
L	28.00	30.00		
L1	3.20	3 <b>.</b> 55		



# **GPT05N50**

# POWER FIELD EFFECT TRANSISTOR

#### **IMPORTANT NOTICE**

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