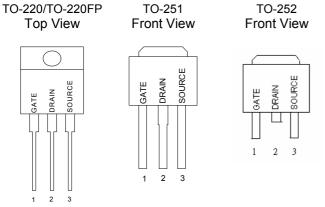




#### **GENERAL DESCRIPTION**

This advanced high voltage MOSFET is designed to withstand high energy in the avalanche mode and switch efficiently. This hew high energy device also offers a drain-to-source diode with fast recovery time. Designed for high voltage, high speed switching applications such as power supplies, converters, power motor controls and bridge circuits. ◆

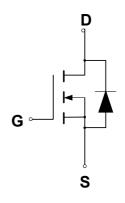
#### PIN CONFIGURATION



#### **FEATURES**

- Higher Current Rating
- ◆ Lower Rds(on)
- ◆ Lower Capacitances
- ◆ Lower Total Gate Charge
- Tighter VSD Specifications
- Avalanche Energy Specified

#### **SYMBOL**



N-Channel MOSFET

### **ABSOLUTE MAXIMUM RATINGS**

Rating			Value	Unit
Drain to Current — Continuous			5.5	Α
– Pul	sed	I <sub>DM</sub>	16.5	
Gate-to-Source Voltage	- Continue	$V_{GS}$	±30	V
Total Power Dissipation	TO-251,252	$P_D$	49	W
	TO-220		102	
	TO-220FP		30	
Derate above 25°ℂ	TO-251, 252		0.4	W/°C
	TO-220		1	
	TO-220FP		0.25	
Operating and Storage Temperature Range			-55 to 150	$^{\circ}\!\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-$ T <sub>J</sub> = 25 $^{\circ}$ C				
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 4.5A, L = 10mH, R_G = 25\Omega)$			101.25	mJ
Thermal Resistance — Junction to Case TO-251, 252			2.4	°C/W
TO-220			1.2	
TO220FP			4.4	
<ul><li>Junction to Ambient TO-251,252</li></ul>			120	
TO-220, TO-220FP			62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		TL	260	$^{\circ}\!\mathbb{C}$
ESD SENSITIVITY — HBM, C=100pF, R=1.5kΩ		Vesd	2000	V





### **ORDERING INFORMATION**

Part Number	Package
GPT06N60GN220*	TO-220
GPT06N60GN220FP*	TO-220 Full Package
GPT06N60GN251*	TO-251
GPT06N60GN252*	TO-252

<sup>\*</sup>Note: G: Suffix for Pb Free Product

#### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

				GPT06N60		
Cha	Symbol	Min	Тур	Max	Units	
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	600			V	
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$						
Drain-Source Leakage Current		I <sub>DSS</sub>			1	uA
$(V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Fo	orward	$I_{GSSF}$			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Re	everse	$I_{GSSR}$			100	nA
$(V_{gsr} = -30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	2.5	3.5	4.5	V
$(V_{DS} = V_{GS}, I_{D} = 250 \ \mu A)$						
Static Drain-Source On-Resistan	ce ( $V_{GS}$ = 10 V, $I_D$ = 3.0A) *	R <sub>DS(on)</sub>		1.25	1.45	Ω
Forward Transconductance (V <sub>DS</sub>	= 15 V, I <sub>D</sub> = 3.0 A) *	<b>g</b> FS		5		S
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	C <sub>iss</sub>		953.1		pF
Output Capacitance	f = 1.0 MHz)	Coss		82.1		pF
Reverse Transfer Capacitance		$C_{rss}$		5.88		pF
Turn-On Delay Time	$(V_{DD} = 300 \text{ V}, I_D = 6.0 \text{ A},$	$t_{d(on)}$		18.9		ns
Rise Time	$V_{GS} = 10 \text{ V}.$	t <sub>r</sub>		19.2		ns
Turn-Off Delay Time	$R_{G} = 9.1\Omega$ ) *	$t_{\text{d(off)}}$		34.9		ns
Fall Time	NG - 3.122)	t <sub>f</sub>		19		ns
Total Gate Charge	$(V_{DS} = 480 \text{ V}, I_{D} = 6.0 \text{ A},$ $V_{GS} = 10 \text{ V})^*$	$Q_g$		20.2		nC
Gate-Source Charge		$Q_{gs}$		4.81		nC
Gate-Drain Charge	VGS - 10 V)	$Q_{gd}$		8.21		nC
SOURCE-DRAIN DIODE CHAR	ACTERISTICS					
Forward On-Voltage(1)	/I = 6 0 A	V <sub>SD</sub>			1.5	V
Forward Turn-On Time	$(I_S = 6.0 \text{ A}, d_{IS}/d_t = 100\text{A/µs})$	t <sub>on</sub>		**		ns
Reverse Recovery Time	u <sub>IS</sub> /u <sub>t</sub> – 100Α/μ5)	t <sub>rr</sub>		301		ns

<sup>\*</sup> Pulse Test: Pulse Width  $\leq$ 300 $\mu$ s, Duty Cycle  $\leq$ 2%

<sup>\*\*</sup> Negligible, Dominated by circuit inductance





#### TYPICAL ELECTRICAL CHARACTERISTICS

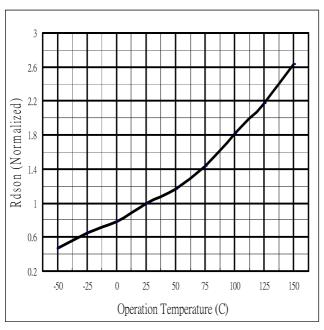


Fig 1. On-Resistance Variation with vs. Temperature

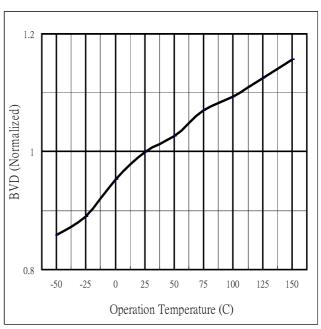


Fig.2 Breakdown Voltage Variation vs. Temperature

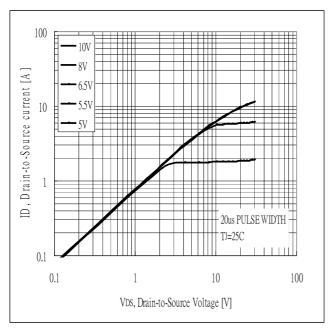


Fig 3. Typical Output Characteristics

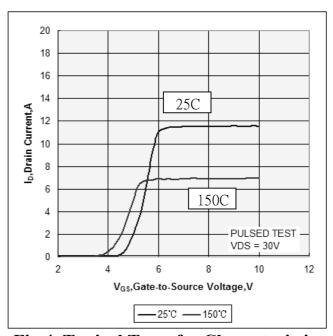


Fig 4. Typical Transfer Characteristics







## Power Field Effect Transistor

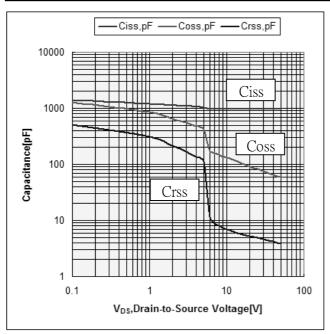


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

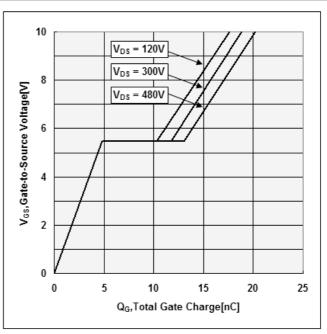


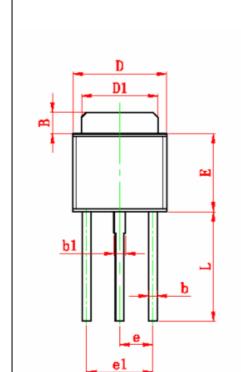
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

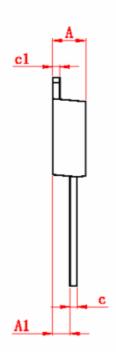




## **PACKAGE DIMENSION**

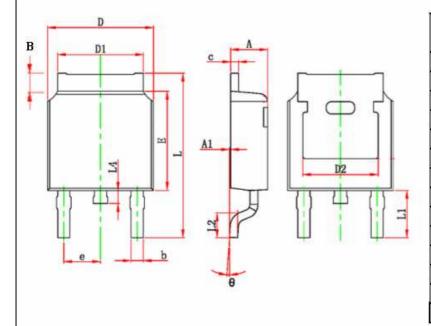
TO-251





Symbol	Dimensions In Millimeters		
	Min.	Max	
А	2.10	2.50	
A1	0.90	1.35	
В	0.90	1.65	
Ъ	0.45	0.75	
b1	0.65	0.95	
С	0.40	0.60	
c1	0.40	0.60	
D	6.30	6.80	
D1	5.00	5.50	
E	5.40	6.30	
е	2.3 TYP.		
e1	4.40	4.80	
L	7.40	8.00	

#### TO-252



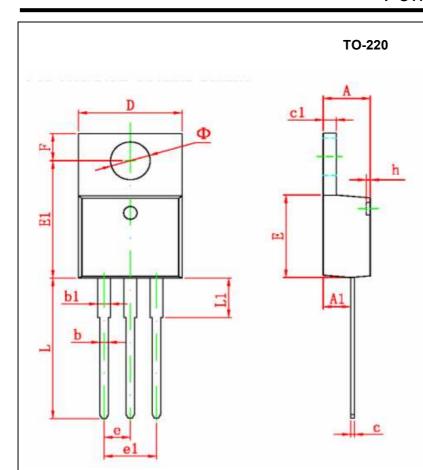
Symbol	Dimensions	In Millimeters
	Min.	Max
Α	2.10	2.50
A1	0.90	1.35
В	0.90	1.65
Ъ	0.45	0.90
С	0.40	0.60
D	6.30	6.80
D1	5.00	5.50
D2	4.83 TYP.	
Е	5.90	6.30
е	2.3 TYP.	
L	9.30	10.50
L2	1.20	1.80
L4	0.60	1.00
$\Theta$	0.00	10.00





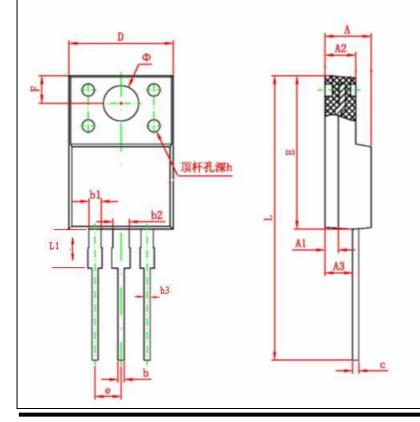
## **GPT06N60**

# Power Field Effect Transistor



Symbol	Dimensions	In Millimeters	
	Min.	Max	
Α	4.40	4.80	
A1	2.10	2.84	
Ъ	0.71	0.91	
b1	1.17	1.37	
С	0.30	0.60	
c1	1.17	1.47	
D	9.40	10.60	
Е	8.40	9.60	
е	2.54 TYP.		
e1	4.90	5.60	
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	12.50	14.00	
L1	3.50	4.00	

#### TO-220FP



Come lo a l	Dimensions :	In Millimete	
Symbol	Min.	Max	
Α	3.80	4.70	
A1	1.3	REF.	
A2	2.20	3.20	
A3	2.10	3.20	
Ъ	0.30	0.95	
b1	1.00	1.75	
b2	1.00	1.75	
b3	0.50	0.80	
С	0.30	0.90	
D	9.90	10.40	
Е	14.60	16.20	
е	2.54 TYP.		
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	28.00	30.00	
L1	3.20	3.55	





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臺灣	深圳
新北市汐止區新台五路一段 96 號 21F	深圳市福田区深南大道 7002 号财富广场 A 座 4V,
21F., No. 96, Sec. 1, Sintai 5th Rd., Sijhih City, Taipei County 22102, Taiwan, R.O.C.	4V, Tower A, Fortune Plaza, No. 7002, Shennan Road, Futian District, Shenzhen City, China PC: 518040
TEL: +886-2-2696 3558	TEL: +86-755-83709176
FAX: +886-2-2696 3559	FAX: +86-755-83709276