

## **GENERAL DESCRIPTION**

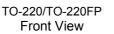
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

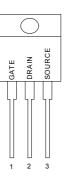
## FEATURES

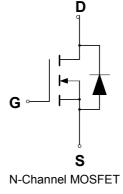
SYMBOL

- Reduced Gate Charge
- Ultra Low On-Resistance Provides Higher Efficiency
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- ▶ I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature

# **PIN CONFIGURATION**







## **ABSOLUTE MAXIMUM RATINGS**

Rating		Value	Unit
Drain to Current – Continuous		6.5	А
- Pulsed	I <sub>DM</sub>	19.5	
Gate-to-Source Voltage – Continue	V <sub>GS</sub>	±30	V
Total Power Dissipation – TO220		156	W
– TO220FP		31	
Derate above 25°C – TO220		1.26	W/°C
– TO220FP		0.33	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy $-T_J$ = 25 $^\circ$ C (V <sub>DD</sub> = 100V, V <sub>GS</sub> = 10V, I <sub>L</sub> = 5.5A, L = 10mH, R <sub>G</sub> = 25Ω)	E <sub>AS</sub>	151.25	mJ
Thermal Resistance – Junction to Case -TO220	θ <sub>JC</sub>	0.8	°C/W
<ul> <li>Junction to Case -TO220FP</li> </ul>		3.9	
<ul> <li>Junction to Ambient -TO220, TO220FP</li> </ul>	$\theta_{JA}$	62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C



POWER FIELD EFFECT TRANSISTOR

#### ORDERING INFORMATION

Part Number	Package	
GPT07N65GN220	TO-220	
GPT07N65GN220FP	TO-220 Full Package	
GPT07N65DGN220FP	TO-220 Full Package	

\*Note: G : Suffix for Pb Free Product

## **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J$  = 25  $^\circ\!\mathrm{C}$  .

			GPT07N65			
Chara	acteristic	Symbol	Min	Тур	Мах	Units
Drain-Source Breakdown Voltage		V <sub>(BR)DSS</sub>	650			V
$(V_{GS} = 0 V, I_D = 250 \mu A)$						
Drain-Source Leakage Current		I <sub>DSS</sub>			1	uA
$(V_{DS} = 650 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Forward		I <sub>GSSF</sub>			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Reverse		I <sub>GSSR</sub>			100	nA
$(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	2.5		4.5	V
$(V_{DS} = V_{GS}, I_{D} = 250 \ \mu A)$						
Static Drain-Source On-Resistance ( $V_{GS}$ = 10 V, $I_{D}$ = 3.5A) *		R <sub>DS(on)</sub>			1.5	Ω
Forward Transconductance ( $V_{DS}$ = 15 V, $I_D$ = 3.5A) *		<b>g</b> <sub>FS</sub>		6		S
Input Capacitance	$(V_{DS} = 25 V, V_{GS} = 0 V)$	C <sub>iss</sub>		1232		pF
Output Capacitance	f = 1.0  MHz	C <sub>oss</sub>		116		pF
Reverse Transfer Capacitance	1 – 1.0 Wi 12)	C <sub>rss</sub>		8.03		pF
Turn-On Delay Time	$(V_{DD} = 325 \text{ V}, I_D = 7 \text{ A},$	t <sub>d(on)</sub>		23.2		ns
Rise Time	$V_{BB} = 323 \text{ V}, \text{ IB} = 7 \text{ A},$ $V_{CS} = 10 \text{ V}.$	t <sub>r</sub>		19.5		ns
Turn-Off Delay Time	$R_{\rm G} = 9.1\Omega$ (*	t <sub>d(off)</sub>		44.8		ns
Fall Time		t <sub>f</sub>		23.5		ns
Total Gate Charge	(1) - 520 (1) - 7	Qg		28.9		nC
Gate-Source Charge	$(V_{DS} = 520 \text{ V}, \text{ I}_D = 7 \text{ A},$ $V_{GS} = 10 \text{ V})^*$	$Q_gs$		6		nC
Gate-Drain Charge		$Q_{gd}$		11		nC
SOURCE-DRAIN DIODE CHARACTE	RISTICS					
Forward On-Voltage(1)	$(1 - 7 \wedge )/ = 0 \rangle/$	V <sub>SD</sub>			1.5	V
Forward Turn-On Time	$(I_{S} = 7A, V_{GS} = 0 V, d_{IS}/d_{t} = 100A/\mu s)$	t <sub>on</sub>		**		ns
Reverse Recovery Time		t <sub>rr</sub>		370		ns

\* Pulse Test: Pulse Width  $\leq$ 300µs, Duty Cycle  $\leq$ 2%

\*\* Negligible, Dominated by circuit inductance



# GPT07N65 / GPT07N65D

Power Field Effect Transistor

# **TYPICAL ELECTRICAL CHARACTERISTICS**

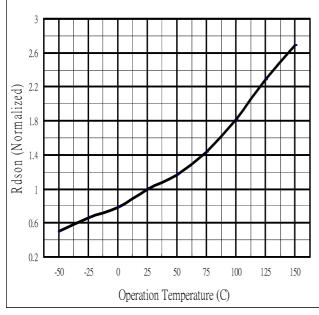


Fig 1. On-Resistance Vs. Temperature

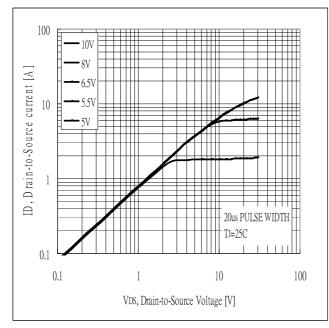


Fig 3. Typical Output Characteristics

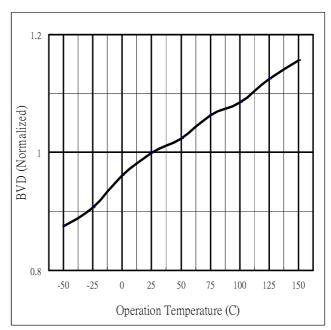


Fig.2 Breakdown Voltage Variation vs. Temperature

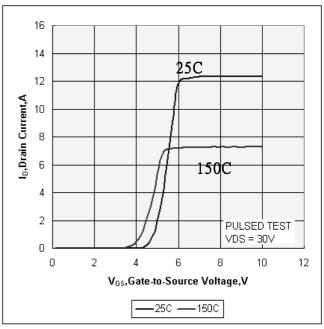


Fig 4. Typical Transfer Characteristics



#### Ciss,pF --Coss,pF — -Crss,pF 10000 -----1000 Capacitance[pF] V Ш 100 10 1 0.1 10 100 1 V<sub>DS</sub>,Drain-to-Source Voltage[V]

Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage



POWER FIELD EFFECT TRANSISTOR

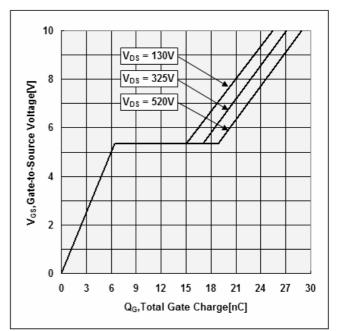


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

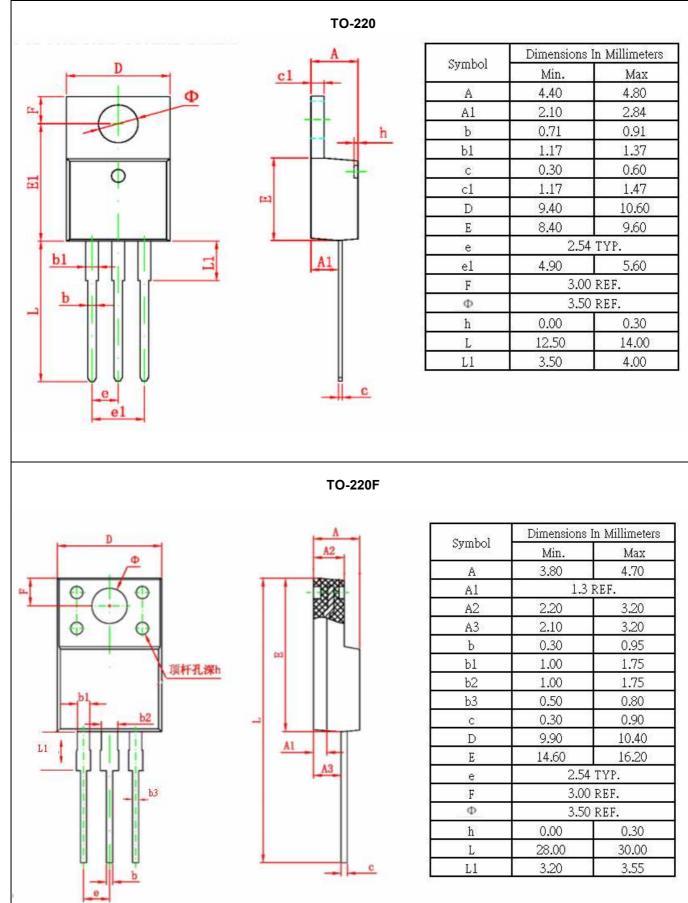




# GPT07N65 / GPT07N65D

POWER FIELD EFFECT TRANSISTOR

## PACKAGE DIMENSION





## **IMPORTANT NOTICE**

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臺灣	深圳
新北市汐止區新台五路一段 96 號 21F	深圳市福田区深南大道 7002 号财富广场 A 座 4V,
21F., No. 96, Sec. 1, Sintai 5th Rd., Sijhih City, Taipei County 22102, Taiwan, R.O.C. TEL: +886-2-2696 3558 FAX: +886-2-2696 3559	4V, Tower A, Fortune Plaza, No. 7002, Shennan Road, Futian District, Shenzhen City, China PC : 518040 TEL: +86-755-83709176 FAX: +86-755-83709276