



#### **GENERAL DESCRIPTION**

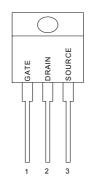
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

#### **FEATURES**

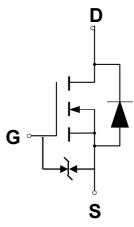
- Robust High Voltage Termination
- Avalanche Energy Specified
- ◆ Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
  - Diode is Characterized for Use in Bridge Circuits
- I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature

#### **PIN CONFIGURATION**

TO-220F/TO220 Top View



#### **SYMBOL**



N-Channel MOSFET

#### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain to Current — Continuous	I <sub>D</sub>	7.8	Α
- Pulsed	I <sub>DM</sub>	23.4	
Gate-to-Source Voltage — Continue	$V_{GS}$	±30	V
Total Power Dissipation(TO220)	P <sub>D</sub>	131	W
(TO220F)		40	
Derate above 25℃ (TO220)		0.9	W/°C
(TO220F)		0.28	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	$^{\circ}$ C
Single Pulse Drain-to-Source Avalanche Energy − T <sub>J</sub> = 25°C E <sub>AS</sub>		211.25	mJ
$(V_{DD} = 100V, V_{GS} = 10V, I_{L} = 6.5A, L = 10mH, R_{G} = 25\Omega)$	∟AS	211.25	1110
Thermal Resistance — Junction to Case (TO220)	$\theta_{JC}$	0.96	°CW
(TO220F)		4.2	
<ul> <li>Junction to Ambient (TO220, TO220F)</li> </ul>	$\theta_{JA}$	62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	$^{\circ}\!\mathbb{C}$
ESD SENSITIVITY — HBM, C=100pF, R=1.5kΩ	Vesd	2000	V





#### **ORDERING INFORMATION**

Part Number	Package
GPT08N50GN220FP*	TO-220F
GPT08N50GN220*	TO-220

<sup>\*</sup>Note: G: Suffix for PB Free Product

### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

				GPT08N50	)	
Characteristic		Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		V <sub>(BR)DSS</sub>	500			V
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$						
Drain-Source Leakage Current		I <sub>DSS</sub>			1	μA
$(V_{DS} = 500 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Fo	orward	$I_{GSSF}$			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Reverse		$I_{GSSR}$			100	nA
$(V_{gsr} = -30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	2.5		4.5	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$	$(V_{DS} = V_{GS}, I_D = 250 \mu A)$					
Static Drain-Source On-Resistance (V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4A) *		R <sub>DS(on)</sub>			0.86	Ω
Forward Transconductance (V <sub>DS</sub>	= 50 V, I <sub>D</sub> = 4A) *	<b>g</b> fs		6		S
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	$C_{iss}$		960.7		pF
Output Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0  MHz)	Coss		106.2		pF
Reverse Transfer Capacitance		$C_{rss}$		6.12		pF
Turn-On Delay Time	$(V_{DD} = 250 \text{ V}, I_D = 8 \text{ A},$ $R_D = 17\Omega,$ $R_G = 6.2\Omega) *$	$t_{d(on)}$		19.5		ns
Rise Time		t <sub>r</sub>		25.6		ns
Turn-Off Delay Time		$t_{d(off)}$		35.5		ns
Fall Time		t <sub>f</sub>		20.8		ns
Total Gate Charge	(V <sub>DS</sub> = 400 V, I <sub>D</sub> = 8 A, V <sub>GS</sub> = 10 V)*	Qg		21.3		nC
Gate-Source Charge		Q <sub>gs</sub>		5.05		nC
Gate-Drain Charge		$Q_{gd}$		9.11		nC
SOURCE-DRAIN DIODE CHARA	ACTERISTICS	<u> </u>		•	•	
Forward On-Voltage(1)	$(I_S = 8 \text{ A}, V_{GS} = 0 \text{ V},$	$V_{SD}$			1.5	V
Forward Turn-On Time		t <sub>on</sub>		**		ns
Reverse Recovery Time	$d_{IS}/d_t = 100A/\mu s)$	t <sub>rr</sub>		293.3		ns

<sup>\*</sup> Pulse Test: Pulse Width  $\leq$ 300 $\mu$ s, Duty Cycle  $\leq$ 2%

<sup>\*\*</sup> Negligible, Dominated by circuit inductance





#### TYPICAL ELECTRICAL CHARACTERISTICS

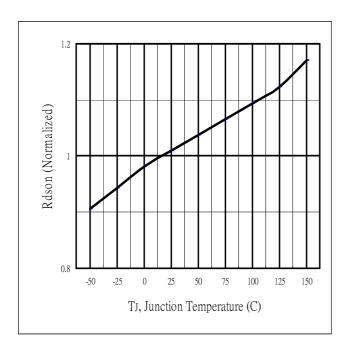


Fig 1. On-Resistance Variation with vs. Temperature

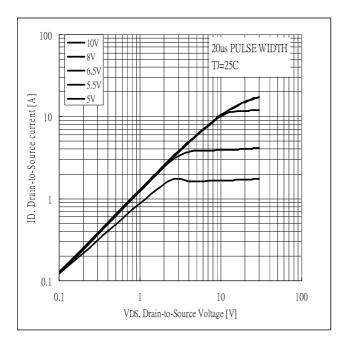


Fig 3. Typical Output Characteristics

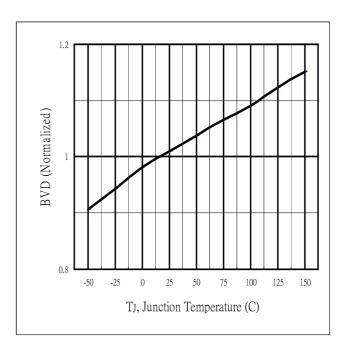


Fig.2 Breakdown Voltage Variation vs. Temperature

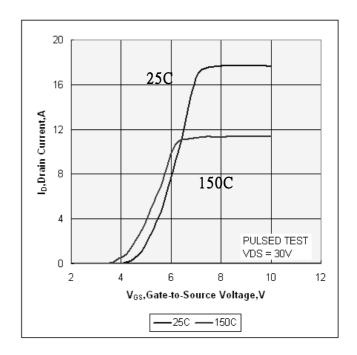
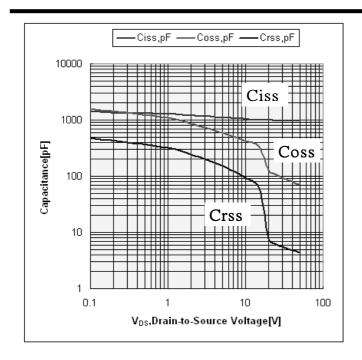


Fig 4. Typical Transfer Characteristics





## Power Field Effect Transistor



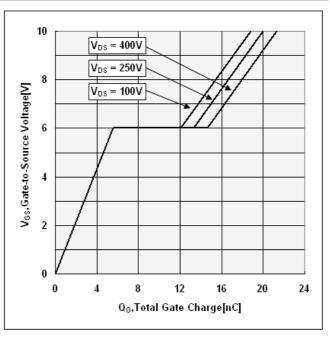


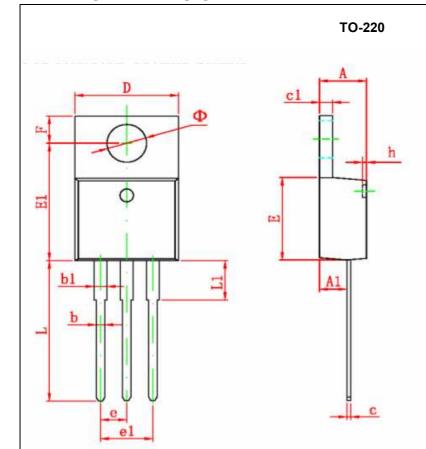
Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage



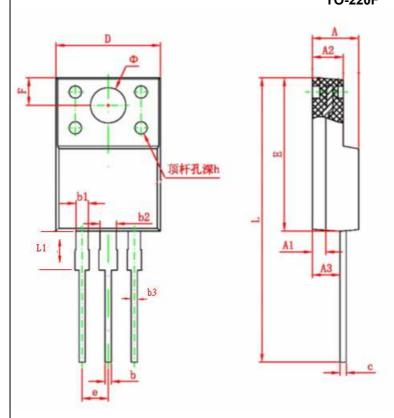


### **PACKAGE DIMENSION**



Symbol	Dimensions 1	In Millimeters
	Min.	Max
Α	4.40	4.80
A1	2.10	2.84
Ъ	0.71	0.91
b1	1.17	1.37
С	0.30	0.60
c1	1.17	1.47
D	9.40	10.60
Е	8.40	9.60
е	2.54 TYP.	
e1	4.90	5.60
F	3.00 REF.	
Φ	3.50 REF.	
h	0.00	0.30
L	12.50	14.00
L1	3.50	4.00

#### TO-220F



Symbol	Dimensions In Millimeters		
	Min.	Max	
Α	3.80	4.70	
A1	1.3 REF.		
A2	2.20	3.20	
A3	2.10	3.20	
Ь	0.30	0.95	
b1	1.00	1.75	
b2	1.00	1.75	
b3	0.50	0.80	
С	0.30	0.90	
D	9.90	10.40	
E	14.60	16.20	
е	2.54 TYP.		
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	28.00	30.00	
L1	3,20	3.55	



### **GPT08N50**

# Power Field Effect Transistor

#### **IMPORTANT NOTICE**

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