



Power Field Effect Transistor

GENERAL DESCRIPTION

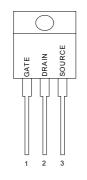
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

FEATURES

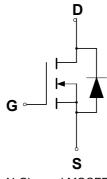
- Reduced Gate Charge
- ◆ Ultra Low On-Resistance Provides Higher Efficiency
- ◆ Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- ♦ I_{DSS} and V_{DS}(on) Specified at Elevated Temperature

PIN CONFIGURATION

TO-220/TO-220FP Front View



SYMBOL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS

Rating		Value	Unit
Drain to Current — Continuous		7.5	Α
- Pulsed	I _{DM}	22.5	
Gate-to-Source Voltage — Continue	V_{GS}	±30	V
Total Power Dissipation – TO220	P _D	145	W
– TO220FP		43	
Derate above 25℃ - TO220		1.15	W/°C
– TO220FP		0.33	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to 150	$^{\circ}\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}$ C ($V_{DD} = 100V$, $V_{GS} = 10V$, $I_L = 7A$, $L = 10mH$, $R_G = 25\Omega$)		245	mJ
Thermal Resistance — Junction to Case -TO220	θ _{JC}	0.87	°C W
 Junction to Case -TO220FP 		3.8	
 Junction to Ambient -TO220, TO220FP 	θ_{JA}	62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		260	$^{\circ}\!\mathbb{C}$





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ORDERING INFORMATION

Part Number	Package
GPT08N60GN220	TO-220
GPT08N60GN220FP	TO-220 Full Package
GPT08N60DGN220FP	TO-220 Full Package

^{*}Note: G : Suffix for Pb Free Product

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $T_J = 25^{\circ}C$.

				GPT08N60		
Characteristic		Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		V _{(BR)DSS}	600			V
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$						
Drain-Source Leakage Current		I _{DSS}			1	uA
$(V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Forward		I _{GSSF}			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Reverse		I _{GSSR}			100	nA
$(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	2.5	3.5	4.5	V
$(V_{DS} = V_{GS}, I_{D} = 250 \ \mu A)$						
Static Drain-Source On-Resistance (V _{GS} = 10 V, I _D = 4A) *		R _{DS(on)}		1.07	1.22	Ω
Forward Transconductance (V _{DS} = 15 V, I _D = 4A) *		g FS		7		S
Input Capacitance	$(V_{DS} = 25 \text{ V. } V_{GS} = 0 \text{ V.}$	C _{iss}		961.7		pF
Output Capacitance	(20 - , 00 - ,	C _{oss}		98.6		pF
Reverse Transfer Capacitance	f = 1.0 MHz)	C _{rss}		6.89		pF
Turn-On Delay Time	// - 200 \/ I - 0 A	t _{d(on)}		19.3		ns
Rise Time	$(V_{DD} = 300 \text{ V}, I_{D} = 8 \text{ A}, V_{GS} = 10 \text{ V}.$	t _r		23		ns
Turn-Off Delay Time	,	t _{d(off)}		37.6		ns
Fall Time	$R_{G} = 9.1\Omega) *$	t _f		16		ns
Total Gate Charge	$(V_{DS} = 480 \text{ V}, I_{D} = 8 \text{ A},$	Q_g		22.6		nC
Gate-Source Charge		Q _{gs}		5.07		nC
Gate-Drain Charge	$V_{GS} = 10 \text{ V})^*$	Q_{gd}		10.1		nC
SOURCE-DRAIN DIODE CHARACTE	RISTICS	-				
Forward On-Voltage(1)	$(I_S = 8A, V_{GS} = 0 \text{ V},$	V_{SD}			1.5	V
Forward Turn-On Time		t _{on}		**		ns
Reverse Recovery Time	$d_{IS}/d_t = 100A/\mu s)$	t _{rr}		381		ns

^{*} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%

^{**} Negligible, Dominated by circuit inductance



POWER FIELD EFFECT TRANSISTOR

TYPICAL ELECTRICAL CHARACTERISTICS

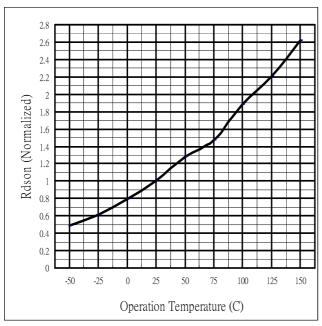


Fig 1. On-Resistance Vs. Temperature

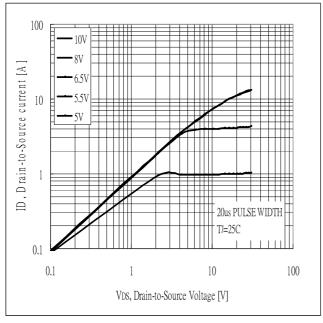


Fig 3. Typical Output Characteristics

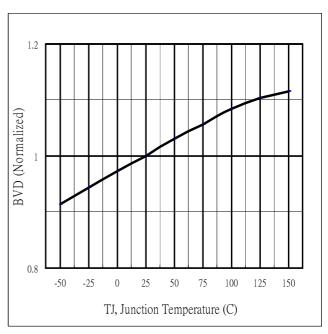


Fig.2 Breakdown Voltage Variation vs. Temperature

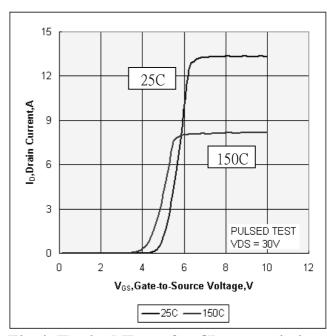


Fig 4. Typical Transfer Characteristics





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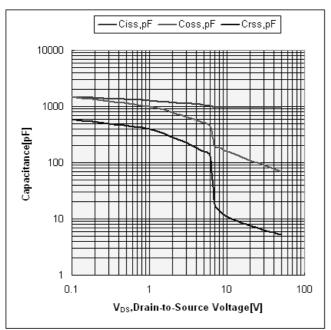


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

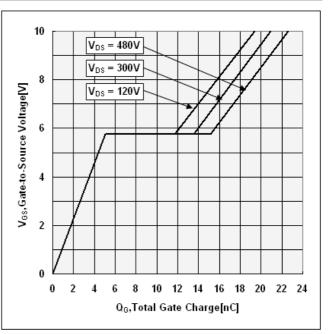


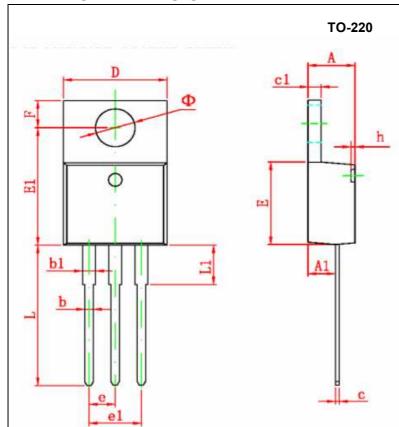
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage





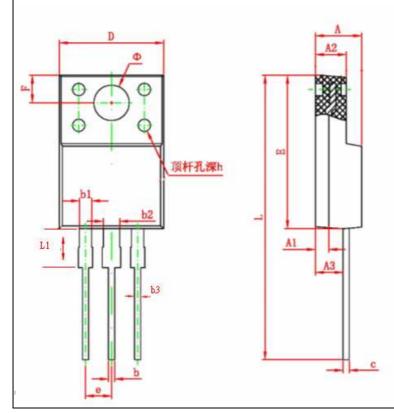
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PACKAGE DIMENSION



Count al	Dimensions 1	In Millimeters
Symbol	Min.	Max
Α	4.40	4.80
A1	2.10	2.84
Ъ	0.71	0.91
b1	1.17	1.37
С	0.30	0.60
c1	1.17	1.47
D	9.40	10.60
Е	8.40	9.60
е	2.54 TYP.	
e1	4.90	5.60
F	3.00 REF.	
Φ	3.50 REF.	
h	0.00	0.30
L	12.50	14.00
L1	3.50	4.00

TO-220F



Council al	Dimensions :	In Millimeters
Symbol	Min.	Max
Α	3.80	4.70
A1	1.3 REF.	
A2	2.20	3.20
A3	2.10	3,20
Ъ	0.30	0.95
b1	1.00	1.75
b2	1.00	1.75
b3	0.50	0.80
C	0.30	0.90
D	9.90	10.40
E	14.60	16.20
е	2.54 TYP.	
F	3.00	REF.
Φ	3.50 REF.	
h	0.00	0.30
L	28.00	30.00
L1	3.20	3.55





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