



Power Field Effect Transistor

GENERAL DESCRIPTION

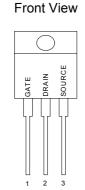
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

FEATURES

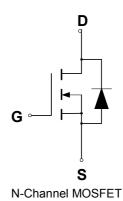
- Reduced Gate Charge
- ◆ Ultra Low On-Resistance Provides Higher Efficiency
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- ◆ I_{DSS} and V_{DS}(on) Specified at Elevated Temperature

PIN CONFIGURATION

SYMBOL



TO-220/TO-220FP



ABSOLUTE MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--|-----------------|------------|------------------------|
| Drain to Current — Continuous | I_D | 10.5 | Α |
| - Pulsed | I_{DM} | 31.5 | |
| Gate-to-Source Voltage — Continue | V_{GS} | ±30 | V |
| Total Power Dissipation – TO220 | P_D | 193 | W |
| – TO220FP | | 38 | |
| Derate above 25℃ - TO220 | | 1.5 | W/°C |
| – TO220FP | | 0.36 | |
| Operating and Storage Temperature Range | T_J, T_{STG} | -55 to 150 | $^{\circ}$ C |
| Single Pulse Drain-to-Source Avalanche Energy $-T_J$ = 25 $^{\circ}$ C | E _{AS} | 405 | mJ |
| $(V_{DD} = 100V, V_{GS} = 10V, I_{L} = 9A, L = 10mH, R_{G} = 25\Omega)$ | | | |
| Thermal Resistance — Junction to Case -TO220 | θ_{JC} | 0.63 | °C/W |
| Junction to Case -TO220FP | | 3.3 | |
| Junction to Ambient -TO220, TO220FP | θ_{JA} | 62.5 | |
| Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds | TL | 260 | $^{\circ}\!\mathbb{C}$ |





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ORDERING INFORMATION

| Part Number | Package |
|------------------|---------------------|
| GPT11N65GN220 | TO-220 |
| GPT11N65DGN220FP | TO-220 Full Package |

^{*}Note: G : Suffix for Pb Free Product

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $T_J = 25^{\circ}C$.

| | | | | GPT11N65 | | |
|---|---|---------------------|-----|----------|------|-------|
| Char | acteristic | Symbol | Min | Тур | Max | Units |
| Drain-Source Breakdown Voltage | | $V_{(BR)DSS}$ | 650 | | | V |
| $(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$ | | | | | | |
| Drain-Source Leakage Current | | I _{DSS} | | | 1 | uA |
| $(V_{DS} = 650 \text{ V}, V_{GS} = 0 \text{ V})$ | | | | | | |
| Gate-Source Leakage Current-Forward | d | I_{GSSF} | | | 100 | nA |
| $(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$ | | | | | | |
| Gate-Source Leakage Current-Revers | e | I _{GSSR} | | | 100 | nA |
| $(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$ | | | | | | |
| Gate Threshold Voltage | | $V_{GS(th)}$ | 3 | | 5 | V |
| $(V_{DS} = V_{GS}, I_{D} = 250 \ \mu A)$ | | | | | | |
| Static Drain-Source On-Resistance (Va | _{GS} = 10 V, I _D = 5.5A) * | R _{DS(on)} | | | 0.82 | Ω |
| Forward Transconductance (V _{DS} = 15 | V, I _D = 5.5A) * | g FS | | 10 | | S |
| Input Capacitance | $(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ | C _{iss} | | 1715 | | pF |
| Output Capacitance | $(v_{DS} - 25 \text{ V}, v_{GS} - 0 \text{ V},$ f = 1.0 MHz) | C _{oss} | | 154 | | pF |
| Reverse Transfer Capacitance | 1 – 1.0 MH2) | C _{rss} | | 10 | | pF |
| Turn-On Delay Time | // - 225 // L - 44 A | t _{d(on)} | | 30.9 | | ns |
| Rise Time | $(V_{DD} = 325 \text{ V}, I_D = 11 \text{ A}, V_{GS} = 10 \text{ V}.$ | t _r | | 26.4 | | ns |
| Turn-Off Delay Time | $V_{GS} = 10 \text{ V},$ $R_{G} = 9.1\Omega) *$ | $t_{\text{d(off)}}$ | | 52.7 | | ns |
| Fall Time | $R_G = 9.1\Omega$) | t _f | | 32.1 | | ns |
| Total Gate Charge | 0/ 500 // 1 44 A | Q_g | | 34.8 | | nC |
| Gate-Source Charge | $(V_{DS} = 520 \text{ V}, I_D = 11 \text{ A},$ | Q_{gs} | | 10.5 | | nC |
| Gate-Drain Charge | $V_{GS} = 10 \text{ V})^*$ | Q_{gd} | | 15 | | nC |
| SOURCE-DRAIN DIODE CHARACTE | RISTICS | <u> </u> | • | • | | |
| Forward On-Voltage(1) | 444.14 | V _{SD} | | | 1.5 | V |
| Forward Turn-On Time | $(I_S = 11A, V_{GS} = 0 V,$ | t _{on} | | ** | | ns |
| Reverse Recovery Time | $d_{IS}/d_t = 100A/\mu s)$ | t _{rr} | | 445.3 | | ns |

^{*} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%

^{**} Negligible, Dominated by circuit inductance



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TYPICAL ELECTRICAL CHARACTERISTICS

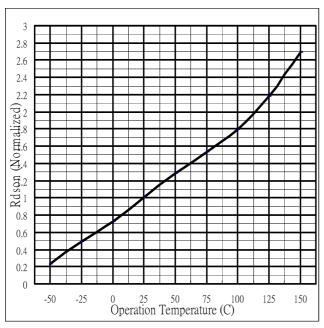


Fig 1. On-Resistance Vs. Temperature

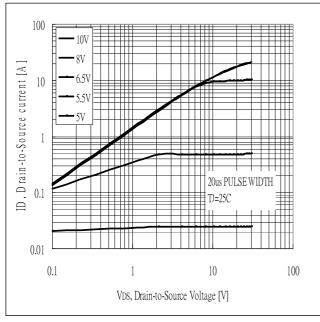


Fig 3. Typical Output Characteristics

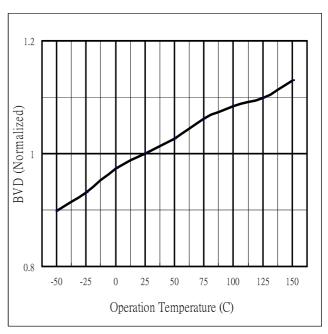


Fig.2 Breakdown Voltage Variation vs. Temperature

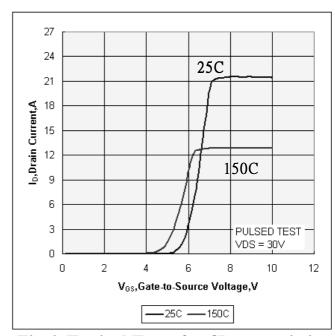


Fig 4. Typical Transfer Characteristics





POWER FIELD EFFECT TRANSISTOR

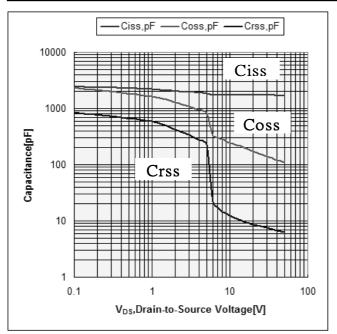


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

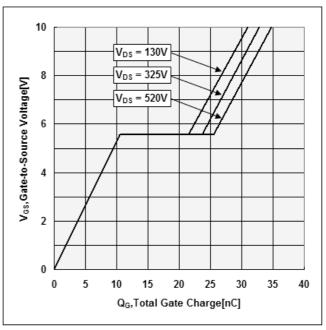


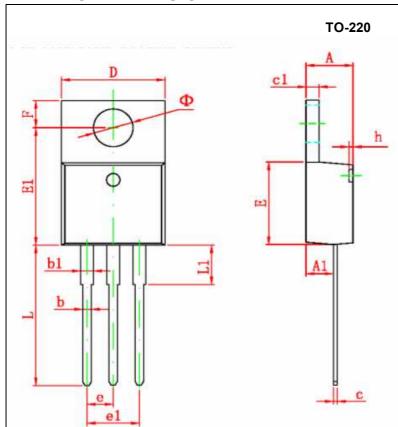
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage





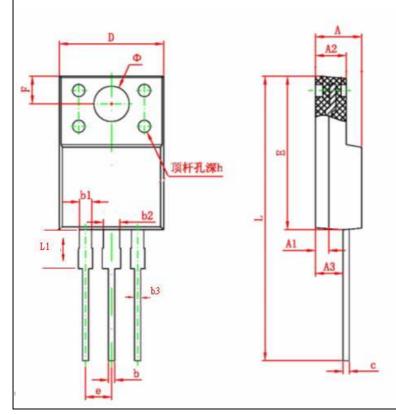
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PACKAGE DIMENSION



| C11 | Dimensions In Millimete | | |
|--------|-------------------------|-------|--|
| Symbol | Min. | Max | |
| Α | 4.40 | 4.80 | |
| A1 | 2.10 | 2.84 | |
| Ъ | 0.71 | 0.91 | |
| b1 | 1.17 | 1.37 | |
| С | 0.30 | 0.60 | |
| c1 | 1.17 | 1.47 | |
| D | 9.40 | 10.60 | |
| Е | 8.40 | 9.60 | |
| е | 2.54 TYP. | | |
| e1 | 4.90 | 5.60 | |
| F | 3.00 | REF. | |
| Φ | 3.50 REF. | | |
| h | 0.00 | 0.30 | |
| L | 12.50 | 14.00 | |
| L1 | 3.50 | 4.00 | |

TO-220F



| C11 | Dimensions In Millimet | | |
|--------|------------------------|-------|--|
| Symbol | Min. | Max | |
| Α | 3.80 | 4.70 | |
| A1 | 1.3 | REF. | |
| A2 | 2.20 | 3.20 | |
| А3 | 2.10 | 3.20 | |
| Ъ | 0.30 | 0.95 | |
| b1 | 1.00 | 1.75 | |
| b2 | 1.00 | 1.75 | |
| b3 | 0.50 | 0.80 | |
| С | 0.30 | 0.90 | |
| D | 9.90 | 10.40 | |
| Е | 14.60 | 16.20 | |
| е | 2.54 TYP. | | |
| F | 3.00 REF. | | |
| Φ | 3.50 REF. | | |
| h | 0.00 | 0.30 | |
| L | 28.00 | 30.00 | |
| L1 | 3.20 | 3.55 | |





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IMPORTANT NOTICE

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