



### POWER FIELD EFFECT TRANSISTOR

#### **GENERAL DESCRIPTION**

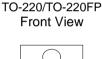
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

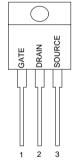
#### **FEATURES**

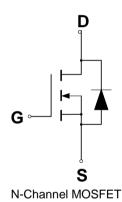
- Reduced Gate Charge
- ◆ Ultra Low On-Resistance Provides Higher Efficiency
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- ◆ I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature

#### PIN CONFIGURATION

### **SYMBOL**







### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain to Current — Continuous		10.5	Α
- Pulsed	I <sub>DM</sub>	31.5	
Gate-to-Source Voltage — Continue	$V_{GS}$	±30	V
Total Power Dissipation – TO220	P <sub>D</sub>	220	W
– TO220FP		40	
Derate above 25℃ - TO220		1.46	W/°C
– TO220FP		0.38	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	$^{\circ}\!\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}$ C ( $V_{DD} = 100V$ , $V_{GS} = 10V$ , $I_L = 10A$ , $L = 10mH$ , $R_G = 25$ )	E <sub>AS</sub>	500	mJ
Thermal Resistance — Junction to Case -TO220	JC	0.52	°CW
<ul> <li>Junction to Case -TO220FP</li> </ul>		3.2	
<ul> <li>Junction to Ambient -TO220, TO220FP</li> </ul>	JA	62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	$^{\circ}\!\mathbb{C}$





# Power Field Effect Transistor

### ORDERING INFORMATION

Part Number	Package
GPT11N70GN220	TO-220
GPT11N70DGN220FP	TO-220 Full Package

<sup>\*</sup>Note: G : Suffix for Pb Free Product

### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

			GPT11N70 / GPT11N70D			
Characteristic		Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	700			V
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$						
Drain-Source Leakage Current		I <sub>DSS</sub>			1	uA
$(V_{DS} = 700 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Forward		$I_{GSSF}$			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Reverse		$I_{GSSR}$			100	nA
$(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	3		5	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$						
Static Drain-Source On-Resistance (\	$I_{GS} = 10 \text{ V}, I_D = 5.5 \text{A}) *$	R <sub>DS(on)</sub>			0.96	
Forward Transconductance (V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5.5A) *		<b>g</b> FS		10		S
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}.$	$C_{iss}$		1702.8	1702.8	pF
Output Capacitance	( 20 - 7 00 - 7	C <sub>oss</sub>		147.8	147.8	pF
Reverse Transfer Capacitance	f = 1.0 MHz)	C <sub>rss</sub>		9.94	9.94	pF
4Turn-On Delay Time	0/ 050 // 1 44 4	t <sub>d(on)</sub>		27.5	27.5	ns
Rise Time	$(V_{DD} = 350 \text{ V}, I_D = 11 \text{ A},$	t <sub>r</sub>		30.3	20.3	ns
Turn-Off Delay Time	$V_{GS} = 10 \text{ V},$ $R_{G} = 9.1 \text{ ) *}$	t <sub>d(off)</sub>	60.3	47.47	60.3	ns
Fall Time		t <sub>f</sub>	26.4	44.4	26.4	ns
Total Gate Charge	(V <sub>DS</sub> = 560 V, I <sub>D</sub> = 11 A, V <sub>GS</sub> = 10 V)*	$Q_g$		38.5	38.5	nC
Gate-Source Charge		$Q_{gs}$		8.53	8.53	nC
Gate-Drain Charge		$Q_gd$		17.1	17.1	nC
SOURCE-DRAIN DIODE CHARACT	ERISTICS		•	•	•	•
Forward On-Voltage(1)	$(I_{S} = 11A, V_{GS} = 0 V, d_{IS}/d_{t} = 100A/\mu s)$	V <sub>SD</sub>			1.5	V
Forward Turn-On Time		t <sub>on</sub>		**		ns
Reverse Recovery Time		t <sub>rr</sub>		458.7		ns

<sup>\*</sup> Pulse Test: Pulse Width  $\leq$ 300 $\mu$ s, Duty Cycle  $\leq$ 2%

<sup>\*\*</sup> Negligible, Dominated by circuit inductance



### TYPICAL ELECTRICAL CHARACTERISTICS

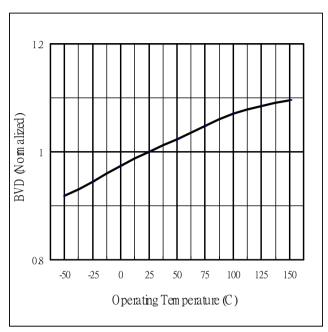


Fig 1. On-Resistance Vs. Temperature

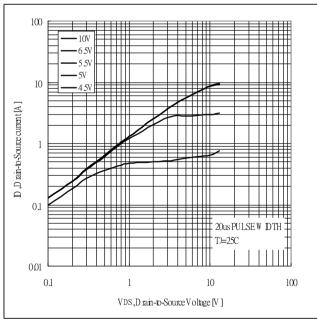


Fig 3. Typical Output Characteristics

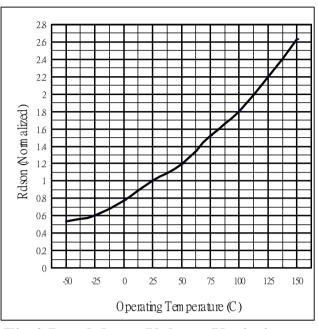


Fig.2 Breakdown Voltage Variation vs. Temperature

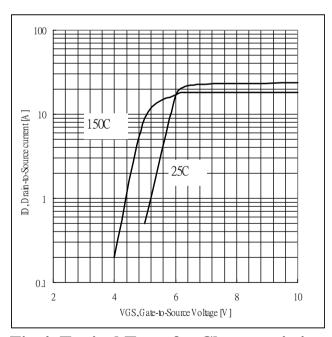


Fig 4. Typical Transfer Characteristics





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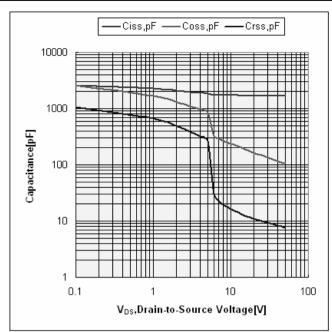


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

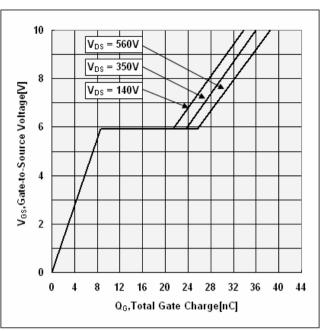


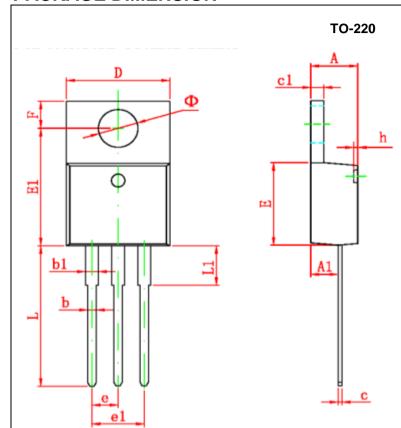
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage





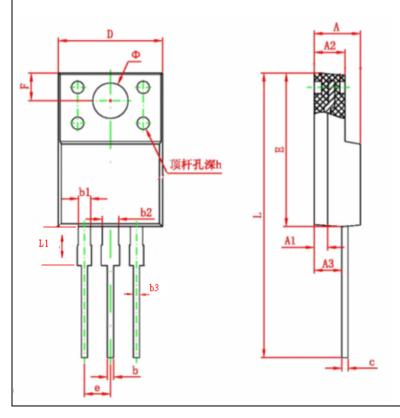
## Power Field Effect Transistor

### **PACKAGE DIMENSION**



Creen had	Dimensions In Millimeters		
Symbol	Min.	Max	
Α	4.40	4.80	
A1	2.10	2.84	
ь	0.71	0.91	
b1	1.17	1.37	
С	0.30	0.60	
c1	1.17	1.47	
D	9.40	10.60	
E	8.40	9.60	
e	2.54 TYP.		
e1	4.90	5.60	
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	12.50	14.00	
L1	3.50	4.00	

### TO-220F



C b al	Dimensions In Millimeter		
Symbol	Min.	Max	
Α	3.80	4.70	
A1	1.3 REF.		
A2	2.20	3.20	
A3	2.10	3.20	
Ъ	0.30	0.95	
b1	1.00	1.75	
b2	1.00	1.75	
b3	0.50	0.80	
С	0.30	0.90	
D	9.90	10.40	
E	14.60	16.20	
е	2.54 TYP.		
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	28.00	30.00	
L1	3.20	3.55	





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