

## **GENERAL DESCRIPTION**

This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

## **PIN CONFIGURATION**

### FEATURES

- Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature
- Ciss improvement



## SYMBOL



N-Channel MOSFET

## **ABSOLUTE MAXIMUM RATINGS**

| Rating   | Symbol          | Value      | Unit |
|--|-----------------|------------|------|
| Drain to Current – Continuous  |                 | 13         | А    |
| - Pulsed   |                 | 39         |      |
| Gate-to-Source Voltage – Continue  |                 | ±30        | V    |
| Total Power Dissipation – TO220  |                 | 193        | W    |
| – TO220FP  |                 | 47         |      |
| Derate above 25℃ – TO220   |                 | 1.54       | W/°C |
| – TO220FP  |                 | 0.38       |      |
| Operating and Storage Temperature Range  |                 | -55 to 150 | °C   |
| Single Pulse Drain-to-Source Avalanche Energy $-$ T <sub>J</sub> = 25 $^\circ\!{ m C}$ |                 | 605        | mJ   |
| $(V_{DD} = 100V, V_{GS} = 10V, I_{L} = 11A, L = 10mH, R_{G} = 25\Omega)$               |                 |            |      |
| Thermal Resistance – Junction to Case -TO220   | θ <sub>JC</sub> | 0.64       | °C/W |
| <ul> <li>Junction to Case -TO220FP</li> </ul>  |                 | 3.7        |      |
| <ul> <li>Junction to Ambient -TO220, TO220FP</li> </ul>                                | $\theta_{JA}$   | 62.5       |      |
| Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds         | TL              | 260        | °C   |
| ESD SENSITIVITY – HBM, C=100pF, R=1.5kΩ  |                 | 2000       | V    |



## **ORDERING INFORMATION**

| Part Number        | Package |
|--------------------|---------|
| GPT13N50CGN220*    | TO-220  |
| GPT13N50CDGN220FP* | TO-220F |

\*Note: G : Suffix for PB Free Product

## **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

|   |  |                      | (   | GPT13N50 | С    |       |
|---|--|----------------------|-----|----------|------|-------|
| Cha   | racteristic  | Symbol               | Min | Тур      | Max  | Units |
| Drain-Source Breakdown Voltage  |  | V <sub>(BR)DSS</sub> | 500 |          |      | V     |
| $(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$                     |  |                      |     |          |      |       |
| Drain-Source Leakage Current  |  | I <sub>DSS</sub>     |     |          |      | μA    |
| (V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V)                        |  |                      |     |          | 1    |       |
| (V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125℃) |  |                      |     |          | 10   |       |
| Gate-Source Leakage Current-Fo  | prward   | I <sub>GSSF</sub>    |     |          | 100  | nA    |
| $(V_{gsf} = 30 \text{ V}, \text{ V}_{DS} = 0 \text{ V})$                |  |                      |     |          |      |       |
| Gate-Source Leakage Current-Re  | Gate-Source Leakage Current-Reverse  |                      |     |          | 100  | nA    |
| $(V_{gsr} = -30 \text{ V}, \text{ V}_{DS} = 0 \text{ V})$               |  |                      |     |          |      |       |
| Gate Threshold Voltage  |  | V <sub>GS(th)</sub>  | 3.2 |          | 5.2  | V     |
| $(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$                                  |  |                      |     |          |      |       |
| Static Drain-Source On-Resistance ( $V_{GS}$ = 10 V, $I_D$ = 6.5A) *    |  | R <sub>DS(on)</sub>  |     |          | 0.49 | Ω     |
| Forward Transconductance (V <sub>DS</sub>                               | = 15 V, I <sub>D</sub> = 6.5A) *   | <b>g</b> fs          |     | 8.7      |      | S     |
| Input Capacitance   | (1/22 - 25)/(1/22 - 0)/(1/22)  | C <sub>iss</sub>     |     | 1578     |      | pF    |
| Output Capacitance  | $(V_{DS} = 25 V, V_{GS} = 0 V,$  | Coss                 |     | 180      |      | pF    |
| Reverse Transfer Capacitance  | $I = I.0 W \Pi Z$  | C <sub>rss</sub>     |     | 9.8      |      | pF    |
| Turn-On Delay Time  | $(V_{DD}$ = 250 V, I <sub>D</sub> = 13 A,<br>R <sub>G</sub> = 25Ω) *                       | t <sub>d(on)</sub>   |     | 33       |      | ns    |
| Rise Time   |  | tr                   |     | 59       |      | ns    |
| Turn-Off Delay Time   |  | t <sub>d(off)</sub>  |     | 75       |      | ns    |
| Fall Time   |  | t <sub>f</sub>       |     | 34.8     |      | ns    |
| Total Gate Charge   | $(V_{DS} = 400 \text{ V}, I_D = 13 \text{ A}, V_{GS} = 10 \text{ V})^*$                    | Qg                   |     | 31       |      | nC    |
| Gate-Source Charge  |  | Q <sub>gs</sub>      |     | 8.7      |      | nC    |
| Gate-Drain Charge   |  | Q <sub>gd</sub>      |     | 12       |      | nC    |
| SOURCE-DRAIN DIODE CHAR   | ACTERISTICS  |                      |     |          |      |       |
| Forward On-Voltage(1)   |  | V <sub>SD</sub>      |     |          | 1.5  | V     |
| Forward Turn-On Time  | $(I_{S} = 13 \text{ A}, V_{GS} = 0 \text{ V}, \\ d_{IS}/d_{t} = 100 \text{A}/\mu\text{s})$ | t <sub>on</sub>      |     | **       |      | ns    |
| Reverse Recovery Time   |  | t <sub>rr</sub>      |     | 450      |      | ns    |

\* Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%

\*\* Negligible, Dominated by circuit inductance



## **GPT13N50C / GPT13N50CD** Power Field Effect Transistor

## **TYPICAL ELECTRICAL CHARACTERISTICS**



Fig 1. On-Resistance Variation with vs. Temperature



Fig.2 Breakdown Voltage Variation vs. Temperature



Fig 3. Typical Output Characteristics



Fig 5. Typical Transfer Characteristics



## GPT13N50C / GPT13N50CD

POWER FIELD EFFECT TRANSISTOR



Fig 6. Typical Capacitance Vs. Gate-to-Source Voltage



Fig 6. Typical Gate Charge Vs. Drain-to-Source Voltage



# GPT13N50C / GPT13N50CD

POWER FIELD EFFECT TRANSISTOR

Max

4.80

2.84

0.91

1.37

0.60

1.47

10.60

9.60

5.60

0.30

14.00

4.00

Max

4.70

3.20

3.20

0.95

1.75

1.75

0.80

0.90

10.40

16.20

0.30

30.00

3.55

## PACKAGE DIMENSION

TO-220





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