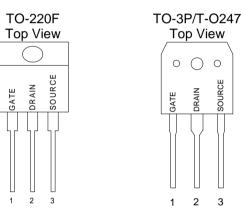


GENERAL DESCRIPTION

This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

PIN CONFIGURATION



FEATURES

- **Robust High Voltage Termination**
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a **Discrete Fast Recovery Diode**
- Diode is Characterized for Use in Bridge Circuits
- I_{DSS} and V_{DS}(on) Specified at Elevated Temperature ٠
- Isolated Mounting Hole Reduces Mounting Hardware

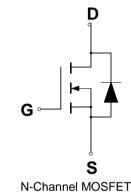
SYMBOL

0 \bigcirc

SOURCE

DRAIN

2 3



ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Current – Continuous		13	А
- Pulsed	I _{DM}	39	
Gate-to-Source Voltage – Continue	V _{GS}	±30	V
Total Power Dissipation – TO220FP		45	W
– TO3P		240	W/°C
Derate above 25°C – TO220FP		0.36	
– TO3P		1.9	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}C$ (V _{DD} = 100V, V _{GS} = 10V, I _L = 10.5A, L = 10mH, R _G = 25)	E _{AS}	551.25	mJ
Thermal Resistance – Junction to Case -TO220FP	JC	3.18	°C/W
 Junction to Case -TO3P 		0.48	
 Junction to Ambient -TO220FP 	JA	62.5	
 Junction to Ambient -TO3P 		40	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	ΤL	260	°C
ESD SENSITIVITY – HBM, C=100pF, R=1.5k		2000	V



POWER FIELD EFFECT TRANSISTOR

ORDERING INFORMATION

Part Number	Package	
GPT13N65GN3P*	TO-3P	
GPT13N65GN247*	TO-247	
GPT13N65DGN220FP*	TO-220F	
*Neter O - Outfiniter DD Free Dreduct		

*Note: G : Suffix for PB Free Product

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $T_J=25\,{}^\circ\!\mathrm{C}$.

			GP13N65			
Characteristic		Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		N/	650			V
$(V_{GS} = 0 V, I_D = 250 \mu A)$		V _{(BR)DSS}	030			V
Drain-Source Leakage Current		I _{DSS}			1	uA
$(V_{DS} = 650 \text{ V}, \text{ V}_{GS} = 0 \text{ V})$		IDSS			1	uA
Gate-Source Leakage Current-Forward		IGSSE			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$	$0 \text{ V}, \text{ V}_{\text{DS}} = 0 \text{ V}$				100	ПА
Gate-Source Leakage Current-Reve	Leakage Current-Reverse				100	nA
$(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$		I _{GSSR}			100	114
Gate Threshold Voltage		V _{GS(th)}	3		5	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$			Ŭ		0	v
Static Drain-Source On-Resistance (V_{GS} = 10 V, I_{D} = 6.5A) *		R _{DS(on)}			0.62	
Forward Transconductance ($V_{DS} = 50 \text{ V}$, $I_D = 6.5\text{A}$) *		g _{FS}		11		S
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{CS} = 0 \text{ V}.$	C _{iss}		2275		pF
Output Capacitance	$(v_{DS} = 25 \text{ V}, v_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz})$	C _{oss}		205.2		pF
Reverse Transfer Capacitance		C _{rss}		10.2		pF
Turn-On Delay Time	$(V_{DD} = 325 \text{ V}, I_D = 13 \text{ A}, R_G = 25)^*$	t _{d(on)}		36.3		ns
Rise Time		tr		67.3		ns
Turn-Off Delay Time		t _{d(off)}		55.8		ns
Fall Time		t _f		37.9		ns
Total Gate Charge	$(V_{DS} = 520 \text{ V}, I_D = 13 \text{ A}, V_{GS} = 10 \text{ V})^*$	Q _g		42.8		nC
Gate-Source Charge		Q _{gs}		12.8		nC
Gate-Drain Charge		Q _{gd}		17.9		nC
	SOURCE-DRAIN DIODE CHA	RACTERISTICS				
Forward On-Voltage(1)		V _{SD}			1.5	V
Forward Turn-On Time		t _{on}		**		ns
Reverse Recovery Time		t _{rr}		486		ns

* Pulse Test: Pulse Width $\ \leq 300 \mu s,$ Duty Cycle $\ \leq 2\%$

** Negligible, Dominated by circuit inductance



POWER FIELD EFFECT TRANSISTOR

TYPICAL ELECTRICAL CHARACTERISTICS

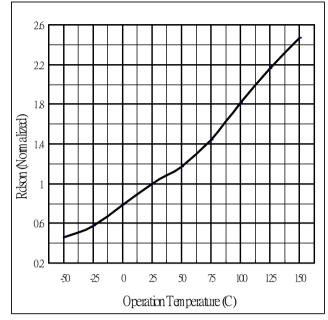


Fig 1. On-Resistance Variation with vs. Temperature

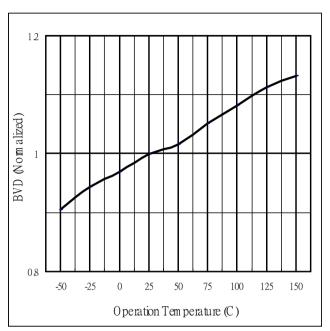


Fig.2 Breakdown Voltage Variation vs. Temperature

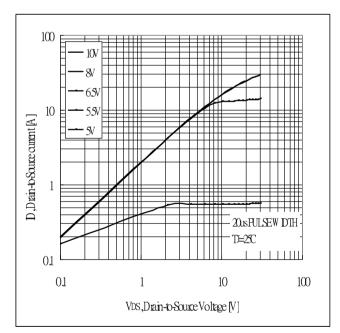


Fig 3. Typical Output Characteristics

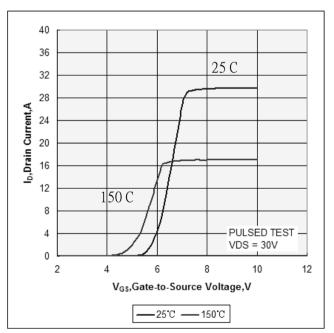


Fig 4. Typical Transfer Characteristics



POWER FIELD EFFECT TRANSISTOR

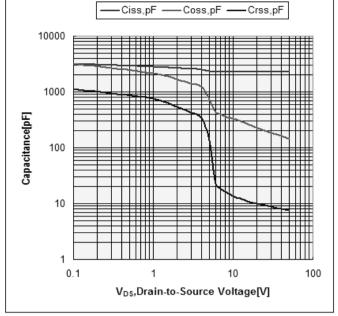
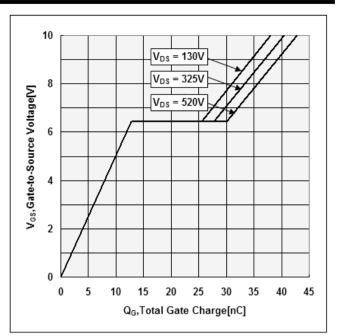


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

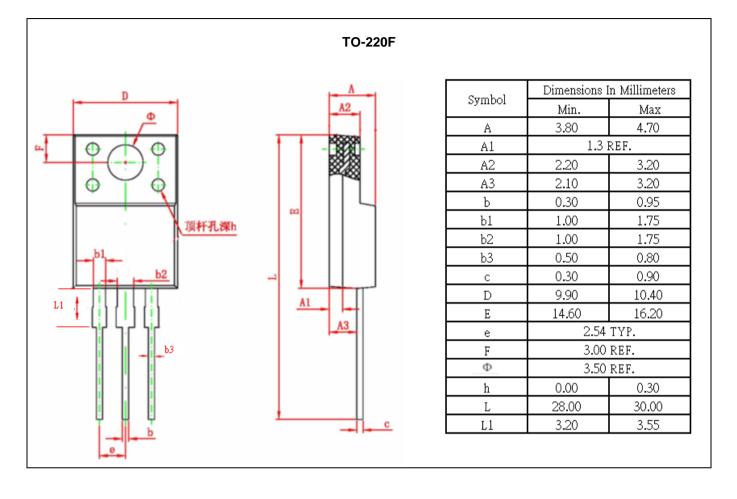






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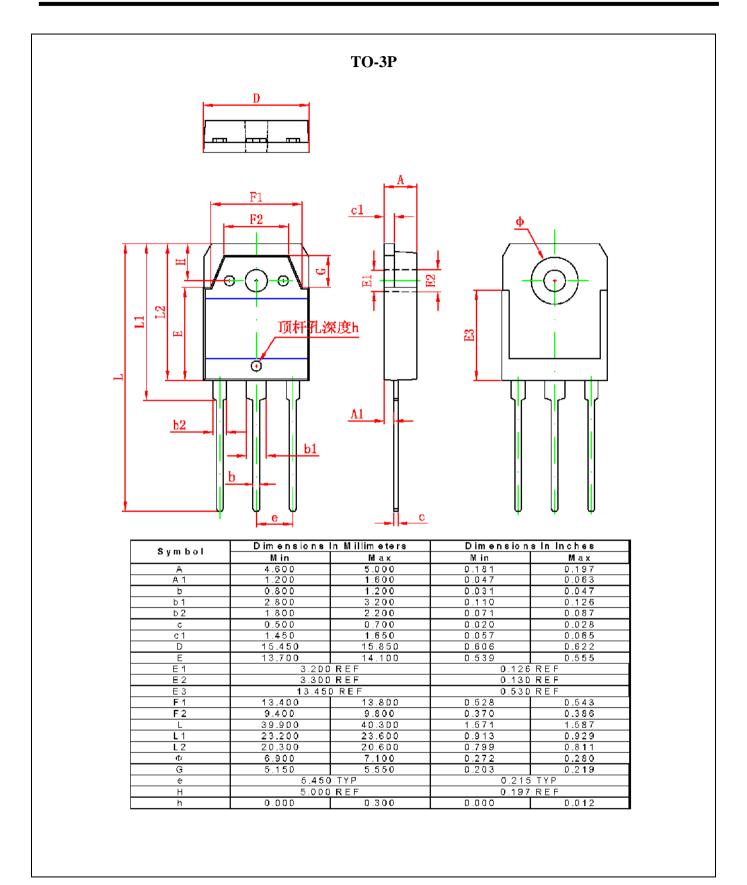
PACKAGE DIMENSION







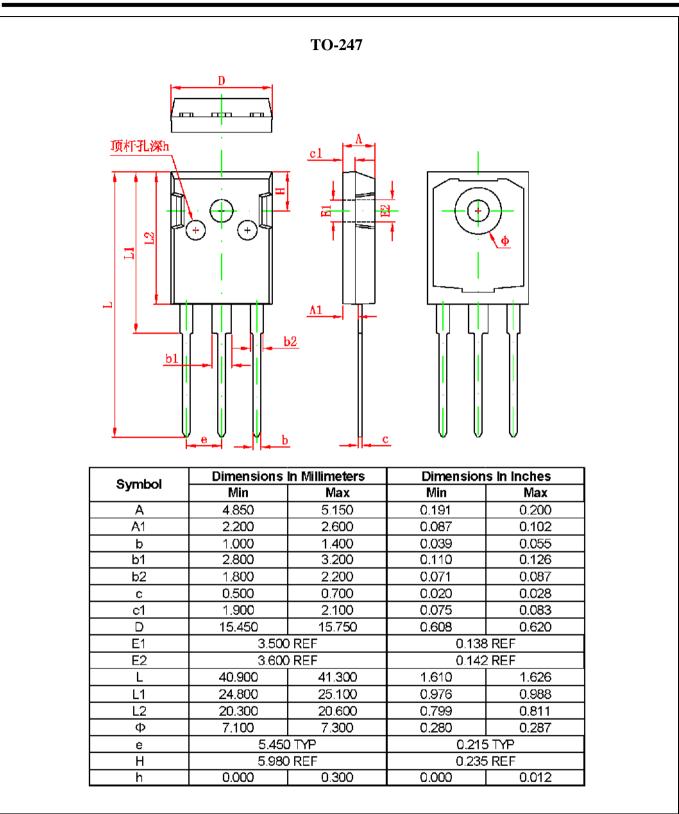
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POWER FIELD EFFECT TRANSISTOR





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