



### Power Field Effect Transistor

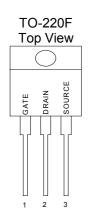
#### **GENERAL DESCRIPTION**

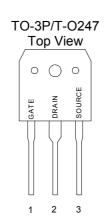
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

#### **FEATURES**

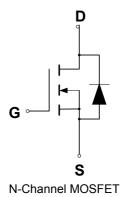
- Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- ◆ I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature
- ♦ Isolated Mounting Hole Reduces Mounting Hardware

#### PIN CONFIGURATION





#### **SYMBOL**



#### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain to Current — Continuous	I <sub>D</sub>	15.7	А
- Pulsed	I <sub>DM</sub>	47.1	
Gate-to-Source Voltage — Continue	$V_{GS}$	±30	V
Total Power Dissipation – TO220FP	$P_D$	53	W
– TO3P		240	W/°C
Derate above 25℃ - TO220FP		0.41	
– TO3P		2	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	$^{\circ}\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}$ C ( $V_{DD} = 100V$ , $V_{GS} = 10V$ , $I_L = 14A$ , $L = 10mH$ , $R_G = 25\Omega$ )	E <sub>AS</sub>	980	mJ
Thermal Resistance — Junction to Case -TO220FP	θ <sub>JC</sub>	3.2	°C/W
<ul><li>Junction to Case -TO3P</li></ul>		0.48	
<ul> <li>Junction to Ambient -TO220FP</li> </ul>	$\theta_{JA}$	62.5	
<ul> <li>Junction to Ambient -TO3P</li> </ul>		40	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T <sub>L</sub>	260	$^{\circ}\!\mathbb{C}$
ESD SENSITIVITY — HBM, C=100pF, R=1.5kΩ	Vesd	2000	V





## Power Field Effect Transistor

#### **ORDERING INFORMATION**

Part Number	Package
GPT16N60GN3P*	TO-3P
GPT16N60GN247*	TO-247
GPT16N60DGN220FP*	TO-220F

<sup>\*</sup>Note: G: Suffix for PB Free Product

#### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

			GP16N60			
Char	Symbol	Min	Тур	Max	Units	
Drain-Source Breakdown Voltage	V	600			V	
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$	$V_{(BR)DSS}$	600			V	
Drain-Source Leakage Current				1	uA	
$(V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V})$	I <sub>DSS</sub>			l	uA	
Gate-Source Leakage Current-Forwar				100	nA	
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$	I <sub>GSSF</sub>			100	IIA	
Gate-Source Leakage Current-Reverse		I <sub>GSSR</sub>			100	nA
$(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$	IGSSR		100	100	IIA	
Gate Threshold Voltage	oltage		3		5	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$			3		3	v
Static Drain-Source On-Resistance (V	R <sub>DS(on)</sub>			0.43	Ω	
Forward Transconductance (V <sub>DS</sub> = 50 V, I <sub>D</sub> = 8A) *				14		S
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	$C_{iss}$		2993		pF
Output Capacitance	$(V_{DS} - 25 V, V_{GS} - 0 V,$ f = 1.0  MHz)	Coss		238		pF
Reverse Transfer Capacitance	1 - 1.0 Wil 12)	C <sub>rss</sub>		11.7		pF
Turn-On Delay Time		$t_{d(on)}$		41.1		ns
Rise Time	$(V_{DD} = 300 \text{ V}, I_D = 16 \text{ A},$	t <sub>r</sub>		45.6		ns
Turn-Off Delay Time	$R_G = 25\Omega)$ *	$t_{d(off)}$		73.6		ns
Fall Time		t <sub>f</sub>		41.6		ns
Total Gate Charge	0/ 400 \/ 1 40 A	$Q_g$		58.7		nC
Gate-Source Charge	$(V_{DS} = 480 \text{ V}, I_D = 16 \text{ A},$	$Q_{gs}$		11.3		nC
Gate-Drain Charge	$V_{GS} = 10 \text{ V})^*$	$Q_{gd}$		21.7		nC
	SOURCE-DRAIN DIODE CH	ARACTERISTICS	•		•	
Forward On-Voltage(1)	// 10 A	V <sub>SD</sub>			1.5	V
Forward Turn-On Time	$(I_S = 16 A,$	t <sub>on</sub>		**		ns
Reverse Recovery Time	$d_{IS}/d_t = 100A/\mu s$ )	t <sub>rr</sub>		502		ns

<sup>\*</sup> Pulse Test: Pulse Width  $\leq$ 300 $\mu$ s, Duty Cycle  $\leq$ 2%

<sup>\*\*</sup> Negligible, Dominated by circuit inductance



Power Field Effect Transistor

#### TYPICAL ELECTRICAL CHARACTERISTICS

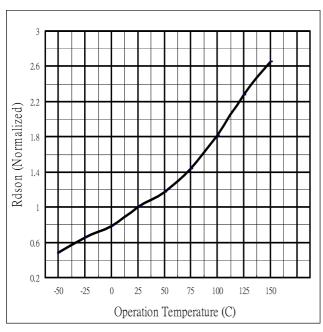


Fig 1. On-Resistance Variation with vs. Temperature

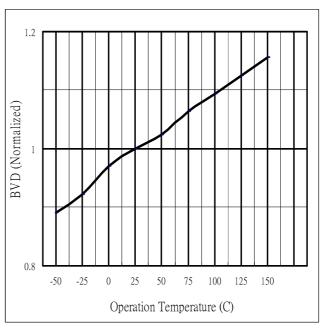


Fig.2 Breakdown Voltage Variation vs. Temperature

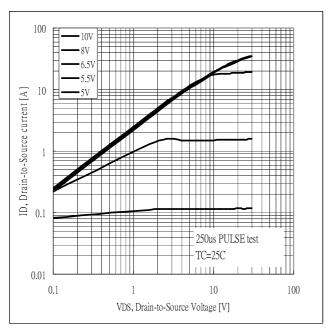


Fig 3. Typical Output Characteristics

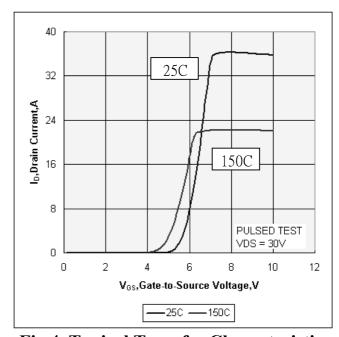


Fig 4. Typical Transfer Characteristics





## POWER FIELD EFFECT TRANSISTOR

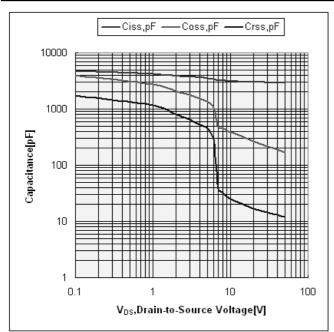


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

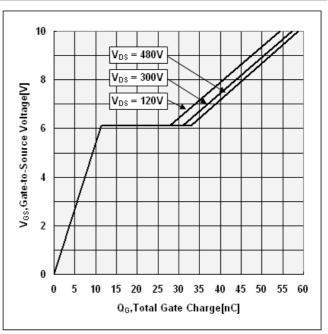


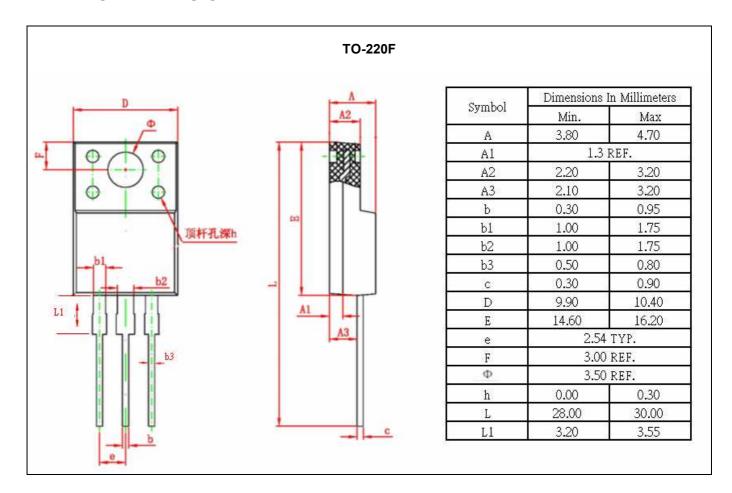
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage





Power Field Effect Transistor

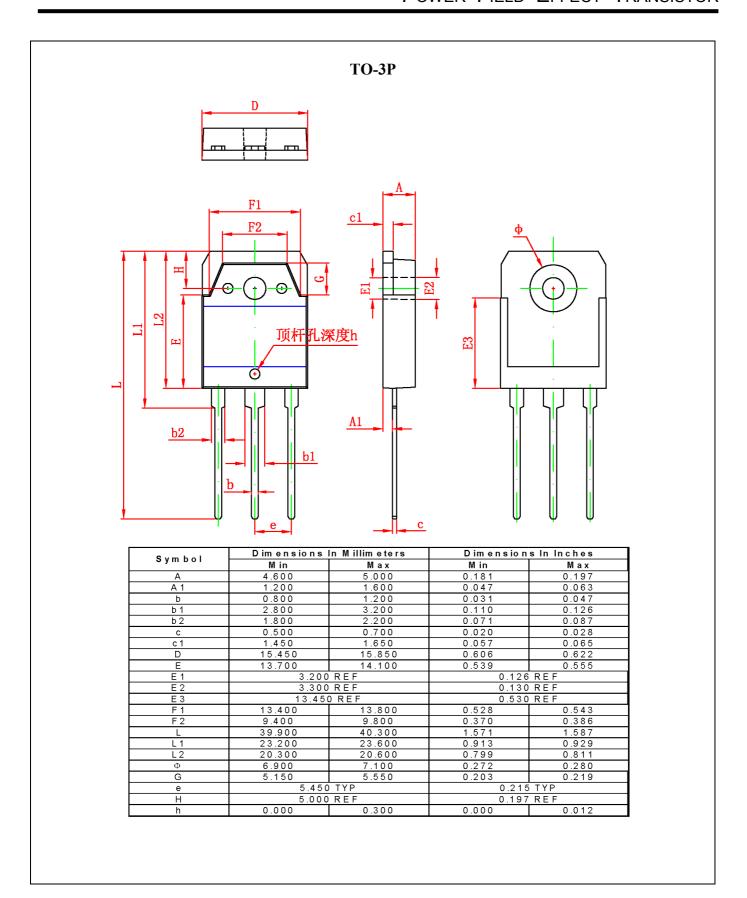
#### **PACKAGE DIMENSION**







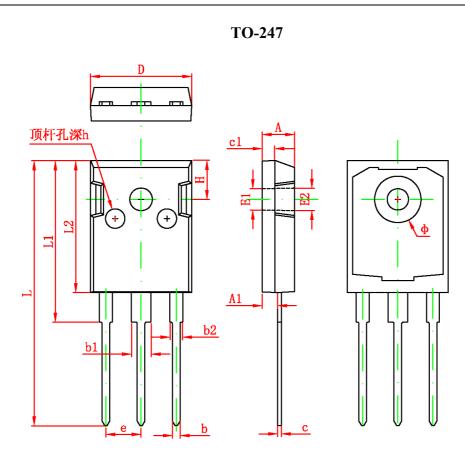
### Power Field Effect Transistor







# Power Field Effect Transistor



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	4.850	5.150	0.191	0.200	
A1	2.200	2.600	0.087	0.102	
b	1.000	1.400	0.039	0.055	
b1	2.800	3.200	0.110	0.126	
b2	1.800	2.200	0.071	0.087	
С	0.500	0.700	0.020	0.028	
c1	1.900	2.100	0.075	0.083	
D	15.450	15.750	0.608	0.620	
E1	3.500 REF		0.138 REF		
E2	3.600 REF		0.142 REF		
L	40.900	41.300	1.610	1.626	
L1	24.800	25.100	0.976	0.988	
L2	20.300	20.600	0.799	0.811	
Φ	7.100	7.300	0.280	0.287	
е	5.450 TYP		0.215 TYP		
Н	5.980 REF		0.235 REF		
h	0.000	0.300	0.000	0.012	





Power Field Effect Transistor

#### **IMPORTANT NOTICE**

Great Power Microelectronic Corporation (GP) reserves the right to make changes to its products or to discontinue any integrated circuit product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

A few applications using integrated circuit products may involve potential risks of death, personal injury, or severe property or environmental damage. GP integrated circuit products are not designed, intended, authorized, or warranted to be suitable for use in life-support applications, devices or systems or other critical applications. Use of GP products in such applications is understood to be fully at the risk of the customer. In order to minimize risks associated with the customer's applications, the customer should provide adequate design and operating safeguards.



虹冠電子工業股份有限公司 Champion Microelectronic Corporation Web:http://www.champion-micro.com/



深圳市冠顺微电子股份有限公司 Shenzhen Great Power Co., Ltd Web:http://www.grtpower.com

臺灣深圳

新北市汐止區新台五路一段 96 號 21F

21F., No. 96, Sec. 1, Sintai 5th Rd., Sijhih City, Taipei County 22102,

Taiwan, R.O.C.

TEL: +886-2-2696 3558 FAX: +886-2-2696 3559 深圳市福田区深南大道 7002 号财富广场 A座 4V,

4V, Tower A, Fortune Plaza, No. 7002, Shennan Road, Futian District, Shenzhen City, China

PC: 518040

TEL: +86-755-83709176 FAX: +86-755-83709276