



### Power Field Effect Transistor

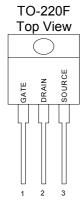
#### **GENERAL DESCRIPTION**

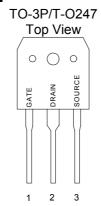
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

#### **FEATURES**

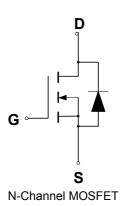
- ◆ Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- ◆ Diode is Characterized for Use in Bridge Circuits
- ♦ I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature
- Isolated Mounting Hole Reduces Mounting Hardware

#### PIN CONFIGURATION





#### **SYMBOL**



### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain to Current — Continuous	I <sub>D</sub>	17.6	Α
- Pulsed	I <sub>DM</sub>	52.8	
Gate-to-Source Voltage — Continue	$V_{GS}$	±30	V
Total Power Dissipation – TO220FP	$P_D$	55	W
– TO3P		251	W/°C
– TO247		232	
Derate above 25℃ - TO220FP		0.37	
– TO3P		2.1	
– TO247		1.9	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	$^{\circ}$ C
Single Pulse Drain-to-Source Avalanche Energy − T <sub>J</sub> = 25°C	_		
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 16A, L = 10mH, R_G = 25\Omega)$	E <sub>AS</sub>	1280	mJ
Thermal Resistance — Junction to Case -TO220FP	$\theta_{ m JC}$	3.15	°CW
<ul><li>Junction to Case -TO3P</li></ul>		0.46	
<ul> <li>Junction to Case -TO247</li> </ul>		0.6	
<ul> <li>Junction to Ambient -TO220FP</li> </ul>	$\theta_{JA}$	62.5	
<ul> <li>Junction to Ambient -TO3P, TO247</li> </ul>		40	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T <sub>L</sub>	260	$^{\circ}\!\mathbb{C}$
ESD SENSITIVITY — HBM, C=100pF, R=1.5kΩ	Vesd	2000	V





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### **ORDERING INFORMATION**

Part Number	Package
GPT18N60GN3P*	TO-3P
GPT18N60GN247*	TO-247
GPT18N60DGN220FP*	TO-220F

\*Note: G: Suffix for PB Free Product

### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

				GPT18N60		
Char	Symbol	Min	Тур	Max	Units	
Drain-Source Breakdown Voltage		000			V	
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$	$V_{(BR)DSS}$	600			V	
Drain-Source Leakage Current				1	uA	
$(V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V})$	I <sub>DSS</sub>			ļ ,	uA	
Gate-Source Leakage Current-Forward				100	<b>π</b> Λ	
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$	I <sub>GSSF</sub>				nA	
Gate-Source Leakage Current-Reverse		I <sub>GSSR</sub>			100	nA
$(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$	$(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$				100	IIA
Gate Threshold Voltage			3		5	V
$(V_{DS} = V_{GS}, I_{D} = 250 \ \mu A)$	$V_{GS(th)}$	3		3	V	
Static Drain-Source On-Resistance (V	R <sub>DS(on)</sub>			0.35	Ω	
Forward Transconductance (V <sub>DS</sub> = 50	<b>g</b> <sub>FS</sub>		22		S	
Input Capacitance	$(V_{DS} = 25 \text{ V. } V_{GS} = 0 \text{ V.}$	C <sub>iss</sub>		3300.9		pF
Output Capacitance	$(v_{DS} = 25 \text{ V}, v_{GS} = 0 \text{ V},$ f = 1.0  MHz)	Coss		353.2		pF
Reverse Transfer Capacitance	1 = 1.0 MH2)	C <sub>rss</sub>		19.27		pF
Turn-On Delay Time		t <sub>d(on)</sub>		38		ns
Rise Time	$(V_{DD} = 300 \text{ V}, I_D = 18 \text{ A},$	t <sub>r</sub>		80.8		ns
Turn-Off Delay Time	$R_G = 25\Omega)$ *	t <sub>d(off)</sub>		123		ns
Fall Time		t <sub>f</sub>		66.4		ns
Total Gate Charge	0/ 400 // 1 40 4	$Q_g$		71.5		nC
Gate-Source Charge	$(V_{DS} = 480 \text{ V}, I_{D} = 18 \text{ A},$	$Q_{gs}$		16.4		nC
Gate-Drain Charge	V <sub>GS</sub> = 10 V)*	$Q_{gd}$		29		nC
	SOURCE-DRAIN DIODE CHA	ARACTERISTICS	•	•		
Forward On-Voltage(1)	// 40 A	V <sub>SD</sub>			1.5	V
Forward Turn-On Time	$(I_S = 18 A,$	t <sub>on</sub>		**		ns
Reverse Recovery Time	$d_{IS}/d_t = 100A/\mu s)$			500		ns

<sup>\*</sup> Pulse Test: Pulse Width  $\leq$ 300 $\mu$ s, Duty Cycle  $\leq$ 2%

<sup>\*\*</sup> Negligible, Dominated by circuit inductance



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### TYPICAL ELECTRICAL CHARACTERISTICS

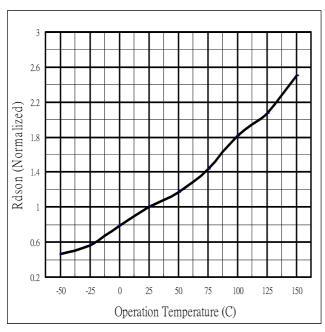


Fig 1. On-Resistance Variation with vs. Temperature

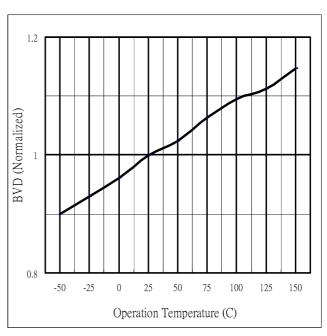


Fig.2 Breakdown Voltage Variation vs. Temperature

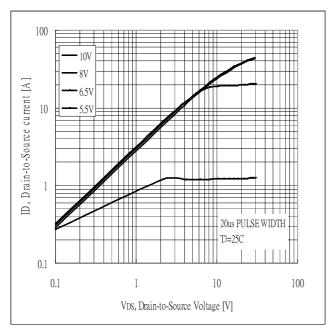


Fig 3. Typical Output Characteristics

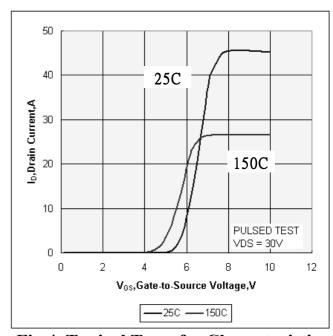


Fig 4. Typical Transfer Characteristics





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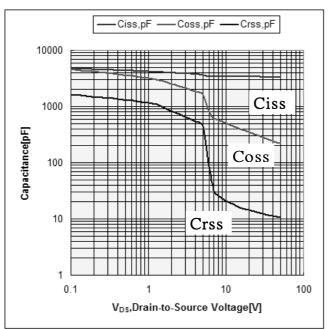


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

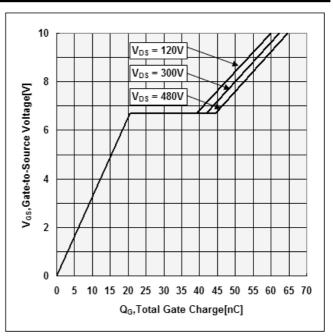


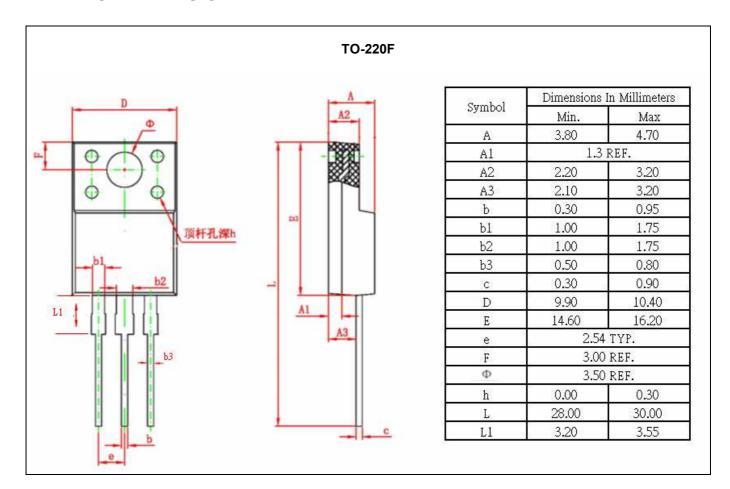
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage





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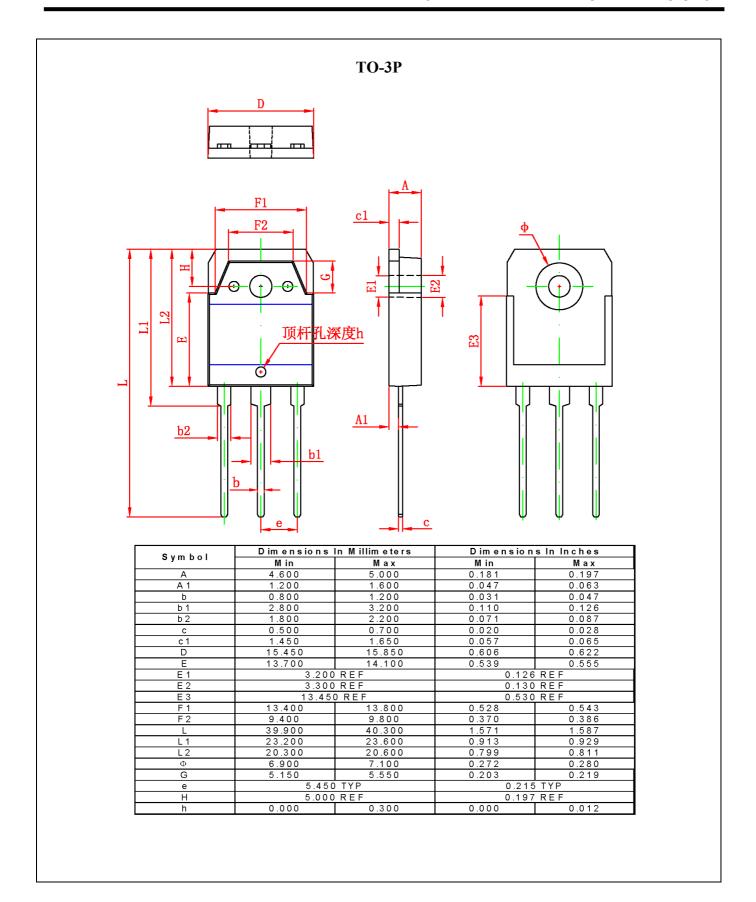
### **PACKAGE DIMENSION**







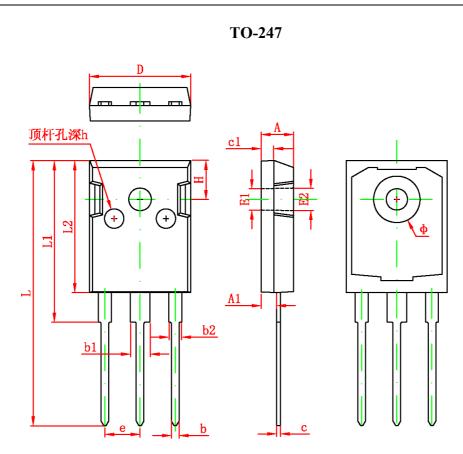
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Symbol	Dimensions In Millimeters		Dimensions In Inches		
Sylfibol	Min	Max	Min	Max	
Α	4.850	5.150	0.191	0.200	
A1	2.200	2.600	0.087	0.102	
b	1.000	1.400	0.039	0.055	
b1	2.800	3.200	0.110	0.126	
b2	1.800	2.200	0.071	0.087	
С	0.500	0.700	0.020	0.028	
c1	1.900	2.100	0.075	0.083	
D	15.450	15.750	0.608	0.620	
E1	3.500 REF		0.138 REF		
E2	3.600 REF		0.142 REF		
L	40.900	41.300	1.610	1.626	
L1	24.800	25.100	0.976	0.988	
L2	20.300	20.600	0.799	0.811	
Φ	7.100	7.300	0.280	0.287	
е	5.450 TYP		0.215 TYP		
Н	5.980 REF		0.235 REF		
h	0.000	0.300	0.000	0.012	





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### **IMPORTANT NOTICE**

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