GTM CORPORATION ISSUED DATE :2004/12/16 REVISED DATE :

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R1

R2

GND

GQ2153

CMOS Positive Voltage Regulator

Description

The GQ2153 series of positive, linear regulators feature low quiescent current (30µA typ.) with low dropout voltage, making them ideal for battery applications.

These rugged devices have both Thermal Shutdown, and Current Fold-back to prevent device failure under the "Worst" of operating conditions.

In applications requiring a low noise, regulated supply, place a 1000pF capacitor between Bypass and Ground. The GQ2153 is stable with an output capacitance of 2.2µF or greater.

EN

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Functional Block Diagram

Overcurrent

Shutdown

Thermal Shutdown

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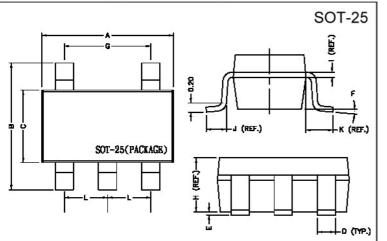
Features

- Very Low Dropout Voltage
- Guaranteed 300mA output
- Over-Temperature Shutdown
- Current Limiting
- Short Circuit Current Fold-back
- Highly Accurate± 1.5%
- Noise Reduction Bypass Capacitor
- · Power-saving Shutdown Mode
- Factor Pre-set Output Voltage

Applications

- Battery Powered Widgets
- Instrumentation
- Wireless Devices
- PC Peripherals
- Portable Electronics
- Cordless Phones
- Electronic Scales

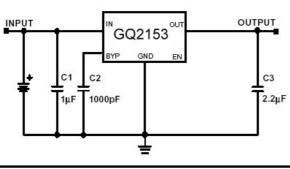
Package Dimensions



Vout Marking : 5 4 1.8v=18 2.5v=25 3 E 2 3.3v=33 Accurate ±1.5% Date Code serial:01~99 Nth month:A~M I no use Year:"4"=2004 "5"=2005 1:EN 4:Vout 2:Gnd 5:Vin 1 2 3 3:BYP

REF.	Millimeter		REF.	Dimensions	
nLI.	Min.	Max.	ΠLΙ.	Millimeter	
Α	2.70	3.10	G	1.90 REF.	
В	2.60	3.00	Н	1.20 REF.	
С	1.40	1.80	I	0.12 REF.	
D	0.30	0.55	J	0.37 REF.	
E	0	0.10	K	0.60 REF.	
F	0°	10°	L	0.95 REF.	

Typical Application Circuit



Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Input Max Voltage	Vin	8	V
Output Current	IOUT PD/(VIN-VO)		mA
Output Voltage	Vout	1.5~3.8	V
Operating Ambient Temperature	Topr	-40 ~ +85	°C
Junction Temperature	Tj	-40 ~ +125	°C
Maximum Junction Temperature	Tj Max	150	°C
Power Dissipation(△T=100°C)	PD	380	mW
EDS Classification		В	

Electrical Characteristics Ta=25°C unless otherwise noted

Parameter	Symbol	Condition		Min	TYP	Max	Unit
	Vout(E)	VIN=VOUT(T)+2V, IO=1mA		-1.5%	Vout(T)	1.5%	V
Output Voltage	(Note1)	VIN=VOUT(T)+2V, Io=300mA	-2.5%	(Note2)	2.5%	V
Output Current	Io	$V_{IN}=V_{OUT}(T)+2V$, $V_{OUT} \ge V_{OUT}(E)^*0.96$		300	-	-	mA
Current Limit	ILIM	VIN=VOUT(T)+2V, VO>1.2V		300	450	-	mA
Load Regulation	REGLOAD	VIN=VOUT(T)+2	V, Io=1mA to 300mA	-1	0.2	1	%
	Vdropout	Io=300mA	$1.2V \leq V_{OUT}(T) \leq 2.0V$	-	-	1300	mV
Dropout Voltage		Vo=Vout(E)-2%	$2.0V < V_{OUT}(T) \leq 2.8V$	-	-	400	
			2.8V <vout(t)< td=""><td>-</td><td>-</td><td>300</td></vout(t)<>	-	-	300	
Quiescent Current	Iq	VIN= VOUT(T)+1V, IO=0mA		-	30	50	μA
Ground Pin Current	Ignd	VIN= VOUT(T)+2	2V, Io=1mA~300mA	-	35	-	μA
		Io=1mA	$1.2V\!\leq\!V_{OUT}(T)\!\leq\!1.4V$	-0.2	-	0.2	%
Line Regulation	REGLINE	$V_{IN}=V_{OUT}(T)+1$ to $V_{OUT}(T)+2$	$1.4V < V_{OUT}(T) \le 2.0V$	-0.15	-	0.15	
Line Regulation			2.0V <vout(t)<4.0v< td=""><td>-0.1</td><td>0.02</td><td>0.1</td></vout(t)<4.0v<>	-0.1	0.02	0.1	
			4.0V≦Vout(T)	-0.4	0.2	0.4	
Input Voltage	Vin			Note3	-	7	V
Over Temperature Shutdown	OTS			-	150	-	°C
Over Temperature Hysterisis	OTH			-	30	-	°C
Output Voltage Temperature Coefficient	тс			-	30	-	ppm/ ℃
Short Circuit Current(Note4)	Isc	VIN=VOUT(T	VIN=VOUT(T)+1V, VOUT<0.8V		150	300	mA
	PSRR	1. 100 1	f=100Hz	-	60	-	dB
Power Supply Rejection		Io=100mA Co=2.2µF	f=1kHz	-	50	-	
		p.	f=10kHz	-	20	-	
Output Voltage Noise	En	f=10Hz~100kHz Io=10mA	Co=2.2µF	-	30	-	μVrms
EN Input Threshold	Veh	V _{IN} =2.7V to 7V		2.0	-	VIN	V
	Vel	V _{IN} =2.7V to 7V		0	-	0.4	V
EN Input Bias Current	Іен	VEN=VIN, VIN=2.7V to 7V		-	-	0.1	μA
	Iel	VEN= 0V, VIN=2.7V to 7V		-	-	0.5	μA
Shutdown Supply Current	Isd	Vin=5V, Vo=0V, Ven <vel< td=""><td>-</td><td>0.5</td><td>1</td><td>μA</td></vel<>		-	0.5	1	μA
Shutdown Output Voltage	Vo,sd	Io=0.4mA, Ven <vel< td=""><td>0</td><td>-</td><td>0.4</td><td>V</td></vel<>		0	-	0.4	V

Note 1: VOUT (E) = Effective Output Voltage (i.e. the output voltage when "VOUT (T) + 2.0V" is provided at the VIN pin while maintaining a certain lout value).

2: VOUT (T) = Specified Output Voltage

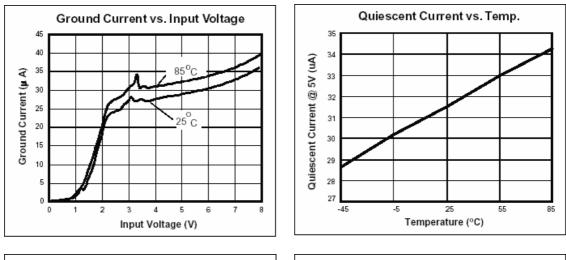
3: VIN (MIN) =VOUT+VDROPOUT

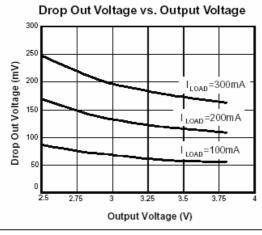
4: To prevent the Short Circuit Current protection feature from being prematurely activated, the input voltage must be applied before a current source load is applied.

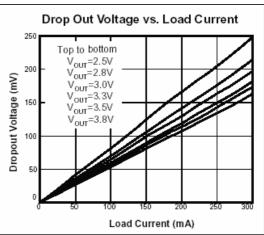
Ordering Information (contd.)

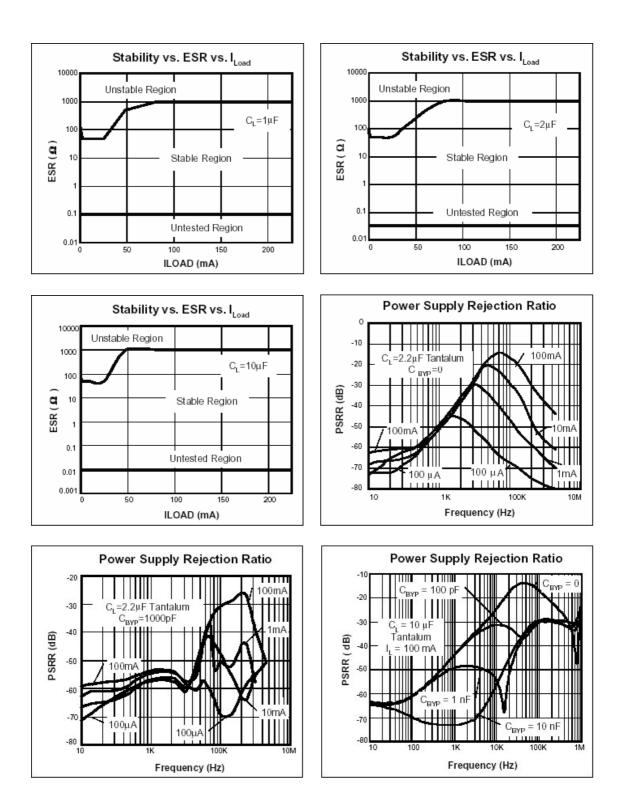
Part Number	umber Marking Output Voltage Part Number		Part Number	Marking	Output Voltage 1.8V	
GQ2153-15	3E152 XXXX	1.5V	GQ2153-18 3E182 xxxx			
GQ2153-25	3E252 XXXX	2.5V	GQ2153-27	3E272 XXXX	2.7V	
GQ2153-28	3E282 XXXX	2.8V	GQ2153-2H	3E2H2 XXXX	2.85V	
GQ2153-29	3E292 XXXX	2.9V	GQ2153-30	3E302 XXXX	3.0V	
GQ2153-31	3E312 XXXX	3.1V	GQ2153-33	3E332 XXXX	3.3V	
GQ2153-34	3E342 XXXX	3.4V	GQ2153-35	3E352 XXXX	3.5V	
GQ2153-36	3E362 XXXX	3.6V	GQ2153-37	3E372 xxxx 3.7		
GQ2153-38	3E382 XXXX	3.8V				

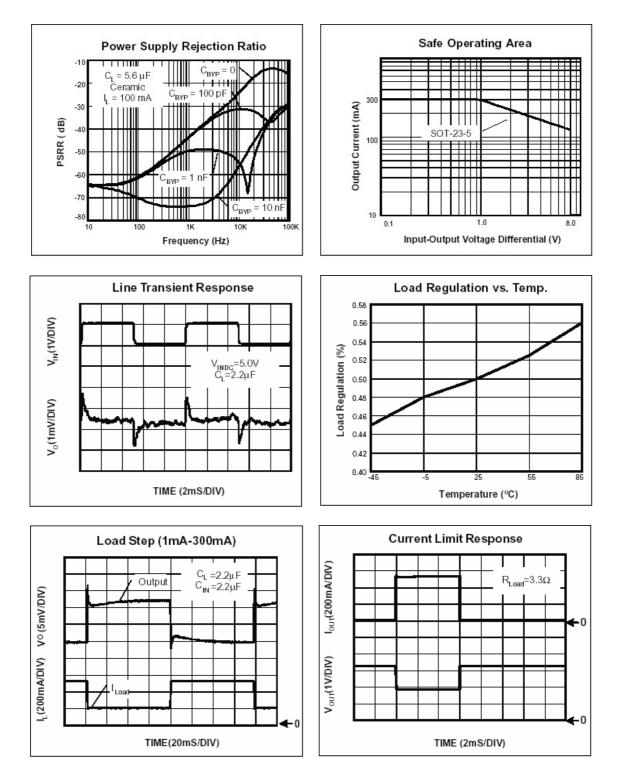
Characteristics Curve

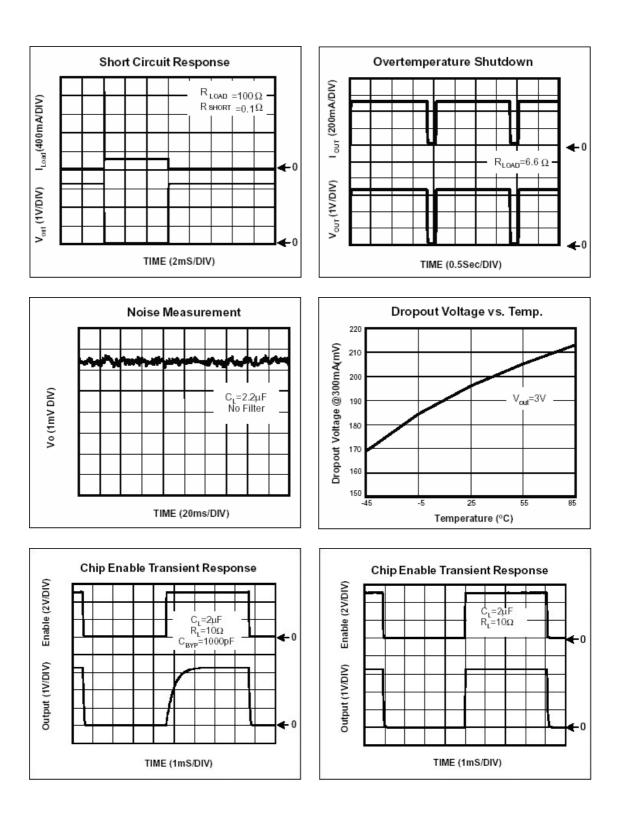


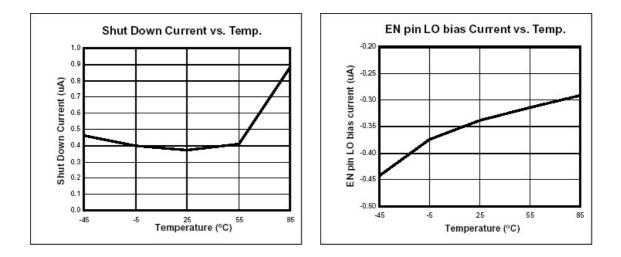












Detailed Description

The GQ2153 series of COMS regulators contain a PMOS pass transistor, voltage reference, error amplifier, over-current protection, and thermal shutdown.

The P-channel pass transistor receives data from the error amplifier, over-current shutdown, and thermal protection circuits. During normal operation, the error amplifier compares the output voltage to a precision reference. Over-current and Thermal shutdown circuits become active when the junction temperature exceeds 150° C, or the current exceeds 300mA. During thermal shutdown, the output voltage remains low. Normal operation is restored when the junction temperature drops below 120° C.

The GQ2153 switches from voltage mode to current mode when the load exceeds the rated output current. This prevents over-stress. The GQ2153 also incorporates current fold-back to reduce power dissipation when the output is short circuited. This feature becomes active when the output drops below 0.8 volts, and reduces the current flow by 65%. Full current is restored when the voltage exceeds 0.8 volts.

External Capacitors

The GQ2153 is stable with an output capacitance to ground of 2.2μ F or greater. Ceramic capacitors have the lowest ESR, and will offer the best AC performance. Conversely, Aluminum Electrolytic capacitors exhibit the highest ESR, resulting in the poorest AC response. Unfortunately, large value ceramic capacitors are comparatively expensive. One option is to parallel a 0.1μ F ceramic capacitor with a 10μ F Aluminum Electrolytic. The benefit is low ESR, high capacitance, and low overall cost.

A second capacitor is recommended between the input and ground to stabilize Vin. The input capacitor should be at least 0.1µF to have a beneficial effect.

A third capacitor can be connected between the BY-PASS pin and GND. This capacitor can be a low cost Polyester Film variety between the value of 0.001~0.01µF. A large capacitor improves the AC ripple rejection, but also makes the output come up slowly. This "Soft" turn-on is desirable in some applications to limit turn-on surges.

All capacitors should be placed in close proximity to the pins. A "Quiet" ground termination is desirable. This can be achieved with a "Star" connection.

Enable

The Enable pin normally floats high. When actively, pulled low, the PMOS pass transistor shuts off, and all internal circuits are powered down. In this state, the quiescent current is less than 1µA. This pin behaves much like an electronic switch.

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