GS391 Low Power Low Offset Voltage Single Comparator

Product Description

The GS391 consists of a independent precision voltage comparator which was designed specifically to operate from a single power supply over a wide range of voltages.

Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

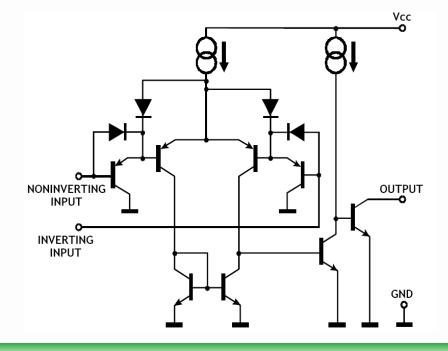
The comparator also has a unique characteristic in that the input common-mode voltage range includes ground, even though operated from a single power supply voltage.

The GS391 was designed to directly interface with TTL and CMOS. When operated from both plus and minus power supplies, the GS391 will directly interface with MOS logic where their low power drain is a distinct advantage over standard comparator.

Features

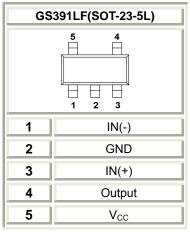
- Wide supply Voltage range: 2.0V to 32V.
- Low supply current drain independent of supply voltage.
- Low input biasing current: 25 nA typ.
- Low input offset current: 5 nA typ.
- Low input offset voltage: 3 mV typ.
- Input common-mode voltage range includes GND.
- Differential input voltage range equal to the power supply voltage
- Low output saturation voltage.
- Output voltage compatible with TTL, MOS and CMOS logic.
- RoHS Compliant and Halogen Free

Block Diagram

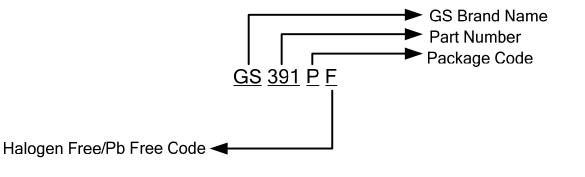




Packages & Pin Assignments

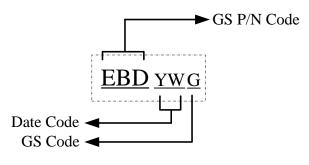


Ordering Information



GS Complete P/N	Package	Marking	Q'ty / Reel
GS391LF	SOT-23-5L	EBD _{YWG}	3К

Marking Information





Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
Vcc	Supply Voltage	36	V
V _{IDR}	Differential Input Voltage	36	V
V _{IN}	Input Voltage	-0.3 to +36	V
I _{IN}	Input Current	20	mA
PD	Power Dissipation (Note 1)	500	mW
los	Output Short-Circuit to GND	Continuous	
T _{PR}	Operating Temperature Range	0 to 70	°C
T _{STG}	Storage temperature Range	-65 to 150	°C
Αιθ	Junction to Ambient Thermal Resistance	250	°C/W

Note 1: For operating at high temperatures, the GS391 must be derated based on a 125°C maximum junction temperature and a thermal resistance of 170°C /W which applies for the device soldered in a PCB, operating in a still air ambient. The low bias dissipation and the "ON-OFF" characteristic of the outputs keeps the chip dissipation very small ($P_D \le 100$ mW), provided the output transistors are allowed to saturate.

Electrical Characteristics

at specified free-air temperature, V_{CC}=5V (Unless Otherwise Noted)

Symbol	Parameter		*Test cond	litions	Min	Тур	Max	Unit	
V	Input offset		5 V to 30V,	25 °C		2	5		
V _{IO}	voltage		V _{IC} = V _{ICR} min, Vo=1.4 V	Full range			9	mV	
I _{IO}	Input offset	Vo	=1.4 V	25 °C		5	50	nA	
	current	v0	-1. 4 V	Full range			150		
IIB	Input bias current	Vo	Vo=1.4 V	25 °C		25	250	nA	
чв		•••	-1. 4 V	Full range			400		
V	**Common-mode			25 °C	0 to V _{CC} - 1.5			v	
VICR	input voltage range			Full range	0 to V_{CC} - 2.0			V	
A _{VD}	Large-signal differential voltage amplification	Vo=1.4	= 15 V, V to 11.4 V, kΩ to V _{CC}	25 °C	50	200		V/mV	
I _{ОН}	High-level output	V _{OH} =5	V, V _{ID} =1V,	25 °C		0.1	50	nA	
ЧОН	current	V _{он} = 3	0V, V _{ID} =1V	Full range			1	μA	
V _{OL}	Low-level output		I _{OL} = 4mA, V _{ID} =-1V	25 °C		150	400	mV	
V OL	voltage	voltage	Full range			700			
lol	Low-level output current	V _{OL} = 1.5V, V _{ID} =-1V		25 °C	6			mA	
Icc	Supply current	Icc Supply current	R₁ = ∞	$V_{\rm CC} = 5V$	25 °C		0.8	1	mA
			$V_{CC} = 30V$	Full range			2.5		

* Full range (MIN to MAX), for the GS391 is 0°C to 70°C. All characteristics are measured with zero common-mode input voltage unless otherwise specified.

** The voltage at either input or common-mode should not be allowed to go negative by more than 0.3V. The upper end of the common-mode voltage range is V_{CC} -1.5V, but either or both inputs can go to 30V without damage



GS391

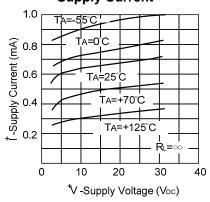
Switching Characteristics V_{cc}=5V, T_A=25 °C

Parameter	Test conditions			Unit
Response time	R_L connected to 5V through 5.1 k Ω , C_L =15pF*	100-mV input step with 5-mV overdrive	1.3	μs
une	(See Note 1)	TTL-level input step	0.3	

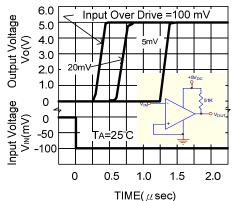
 $^{*}C_{L}$ includes probe and jig capacitance.

Note 1: The response time specified is the interval between the input step function and the instant when the output crosses 1.4V.

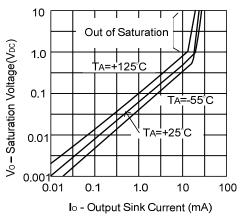




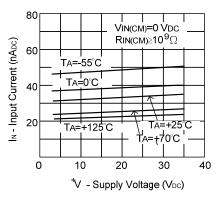
Response Time for Various Input Overdrives–Positive Transition



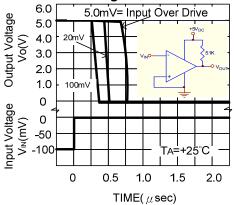
Output Saturation Voltage



Input Current

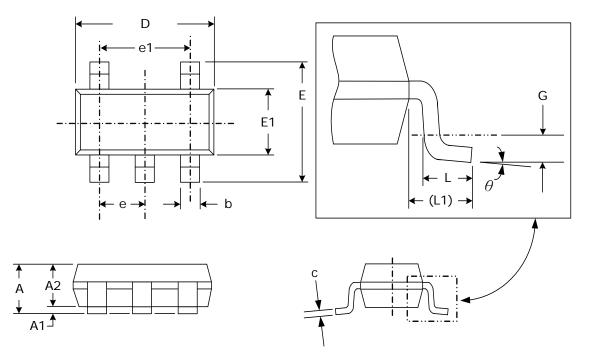


Response Time for Various Input Overdrives–Negative Transition



Package Dimension

SOT-23-5L



	Dimensions				
SYMBOL	Millin	neters	Inches		
STWIDOL	MIN MAX		MIN	MAX	
Α	0.95	1.45	.037	.057	
A1	0.05	0.15	.002	.006	
A2	0.90	1.30	.035	.051	
b	0.30	0.50	.012	.020	
С	0.08	0.20	.003	.008	
D	2.80	3.00	.110	.118	
E	2.60	3.00	.102	.118	
E1	1.50	1.70	.059	.067	
е	0.95	(TYP)	.037 (TYP)		
e1	1.90	(TYP)	.075 (TYP)		
L	0.35	0.55	.014	.022	
L1	0.60	(TYP)	.024 (TYP)		
G	0.25	(TYP)	.010 (TYP)		
θ	0°	8°	0°	8°	

– **GS**391



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