GS5256

150KHz 3A Step-Down Voltage Regulator

Product Description

The GS5256 series regulators are monolithic integrated circuits that provide all the active functions for a step-down switching regulator; capable of driving a 3A load with excellent line and load regulation. These devices are available in fixed output voltages of 3.3V, 5V and an adjustable output version.

Requiring a minimum number of external components, these regulators are simple to use and include internal frequency compensation, and a fixed-frequency oscillator.

The GS5256 series operates at a switching frequency of 150 kHz thus allowing smaller sized filter components than what would be needed with lower frequency switching regulators. Available in a standard 5-lead TO-220 package and a 5-lead TO-263 surface mount package.

A standard series of inductors are optimized for use with the GS5256 series. This feature greatly simplifies the design of switch-mode power supplies.

Other features include a guaranteed $\pm 4\%$ tolerance on output voltage under specified input voltage and output load conditions, and $\pm 15\%$ on the oscillator frequency. External shutdown is included, featuring typically $80\mu A$ standby current. Self-protection features include a two stage frequency reducing current limit for the output switch and an over temperature shutdown for complete protection under fault conditions.

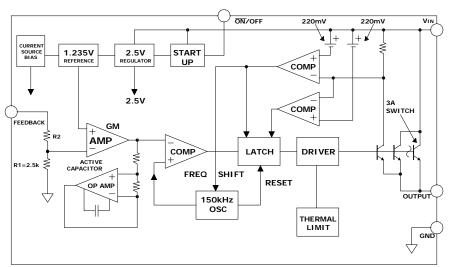
Features

- 3.3V. 5V and adjustable output versions
- Adjustable version output voltage range, 1.2V to 37V
- ±4% max over line and load conditions
- Guaranteed 3A output load current
- Requires only 4 external components
- Excellent line and load regulation specifications
- 150kHz fixed frequency internal oscillator
- TTL shutdown capability
- Low power standby mode, I_Q typically 80µA
- High efficiency
- Uses readily available standard inductors
- Thermal shutdown and current limit protection
- Available in TO-220-5 and TO-263-5 packages
- RoHS Compliant, 100%Pb & Halogen Free

Applications

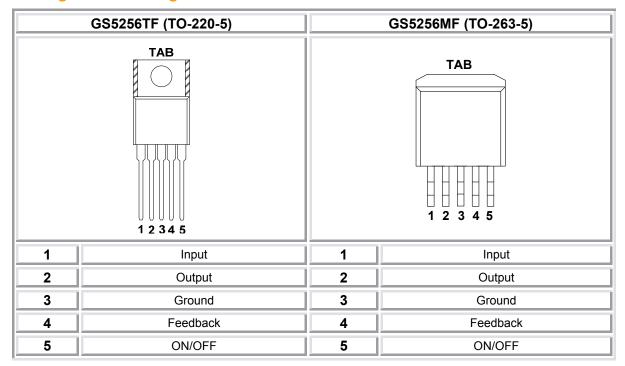
- Efficient pre-regulator for linear regulator
- Simple high-efficiency step-down regulator
- On-card switching regulators
- Positive to negative converter

Block Diagram

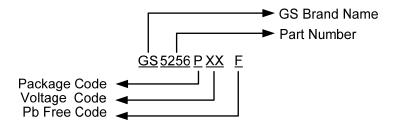




Packages & Pin Assignments



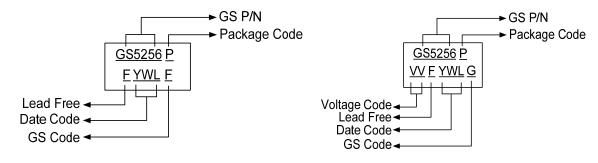
Ordering Information



*For other voltages, please contact factory.

Pac	Output Voltage	
TO-220	TO-263	Output Voltage
GS5256TF	GS5256MF	ADJ
GS5256T33F	GS5256M33F	3.3V
GS5256T50F	GS5256M50F	5.0V

Marking Information





Absolute Maximum Rating

Parameter	Maxi	mum	Unit
Maximum Supply Voltage	4	V	
ON /OFF Pin Input Voltage	-0.3 ≤ \	/ ≤ +25	V
Feedback Pin Voltage	-0.3 ≤ \	/ ≤ +25	V
Output Voltage to Ground (Steady State)	-	1	V
Storage Temperature Range	-65 to +150		°C
Maximum Junction Temperature	+150		°C
Lead Temperature (Soldering, 10 Seconds)	260		°C
Minimum ESD Rating (C=100pF, R=1.5KΩ)	2		kV
Power Dissipation (Internally limited),P _D	TO-220 TO-263	2 2	W
Thermal Resistance (Junction to Ambient),θ _{JA}	TO-220 TO-263	62.5 62.5	°C/W

Note:

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of the specifications is not implied.

Exposure to Absolute Maximum Ratings conditions for extended periods may affect device reliability.

Operating Ratings

Parameter	Value	Unit
Temperature Range	-40 ≤T _J ≤+125	°C
Supply Voltage	4.5 to 40	V

Electrical Characteristics

GS5256-3.3

Symbol	Parameter	Conditions	MIN	TYP	MAX	Unit
		4.75V≤V _{IN} ≤40V,0.2A≤I _{LOAD} ≤3A(Note3,4)				
V _{OUT}	Output Voltage	TJ=25 °C	3.168	3.3	3.432	V
		T _J =-40 °C to 125 °C	3.135	3.3	3.465	V
η	Efficiency	V _{IN} =12V, I _{LOAD} =3A		73		%

GS5256-5.0

Symbol	Parameter	Conditions	MIN	TYP	MAX	Unit
		7V≤V _{IN} ≤40V, 0.2A≤I _{LOAD} ≤3A (Note 3,4)				
V _{OUT}	Output Voltage	T _J =25 °C	4.80	5.0	5.2	V
		T _J =-40 °C to 125 °C	4.75	5.0	5.25	V
η	Efficiency	V _{IN} =12V, I _{LOAD} =3A		80		%

GS5256-ADJ

Symbol	Parameter	Conditions	MIN	TYP	MAX	Unit
.,		4.5V≤V _{IN} ≤40V, 0.2A≤I _{LOAD} ≤3A V _{OUT} programmed for 3V, Circuit of Figure1.				
V _{OUT}	Output Voltage	T _J =25 °C	1.193	1.23	1.267	V
		T _J =-40 °C to 125 °C	1.180	1.23	1.280	V
η	Efficiency	V _{IN} =12V, I _{LOAD} =3A, V _{OUT} =3V		73		%



Electrical Characteristics (Continued)

Unless otherwise specified, V_{IN} =12V for the 3.3V, 5V, and Adjustable version, I_{LOAD} =500mA.

Symbol	Parameter Conditions			TYP	MAX	Unit
		Device Parameters				
		V _{FB} =1.3V(Adjustable Version Only)				
l _Β	Feedback Bias Current	T _J =25 °C	-	10	50	nA
		T _J =-40 °C to 125 °C	-	-	100	nA
		T _J =25 °C (Note 6)	-	150	-	kHz
Fosc	Oscillator Frequency	T _J =0 °C to 125 °C	127	-	173	kHz
		T _J =-40 °C to 125 °C	110	-	173	kHz
		I _{OUT} =3A (Note 7,8)				
V_{SAT}	Saturation Voltage	T _J =25 °C	-	1.16	1.4	V
		T _J =-40 °C to 125 °C	-	-	1.5	V
DC	Max Duty Cycle (ON) Min Duty Cycle (OFF)	(Note 8) (Note 9)	-	100 0	-	% %
		Peak Current (Notes 7, 8)				
I _{CL}	Current Limit	T _J =25 °C	3.6	4.5	6.9	Α
		T _J =-40 °C to 125 °C	3.4	4.5	7.5	Α
Iμ	Output Leakage Current	Output = 0V (Notes 7,8)	-	-	50	μA
".	Output Leakage Current	Output = -1V (Notes 10)		2	30	mA
ΙQ	Quiescent Current	(Note 9)	-	5	10	mA
I _{STBY}	Standby Quiescent Current	ON/OFF Pin=5V(OFF), (Notes 10)	-	80	200	μΑ
		ON/OFF Control				
V _{IH}	ON/OFF Pin Logic Input	Low (Regulator ON)	-	1.3	0.6	V
V _{IL}	Threshold Voltage	High (Regulator OFF)	2.0	1.3	-	V
I _{IH}	ON/OFF Pin Input Current	V _{LOGIC} = 2.5V (Regulator OFF)		5	15	μΑ
I _{IL}	STAGET THE IMPACTOR TELL	V _{LOGIC} = 0.5V (Regulator ON)		0.02	5	μΑ

- Note 1 Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics.
- Note 2 The human body model is a 100pF capacitor discharged through a 1.5k resistor into each pin.
- **Note 3** Typical numbers are at 25°C and represent the most likely norm.
- Note 4 All limits guaranteed at room temperature (standard type face) and at temperature extremes (bold type face). All room temperature limits are 100% production tested. All limits at temperature extremes are guaranteed via correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).
- **Note 5** External components such as the catch diode, inductor, input and output capacitors, and voltage programming resistors can affect switching regulator system performance. When the GS5256 is used as shown in the Figure 1 test circuit, system performance will be as shown in system parameters section of Electrical Characteristics.
- **Note 6** The switching frequency is reduced when the second stage current limit is activated. The amount of reduction is determined by the severity of current over-load.
- Note 7 No diode, inductor or capacitor connected to output pin.
- Note 8 Feedback pin removed from output and connected to 0V to force the output transistor switch ON.
- **Note 9** Feedback pin removed from output and connected to 12V for the 3.3V, 5V, and the ADJ. version, and 15V for the 12V version, to force the output transistor switch OFF.
- Note10 $V_{IN} = 40V$
- Note11 Junction to ambient thermal resistance (no external heat sink) for the TO-220 package mounted vertically, with the leads soldered to a printed circuit board with (1 oz.) copper area of approximately 1 in²
- **Note12** Junction to ambient thermal resistance with the TO-263 package tab soldered to a single printed circuit board with 0.5 in² of (1 oz.) copper area.

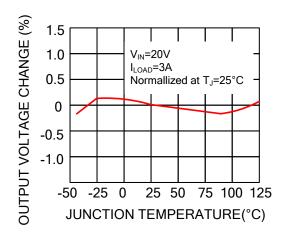


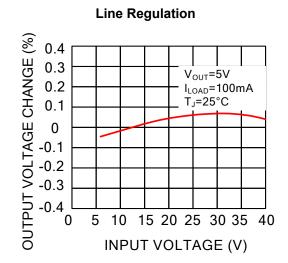
Note13 Junction to ambient thermal resistance with the TO-263 package tab soldered to a single sided printed circuit board with 2.5 in² of (1 oz.) copper area.

Note14 Junction to ambient thermal resistance with the TO-263 package tab soldered to a double sided printed circuit board with 3 in² of (1 oz.) copper area on the GS5256S side of the board, and approximately 16 in² of copper on the other side of the p-c board.

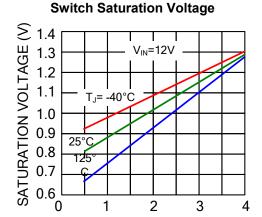
Typical Performance Characteristics

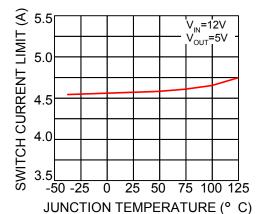
Normalized Output Voltage



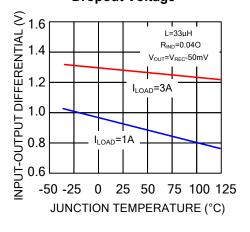


Efficiency 95 20V 3A Load 90 12V ∃fficiency (%) 85 80 5V 75 70 3.3V 65 10 15 20 25 30 35 40 0 5 INPUT VOLTAGE (V)





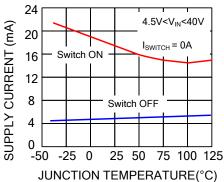
Switch Current Limit



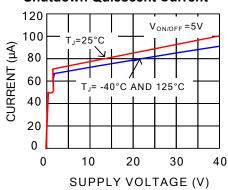
Dropout Voltage

SWITCH CURRENT (A)

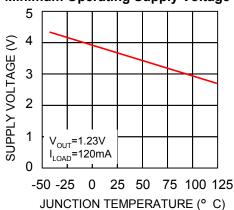
Operating Quiescent Current



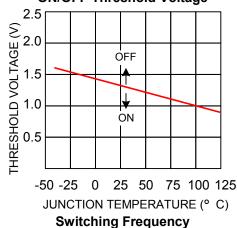
Shutdown Quiescent Current



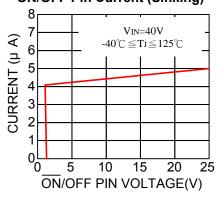
Minimum Operating Supply Voltage

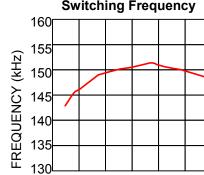


ON/OFF Threshold Voltage



ON/OFF Pin Current (Sinking)





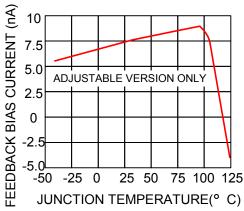
25 50

JUNCTION TEMPERATURE(°C)

75 100 125

-25

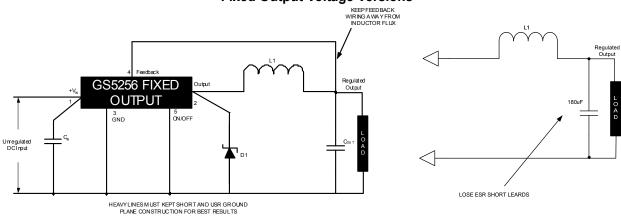
Feedback Pin Bias Current





Test Circuit and Layout Guidelines

Fixed Output Voltage Versions



Adjustable Output Voltage Versions

LOCATE THE PROGRAMMING RESISTORS NEAR THE FEEDBACK PIN USING SHORT LEADS KEEP FEEDBACK WIRING AWAY FROM INDUCTOR FLUX L1 4 Feedback GS5256 ADJ Regulated Output **OUTPUT** 5 ON/OFF 3 GND Unregulated DC Input ר ס

HEAVY LINES MUST KEPT SHORT AND USR GROUND PLANE CONSTRUCTION FOR BEST RESULTS

$$V_{OUT} = V_{REF} (1 + \frac{R2}{R1})$$

Where
$$V_{REF} = 1.23V$$

$$R_2 = R_1 (\frac{V_{\text{OUT}}}{V_{\text{REF}}} - 1)$$



GS5256 Design Procedure (Fixed Output)

PROCEDURE (Fixed Output Voltage Version)

 V_{OUT} = Regulated Output Voltage (3.3V, 5V, 12V) V_{IN} (max) = Maximum DC Input Voltage I_{LOAD} (max) = Maximum Load Current

1.Inductor Selection (L1)

- **A.** Select the correct inductor value selection guide. (Output voltages of 3.3V, 5V, or 12V respectively.) For all other voltages, see the design procedure for the adjustable version.
- **B.** From the inductor value selection guide, identify the inductance region intersected by the Maximum Input Voltage line and the Maximum Load Current line. Each region is identified by an inductance value and an inductor code (LXX).
- C. Select an appropriate inductor from the four manufacturer's part numbers listed.

2.Output Capacitor Selection (C_{OUT})

- **A.** In the majority of applications, low ESR (Equivalent Series Resistance) electrolytic capacitors between 82μF and 820μF and low ESR solid tantalum capacitors between 10μF and 470μF provide the best results. This capacitor should be located close to the IC using short capacitor leads and short copper traces. Do not use capacitors larger than 820μF.
- **B.** To simplify the capacitor selection procedure, refer to the quick design component selection table shown in Figure 2. This table contains different input voltages, output voltages, and load currents, and lists various inductors and output capacitors that will provide the best design solutions.
- **C.** The capacitor voltage rating for electrolytic capacitors should be at least 1.5 times greater than the output voltage, and often much higher voltage ratings are needed to satisfy the low ESR requirements for low output ripple voltage.

3.Catch Diode Selection (D1)

- **A.** The catch diode current rating must be at least 1.3 times greater than the maximum load current. Also, if the power supply design must withstand a continuous output short, the diode should have a current rating equal to the maximum current limit of the GS5256. The most stressful condition for this diode is an overload or shorted output condition.
- B. The reverse voltage rating of the diode should be at least 1.25 times the maximum input voltage.
- C. This diode must be fast (short reverse recovery time) and must be located close to the GS5256 using short leads and short printed circuit traces. Because of their fast switching speed and low forward voltage drop, Schottky diodes provide the best performance and efficiency, and should be the first choice, especially in low output voltage applications. Ultra-fast recovery, or High-Efficiency rectifiers also provide good results. Ultra-fast recovery diodes typically have reverse recovery times of 50 ns or less. Rectifiers such as the 1N5400 series are much too slow and should not be used.

4.Input Capacitor (C_{IN})

A low ESR aluminum or tantalum bypass capacitor is needed between the input pin and ground pin to prevent large volt-age transients from appearing at the input. This capacitor should be located close to the IC using short leads. In addition, the RMS current rating of the input capacitor should be selected to be at least 1/2 the DC load current. The capacitor manufacturers data sheet must be checked to assure that this current rating is not exceeded. The curve shown in Figure 9 shows typical RMS current ratings for several different aluminum electrolytic capacitor values. For an aluminum electrolytic, the capacitor voltage rating should be approximately 1.5 times the maximum input voltage. The tantalum capacitor voltage rating should be 2 times the maximum input voltage and it is recommended that they be surge current tested by the manufacturer. Use caution when using ceramic capacitors for input bypassing, because it may cause severe ringing at the $V_{\rm IN}$ pin

EXAMPLE (Fixed Output Voltage Version)

Given:

 $V_{OUT} = 5V$ $V_{IN} (max) = 12V$ $I_{LOAD} (max) = 3A$

1.Inductor Selection (L1)

- **A.** Use the inductor selection guide for the 5V version shown.
- **B.** From the inductor value selection guide shown in Figure 5, the inductance region intersected by the 12V horizontal line and the 3A vertical line is 33 μH, and the inductor code is L40.
- **C.** The inductance value required is 33 μH. From the table in Figure 8, go to the L40 line and choose an inductor part number from any of the four manufacturers shown. (In most in-stance, both through hole and surface mount inductors are available.)



2.Output Capacitor Selection (C_{OUT})

- A. See section on output capacitors in application information section.
- **B.** From the quick design component selection table shown in Figure 2, locate the 5V output voltage section. In the load current column, choose the load current line that is closest to the current needed in your application, for this example, use the 3A line. In the maximum input voltage column, select the line that covers the input voltage needed in your application, in this example, use the 15V line. Continuing on this line are recommended inductors and capacitors that will provide the best overall performance. The capacitor list contains both through hole electrolytic and surface mount tantalum capacitors from four different capacitor manufacturers. It is recommended that both the manufacturers and the manufacturer's series that are listed in the table be used. In this example aluminum electrolytic capacitors from several different manufacturers are available with the range of ESR numbers needed. 330µF 35V
- C. For a 5V output, a capacitor voltage rating at least 7.5V or more is needed. But even a low ESR, switching grade, 220µF 10V aluminum electrolytic capacitor would exhibit approximately 225 mW of ESR (see the curve in Figure 14 for the ESR vs voltage rating). This amount of ESR would result in relatively high output ripple voltage. To reduce the ripple to1% of the output voltage, or less, a capacitor with a higher value or with a higher voltage rating (lower ESR) should be selected. A 16V or 25V capacitor will reduce the ripple volt-age by approximately half.

3.Catch Diode Selection (D1)

A.Refer to the table shown in Figure 11. In this example, a 5A, 20V, Schottky diode will provide the best performance, and will not be overstressed even for a shorted output.

4.Input Capacitor (C_{IN})

The important parameters for the Input capacitor are the input voltage rating and the RMS current rating. With a nominal input voltage of 12V, an aluminum electrolytic capacitor with a voltage rating greater than 18V (1.5 x V_{IN}) would be needed. The next higher capacitor voltage rating is 25V. The RMS current rating requirement for the input capacitor in a buck regulator is approximately 1 /2 the DC load current. In this example, with a 3A load, a capacitor with a RMS current rating of at least 1.5A is needed. The curves shown in Figure 9 can be used to select an appropriate input capacitor. From the curves, locate the 35V line and note which capacitor values have RMS current ratings greater than 1.5A. A 680 μ F/35V capacitor could be used.

For a through hole design, a 680μ F/35V electrolytic capacitor (Panasonic HFQ series or Nichicon PL series or equivalent) would be adequate. other types or other manufacturers capacitors can be used provided the RMS ripple current ratings are adequate.

For surface mount designs, solid tantalum capacitors can be used, but caution must be exercised with regard to the capacitor surge current rating.

GS5256 Design Procedure (Fixed Output)

	Conditions			
Output Voltage (V)	Output Voltage (V) Load Current (A) Max Input Voltage (V)			
		5	22	
	3	7	22	
	3	10	22	
3.3		40	33	
	2	6	22	
		10	33	
		40	47	
		8	22	
	3	10	22	
	3	15	33	
5.0		40	47	
		9	22	
	2	20	68	
		40	68	

Figure 2. GS5256 Fixed Voltage Quick Design Component Selection Table



GS5256 Design Procedure (Adjustable Output)

PROCEDURE (Adjustable Output Voltage Version)

Given:

 V_{OUT} = Regulated Output Voltage V_{IN} (max) = Maximum Input Voltage I_{LOAD} (max) = Maximum Load Current

F = Switching Frequency (Fixed at a nominal 150kHz)

1. Programming Output Voltage

(Selecting R_1 and R_2 , as shown in Figure 1)

Use the following formula to select the appropriate resistor values.

$$V_{OUT} = V_{REF} (1 + \frac{R_2}{R_1})$$
 where $V_{REF} = 1.23V$

Select a value for R1 between 240ς and $1.5k\varsigma$. The lower resistor values minimize noise pickup in the sensitive feedback pin. (For the lowest temperature coefficient and the best stability with time, use 1% metal film resistors.)

$$R_2 = R_1 \left(\frac{V_{OUT}}{V_{REF}} - 1 \right)$$

2.Inductor Selection (L1)

A.Calculate the inductor Volt . microsecond constant E•T (V•µs), from the following formula:

$$E-T = (V_{IN} - V_{OUT} - V_{SAT}) - \frac{Vout+VD}{VIN-VSAT+VD} - \frac{1000}{150kHz} (V-\mu s)$$

where V_{SAT} = internal switch saturation voltage = 1.16V

and V_D = diode forward voltage drop = 0.5V

B.Use the E•T value from the previous formula and match it with the E•T number on the vertical axis of the Inductor Value Selection Guide shown in Figure 7.

C.on the horizontal axis, select the maximum load current.

D.Identify the inductance region intersected by the E•T value and the Maximum Load Current value. Each region is identified by an inductance value and an inductor code (LXX).

E. Select an appropriate inductor from the four manufacturer's part numbers listed in Figure 8.

3. Output Capacitor Selection (Cout)

A.In the majority of applications, low ESR electrolytic or solid tantalum capacitors between 82μF and 820μF provide the best results. This capacitor should be located close to the IC using short capacitor leads and short copper traces. Do not use capacitors larger than 820μF.

B.To simplify the capacitor selection procedure, refer to the quick design table shown in Figure 3. This table contains different output voltages, and lists various output capacitors that will provide the best design solutions.

C.The capacitor voltage rating should be at least 1.5 times greater than the output voltage, and often much higher voltage ratings are needed to satisfy the low ESR requirements needed for low output ripple voltage.

4.Feedforward Capacitor (C_{FF})

For output voltages greater than approximately 10V, an additional capacitor is required. The compensation capacitor is typically between 100 pF and 33 nF, and is wired in parallel with the output voltage setting resistor, R2. It provides additional stability for high output voltages, low input-output voltages, and/or very low ESR output capacitors, such as solid tantalum capacitors.

$$C_{FF} = \frac{1}{31 \times 10^3 \times R_2}$$

This capacitor type can be ceramic, plastic, silver mica, etc.

EXAMPLE (Adjustable Output Voltage Version)

Given:

 $V_{OUT} = 20V$

 $V_{IN}(max) = 28V$

 $I_{LOAD}(max) = 3A$

F = Switching Frequency (Fixed at a nominal 150 kHz).



1.Programming Output Voltage

(Selecting R_1 and R_2 , as shown in Figure 1) Select R_1 to be $1k_{\zeta}$, 1%. Solve for R_2 .

$$R_2 = R_1 \left(\frac{V_{OUT}}{V_{REF}} - 1 \right) = 1k \left(\frac{20V}{1.23V} - 1 \right)$$

 $R_2 {=}\, 1k$ (16.26-1)=15.26k, closest 1% value is a 15.4K $R_2 = 15.4k_{\mbox{\scriptsize G}}.$

2.Inductor Selection (L1)

A. Calculate the inductor Volt-microsecond constant (E-T),

E•T =
$$(28 - 20 - 1.16)$$
• $\frac{20+0.5}{28-1.16+0.5}$ • $\frac{1000}{150 \text{kHz}} \text{(V•}\mu\text{s)}$

E•T =
$$(6.84)$$
• $\frac{20.5}{27.34}$ • $6.67(V$ • μ s) = $34.2(V$ • μ s)

B.E•T = 34.2(V•µs)

 $C.I_{LOAD}$ (max) = 3A

D.From the inductor value selection guide shown in Figure 7, the inductance region intersected by the 34 (V•μs) horizontal line and the 3A vertical line is 47μH, and the inductor code is L39.

E.From the table in Figure 8, locate line L39, and select an inductor part number from the list of manufacturers part numbers.

3.Output Capacitor Selection (Cout)

A.See section on C_{OUT} in Application Information section.

B.From the quick design table shown in Figure 3, locate the output voltage column. From that column, locate the output voltage closest to the output voltage in your application. In this example, select the 24V line. Under the output capacitor section, select a capacitor from the list of through hole electrolytic or surface mount tantalum types from four different capacitor manufacturers. It is recommended that both the manufacturers and the manufacturers series that are listed in the table be used. In this example, through hole aluminum electrolytic capacitors from several different manufacturers are available.

220µF/35V Panasonic HFQ Series

150µF/35V Nichicon PL Series

C.For a 20V output, a capacitor rating of at least 30V or more is needed. In this example, either a 35V or 50V capacitor would work. A 35V rating was chosen, although a 50V rating could also be used if a lower output ripple voltage is needed.

Other manufacturers or other types of capacitors may also be used, provided the capacitor specifications (especially the 100 kHz ESR) closely match the types listed in the table. Refer to the capacitor manufacturers data sheet for this information.

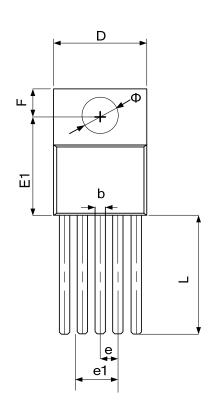
4. Feedforward Capacitor (C_{FF})

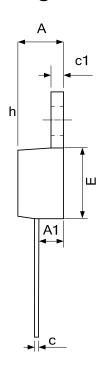
The table shown in Figure 3 contains feed forward capacitor values for various output voltages. In this example, a 560 pF capacitor is needed.



Package Dimension

TO-220-5 Plastic Package

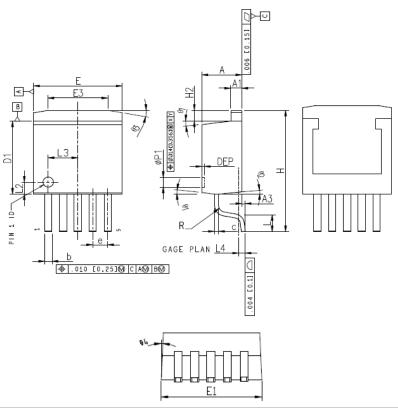




	Dimensions						
SYMBOL	Millin	neters	Inc	ches			
STIVIBOL	MIN	MAX	MIN	MAX			
Α	4.47	4.67	.176	.184			
A 1	2.52	2.82	.099	.111			
b	0.71	0.91	.028	.036			
С	0.31	0.53	.012	.021			
c1	1.17	1.37	.046	.054			
D	9.85	10.15	.388	.400			
E	8.20	8.60	.323	.339			
E1	11.76	12.16	.463	.479			
е	1.70	(TYP)	0.067	7(TYP)			
e1	6.70	6.90	.264	.272			
F	2.59	2.89	.102	.114			
L	13.50	13.90	.531	.547			
Φ	3.79	3.89	.149	.153			



TO-263-5L Plastic Package



		Di	mension	ıs		
SYMBOL		Millimeters		Inches		
SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX
Α	4.45	4.57	4.70	0.175	0.180	0.185
A1	1.22	1.27	1.32	0.048	0.050	0.052
А3	0	-	0.15	0.000	-	0.006
b	0.71	-	0.97	0.028	-	0.038
С	0.38	-	0.76	0.015	-	0.030
D1	8.38	8.70	8.89	0.330	0.343	0.350
E	9.91	10.16	10.39	0.390	0.400	0.410
E1	10.03	10.16	10.54	0.395	0.400	0.415
E3	6.61	6.86	7.11	0.260	0.270	0.280
е		1.70 (BSC)			0.067 (BSC)	
Н	-	-	15.88	-	-	0.625
H2	-	-	1.27	-	-	0.050
L	-	1.98	-	-	0.078	-
L2		1.47 (REF)			0.058 (REF)	
L3		3.40 (REF)			0.134 (REF)	



	Dimensions						
SYMBOL Millimeters Inches							
STIMBUL	MIN	NOM	MAX	MIN	NOM	MAX	
L4	-	0.76	-	-	0.030	-	
Ф _Р 1	1.07	1.20	1.32	0.042	0.047	0.052	
R	-	-	0.76	-	-	0.030	
θ1	-	7°	-	-	7°	-	
θ2	-	3°	-	-	3°	-	
θ3	7°	10°	13°	7°	10°	13°	
θ4	-	3°	-	-	3°	-	
DEP	0.10	0.18	0.25	0.004	0.007	0.010	



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