

GSB1386 PNP EPITAXIAL SILICON TRANSISTOR

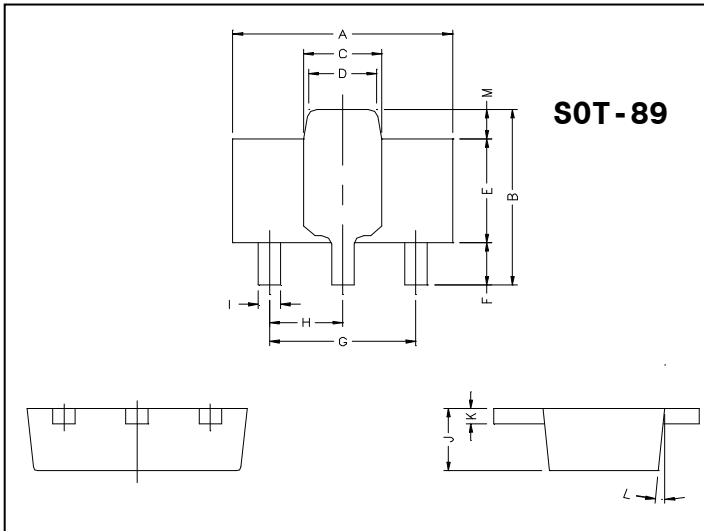
Description

The GSB1386 is a epitaxial planar type PNP silicon transistor .

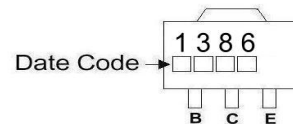
Features

- *Low $V_{CE(sat)}$. $V_{CE(sat)} = -0.35V(Typ.)$ ($I_C/I_B = -4A / -0.1A$).
- *Excellent DC current gain characteristics.
- *Complements the GSD2098/GSD2118/GSD2097.

Package Dimensions



Marking :



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.4	4.6	G	3.00	REF.
B	4.05	4.25	H	1.50	REF.
C	1.50	1.70	I	0.40	0.52
D	1.30	1.50	J	1.40	1.60
E	2.40	2.60	K	0.35	0.41
F	0.89	1.20	L	5°	TYP.
			M	0.70	REF.

Absolute Maximum Ratings (Ta = 25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Junction Temperature	Tj	+150	°C
Storage Temperature	Tstg	-55 ~ +150	°C
Collector to Base Voltage	V _{CB0}	-30	V
Collector to Emitter Voltage	V _{CEO}	-20	V
Emitter to Base Voltage	V _{EBO}	-6	V
Collector Current(DC)	I _C	-5	A
Collector Current(PULSE) (note1)		-10	A
Collector Power Dissipation	P _D	0.5	W
Collector Power Dissipation (note2)		2	W

* 1. Single pulse, PW=100ms.

* 2. When mounted on a 40*40*0.7 mm ceramic board.

Electrical Characteristics (Ta = 25°C, unless otherwise specified)

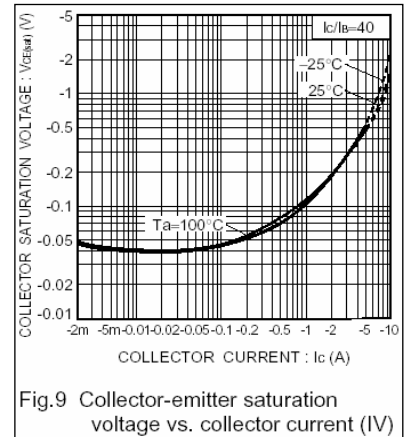
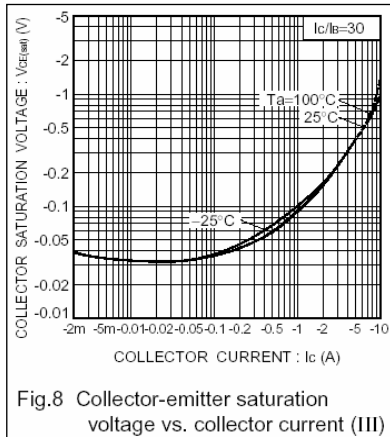
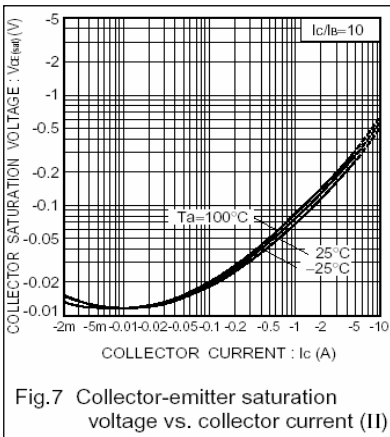
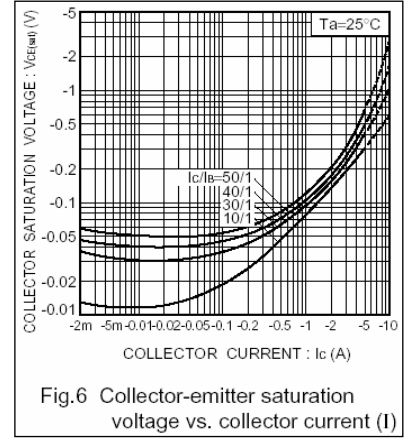
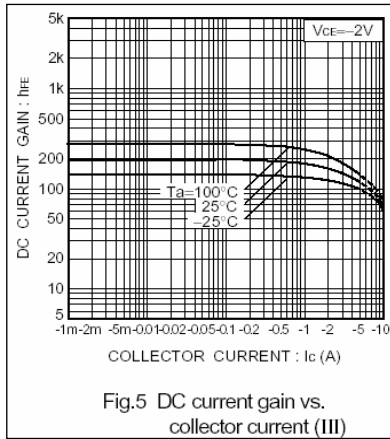
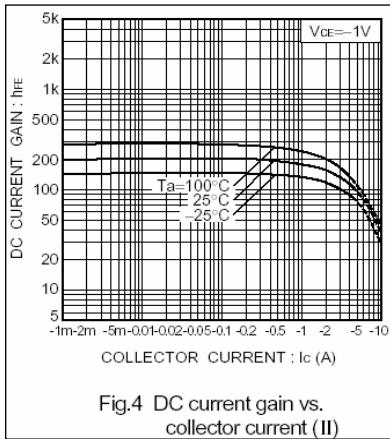
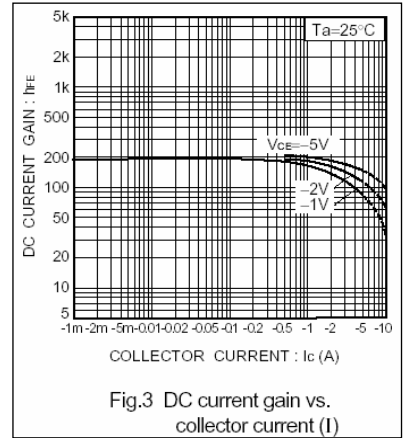
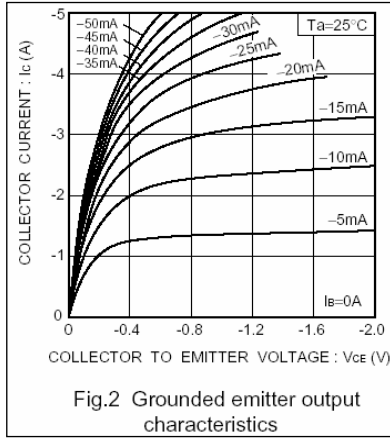
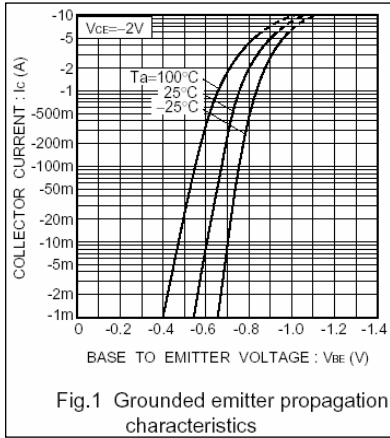
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
V _{CB0}	-30	-	-	V	I _C =-50uA
V _{CEO}	-20	-	-	V	I _C =-1mA
V _{EBO}	-6	-	-	V	I _E =-50uA
I _{CB0}	-	-	-0.5	uA	V _{CB} =-20V
I _{EBO}	-	-	-0.5	uA	V _{EB} =-5V
V _{CE(sat)}	-	-	-1	V	I _C =-4A, I _B =-0.1A
h _{FE}	82	-	390		V _{CE} =-2V, I _C =-0.5A
f _T	-	120	-	MHz	V _{CE} =-6V, I _E =-50mA, f=30MHz
C _{ob}	-	60	-	pF	V _{CB} =-20V, I _E =0A, f=1MHz

Note: Measured using pulse current.

Classification Of hFE

Rank	P	Q	R
RANGE	82 - 180	120 - 270	180 - 390

Characteristics Curve



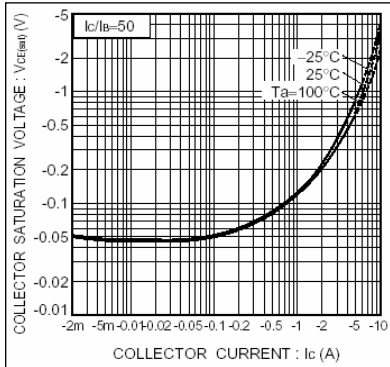


Fig.10 Collector-emitter saturation voltage vs. collector current (V)

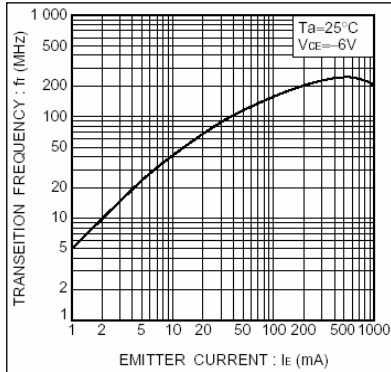


Fig.11 Gain bandwidth product vs. emitter current

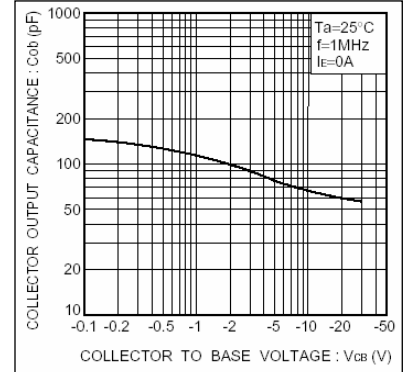


Fig.12 Collector output capacitance vs. collector-base voltage

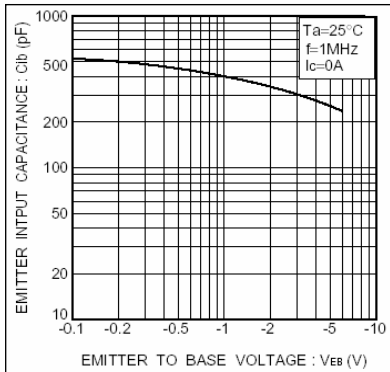


Fig.13 Emitter input capacitance vs. emitter-base voltage

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