

GSC4404

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

BVDSS	30V
RDS(ON)	24mΩ
ID	8.5A

Description

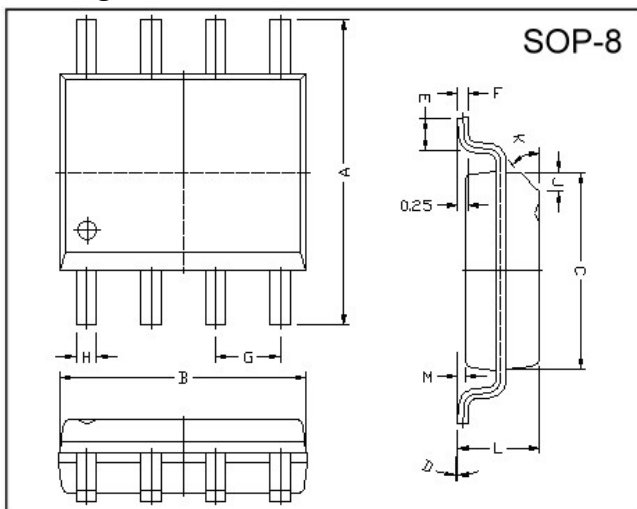
The GSC4404 uses advanced trench technology to provide excellent on-resistance, low gate charge and operation with gate voltage as low as 2.5V.

The SOP-8 package is universally preferred for all commercial-industrial surface mount applications and suited for use as a load switch or in PWM applications.

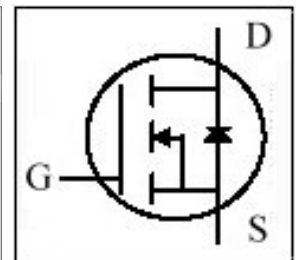
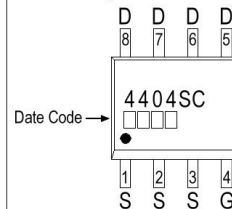
Features

- *Simple Drive Requirement
- *Lower On-resistance
- *Fast Switching Characteristic

Package Dimensions



Marking :



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	5.80	6.20	M	0.10	0.25
B	4.80	5.00	H	0.35	0.49
C	3.80	4.00	L	1.35	1.75
D	0°	8°	J	0.375 REF.	
E	0.40	0.90	K	45°	
F	0.19	0.25	G	1.27 TYP.	

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	±12	V
Continuous Drain Current, $V_{GS}@10V$	$I_D @TA=25^{\circ}C$	8.5	A
Continuous Drain Current, $V_{GS}@10V$	$I_D @TA=70^{\circ}C$	7.1	A
Pulsed Drain Current ¹	I_{DM}	60	A
Total Power Dissipation	$P_D @TA=25^{\circ}C$	2.5	W
Linear Derating Factor		0.02	W/°C
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-ambient Max.	$R_{thj-amb}$	50	°C/W

Electrical Characteristics (T_j = 25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	V _{GS} =0, I _D =250uA
Gate Threshold Voltage	V _{GS(th)}	0.7	-	1.4	V	V _{DS} =V _{GS} , I _D =250uA
Forward Transconductance	g _{fs}	-	16	-	S	V _{DS} =5V, I _D =5A
Gate-Source Leakage Current	I _{GSS}	-	-	±100	nA	V _{GS} = ±12V
Drain-Source Leakage Current(T _j =25°C)	I _{DSS}	-	-	1	uA	V _{DS} =30V, V _{GS} =0
Drain-Source Leakage Current(T _j =55°C)		-	-	5	uA	V _{DS} =24V, V _{GS} =0
Static Drain-Source On-Resistance	R _{DS(ON)}	-	-	24	mΩ	V _{GS} =10V, I _D =8.5A
		-	-	30		V _{GS} =4.5V, I _D =8.5A
		-	-	48		V _{GS} =2.5V, I _D =5.0A
Total Gate Charge ²	Q _g	-	9.7	12	nC	I _D =8.5A V _{DS} =15V V _{GS} =4.5V
Gate-Source Charge	Q _{gs}	-	1.63	-		
Gate-Drain ("Miller") Charge	Q _{gd}	-	3.1	-		
Turn-on Delay Time ²	T _{d(on)}	-	3.3	-	ns	V _{DS} =15V V _{GS} =10V R _G =6Ω R _L =1.8Ω
Rise Time	T _r	-	4.7	-		
Turn-off Delay Time	T _{d(off)}	-	26	-		
Fall Time	T _f	-	4.1	-		
Input Capacitance	C _{iss}	-	857	1050	pF	V _{GS} =0V V _{DS} =15V f=1.0MHz
Output Capacitance	C _{oss}	-	97	-		
Reverse Transfer Capacitance	C _{rss}	-	71	-		

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V _{SD}	-	-	1.0	V	I _S =1.0A, V _{GS} =0V
Continuous Source Current (Body Diode)	I _S	-	-	4.3	A	V _D = V _G =0V, V _S =1.0V
Reverse Recovery Time ²	T _{rr}	-	15	-	ns	I _S =5.0A, V _{GS} =0V di/dt=100A/μs
Reverse Recovery Charge	Q _{rr}	-	8.6	-	nC	

Notes: 1. Pulse width limited by Max. junction temperature.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

3. Surface mounted on 1 in² copper pad of FR4 board; 125°C/W when mounted on Min. copper pad.

Characteristics Curve

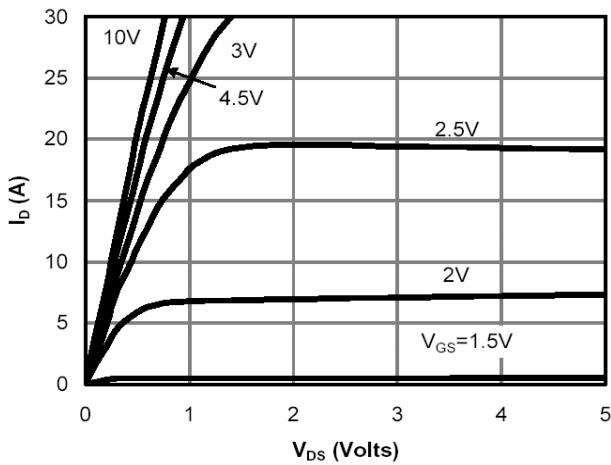


Fig 1. Typical Output Characteristics

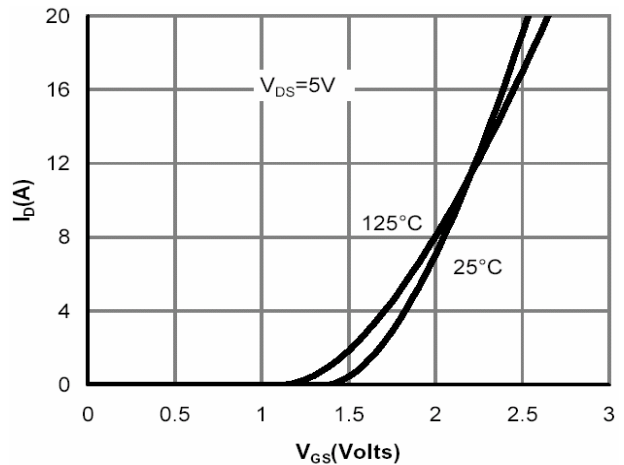


Fig 2. Transfer Characteristics

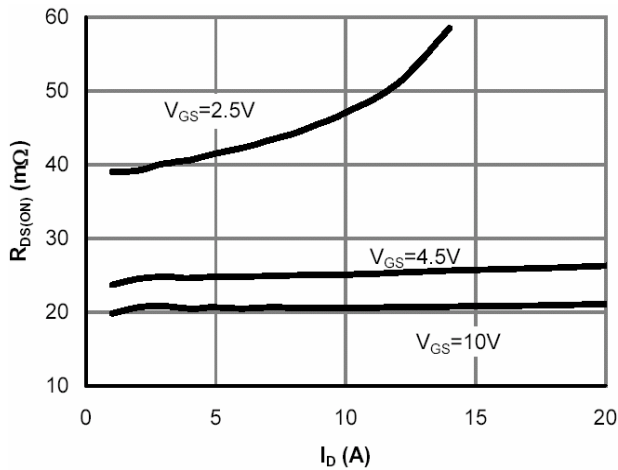


Fig 3. On-Resistance v.s. Drain Current and Gate Voltage

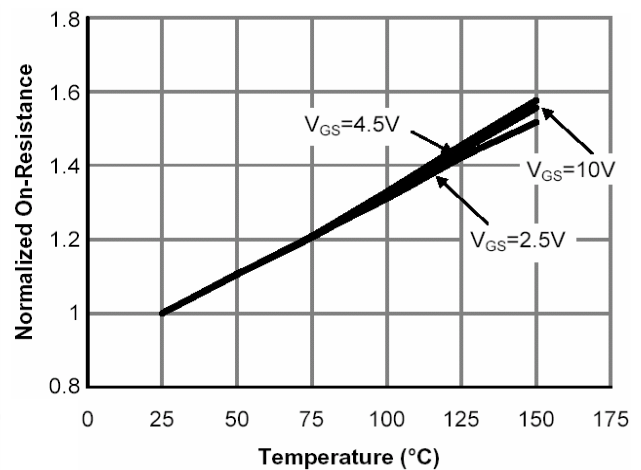


Fig 4. On-Resistance v.s. Junction Temperature

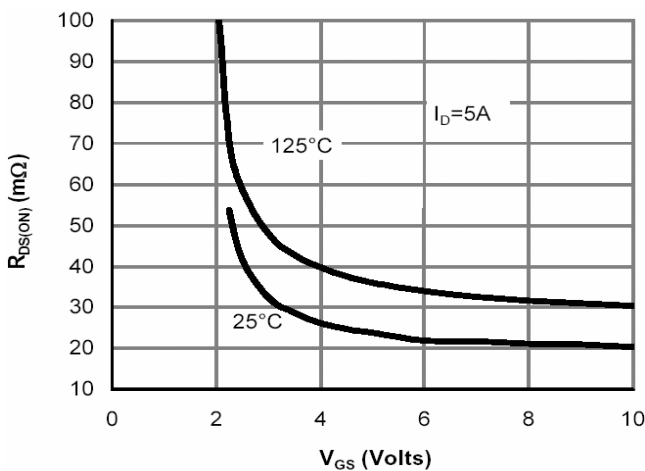


Fig 5. On-Resistance v.s. Gate-Source Voltage

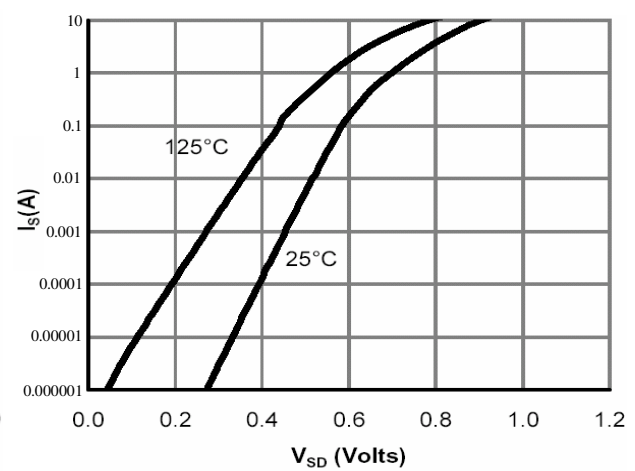


Fig 6. Body Diode Characteristics

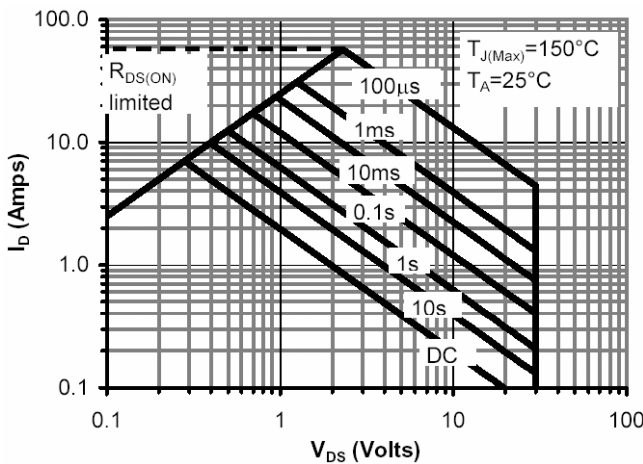


Fig 7. Maximum Safe Operating Area

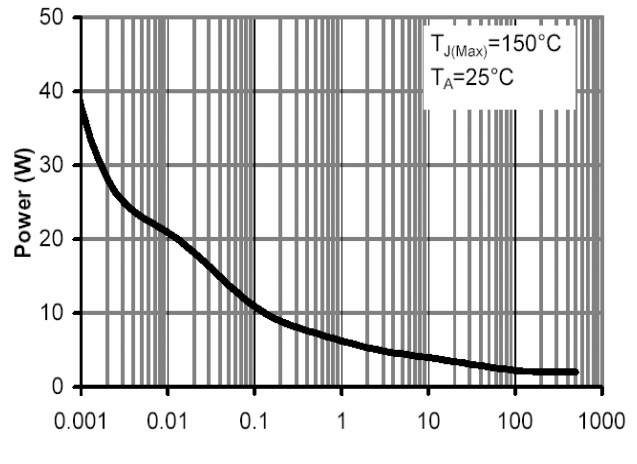


Fig 8. Single Pulse Maximum Power Dissipation

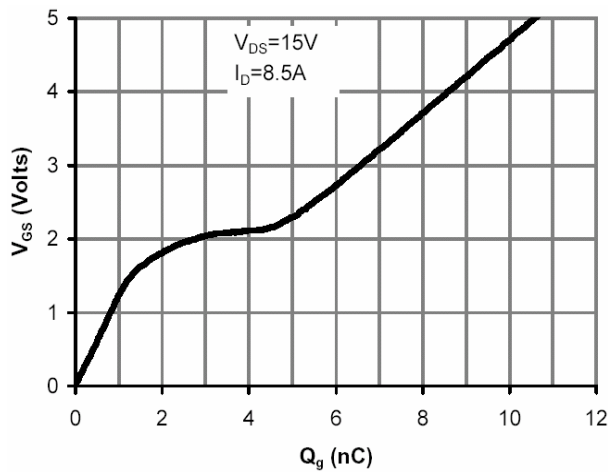


Fig 9. Gate Charge Characteristics

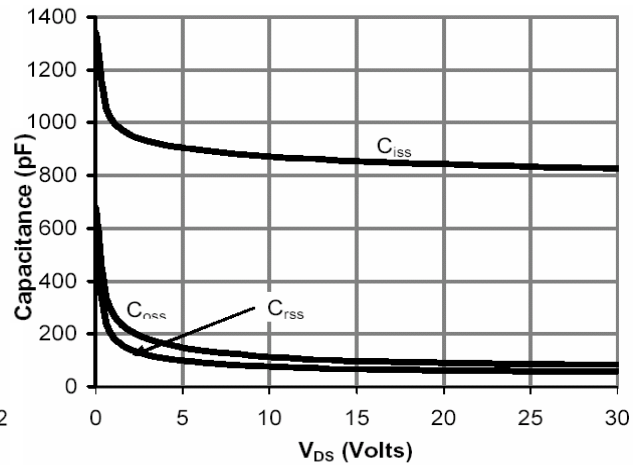


Fig 10. Typical Capacitance Characteristics

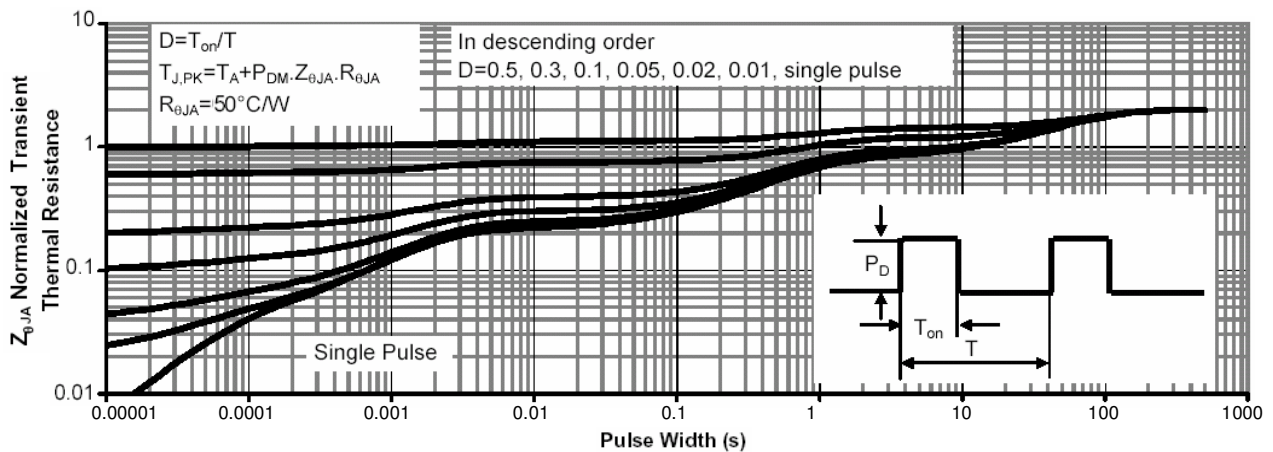


Fig 11. Normalized Maximum Transient Thermal Impedance

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