

# GSM2302AS

## 20V N-Channel Enhancement Mode MOSFET

### Product Description

GSM2302AS, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge.

These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

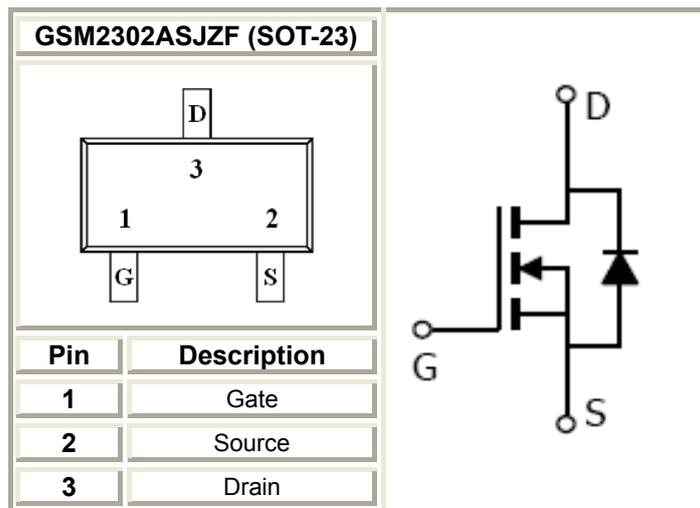
### Features

- 20V/2.4A,  $R_{DS(ON)}=90m\Omega@V_{GS}=4.5V$
- 20V/2.0A,  $R_{DS(ON)}=110m\Omega@V_{GS}=2.5V$
- Super high density cell design for extremely low  $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- SOT-23 package design

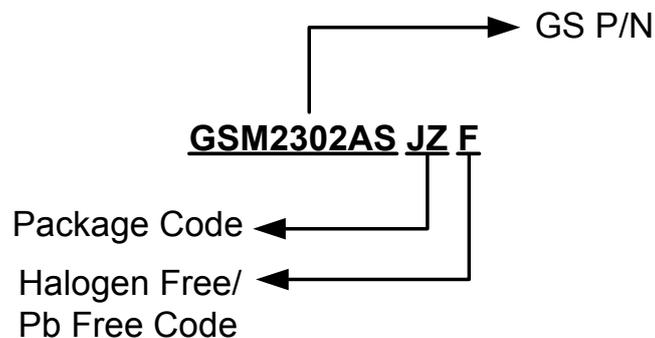
### Applications

- Portable Equipment
- Battery Powered System
- Net Working System

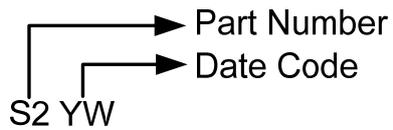
### Packages & Pin Assignments



### Ordering Information



## Marking Information



Part Number	Package	Part Marking	Quantity
GSM2302ASJZF	SOT-23	S2YW	3000pcs

## Absolute Maximum Ratings

$T_A=25^{\circ}\text{C}$  unless otherwise noted

Symbol	Parameter	Typical	Unit
$V_{DSS}$	Drain-Source Voltage	20	V
$V_{GSS}$	Gate –Source Voltage	$\pm 12$	V
$I_D$	Continuous Drain Current( $T_J=150^{\circ}\text{C}$ )	$T_A=25^{\circ}\text{C}$	2.4
		$T_A=70^{\circ}\text{C}$	2.0
$I_{DM}$	Pulsed Drain Current	10	A
$I_S$	Continuous Source Current(Diode Conduction)	1.6	A
$P_D$	Power Dissipation	$T_A=25^{\circ}\text{C}$	1.25
		$T_A=70^{\circ}\text{C}$	0.8
$T_J$	Operating Junction Temperature	150	$^{\circ}\text{C}$
$T_{STG}$	Storage Temperature Range	-55/150	$^{\circ}\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	120	$^{\circ}\text{C}/\text{W}$

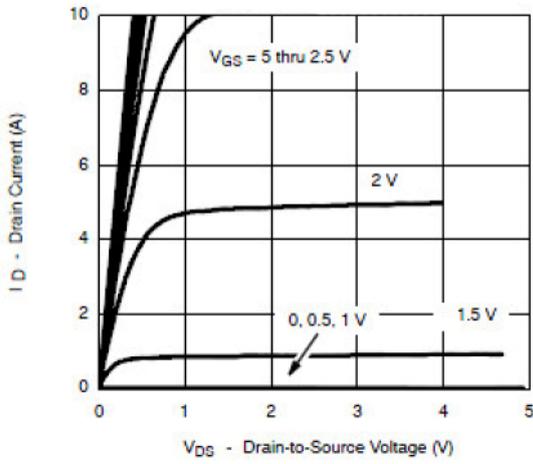
## Electrical Characteristics

T<sub>A</sub>=25°C unless otherwise noted

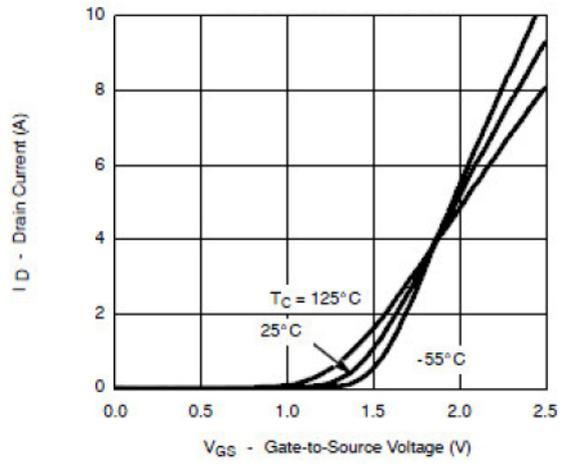
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	20			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	0.5		1.0	V
I <sub>GSS</sub>	Gate Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			±100	nA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 16V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> = 16V, V <sub>GS</sub> =0V, T <sub>J</sub> =85°C			10	
I <sub>D(on)</sub>	On-State Drain Current	V <sub>DS</sub> ≥5V, V <sub>GS</sub> =4.5V	6			A
		V <sub>DS</sub> ≥5V, V <sub>GS</sub> =2.5V	4			
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =4.5V, I <sub>D</sub> =2.4A		70	90	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =2.0A		90	110	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =3.6A		10		S
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =1.6A, V <sub>GS</sub> =0V		0.85	1.2	V
<b>Dynamic</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V f=1MHz		340		pF
C <sub>oss</sub>	Output Capacitance			115		
C <sub>rss</sub>	Reverse Transfer Capacitance			33		
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> ≅3.6A		5.4	10	nC
Q <sub>gs</sub>	Gate-Source Charge			0.65		
Q <sub>gd</sub>	Gate-Drain Charge			1.4		
t <sub>d(on)</sub>	Turn-On Time	V <sub>DD</sub> =10V, R <sub>L</sub> =5.5Ω, I <sub>D</sub> =3.6A, V <sub>GEN</sub> =4.5V, R <sub>G</sub> =6Ω		12	25	ns
t <sub>r</sub>				36	60	
t <sub>d(off)</sub>	Turn-Off Time			34	60	
t <sub>f</sub>				10	25	

## Typical Performance Characteristics

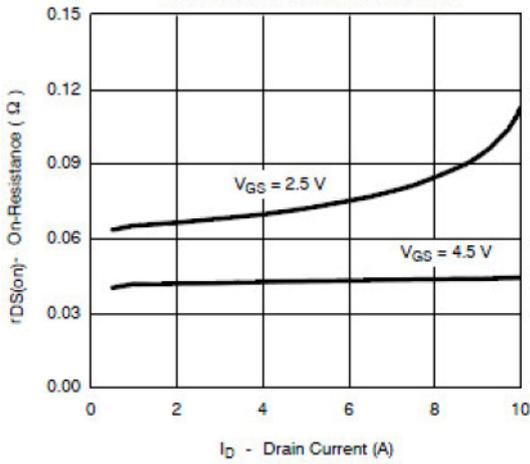
### Output Characteristics



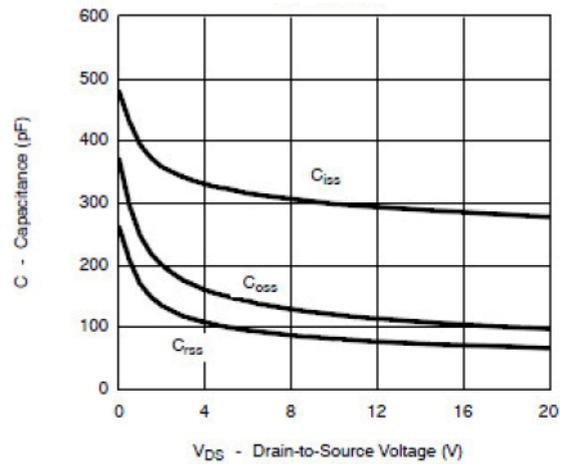
### Transfer Characteristics



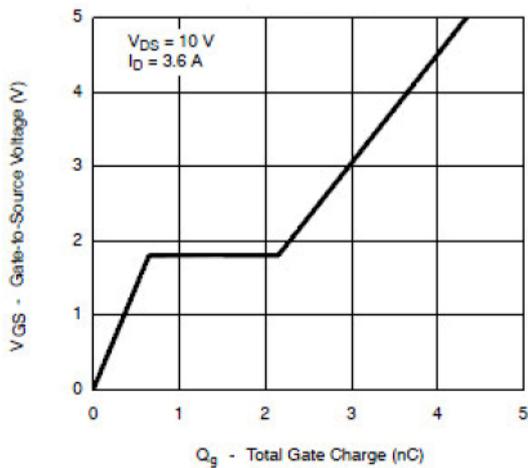
### On-Resistance vs. Drain Current



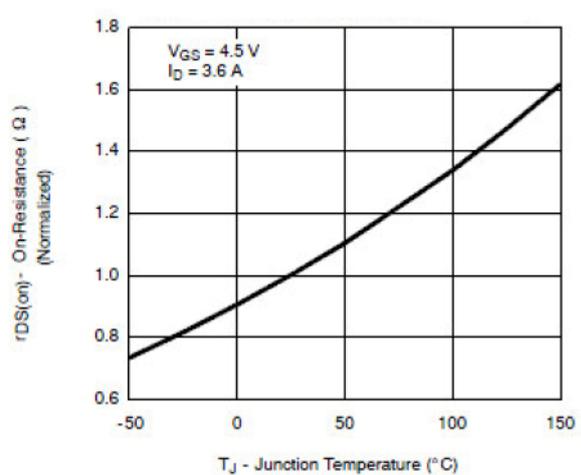
### Capacitance



### Gate Charge

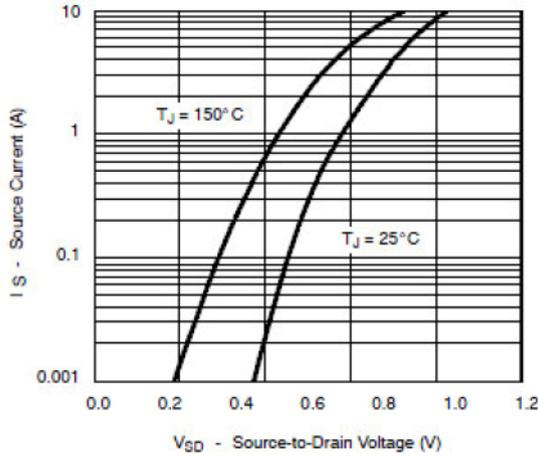


### On-Resistance vs. Junction Temperature

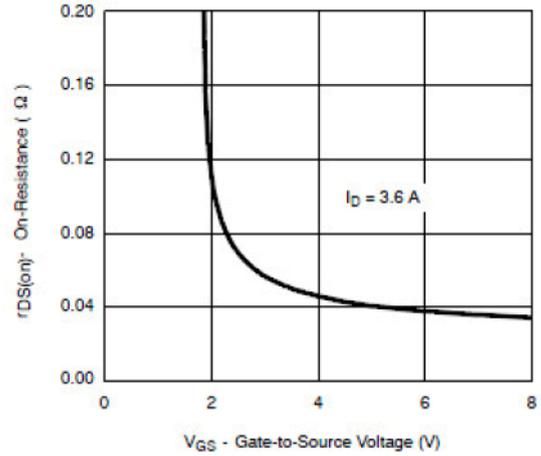


## Typical Performance Characteristics (continue)

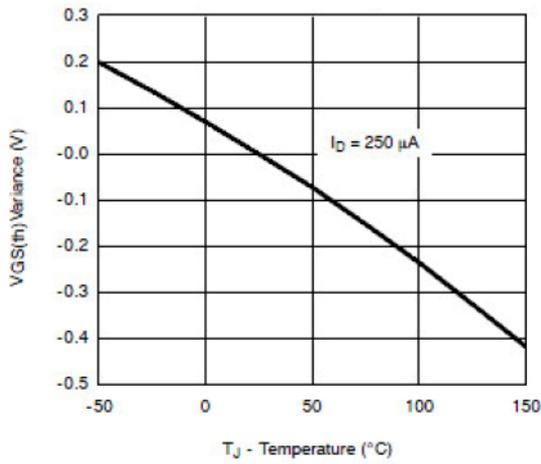
### Source-Drain Diode Forward Voltage



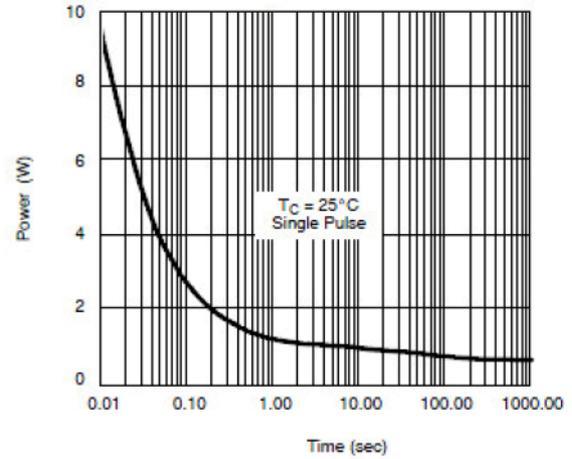
### On-Resistance vs. Gate-to-Source Voltage



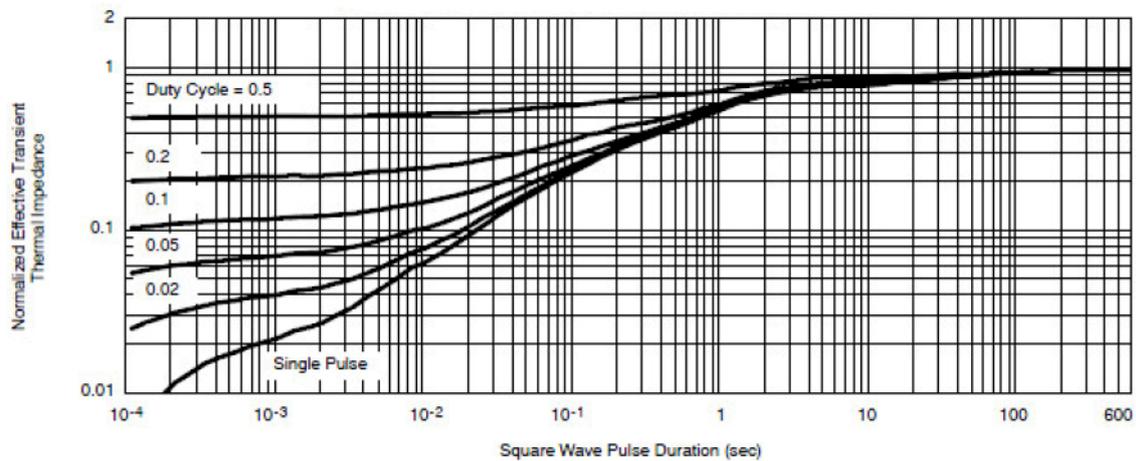
### Threshold Voltage



### Single Pulse Power

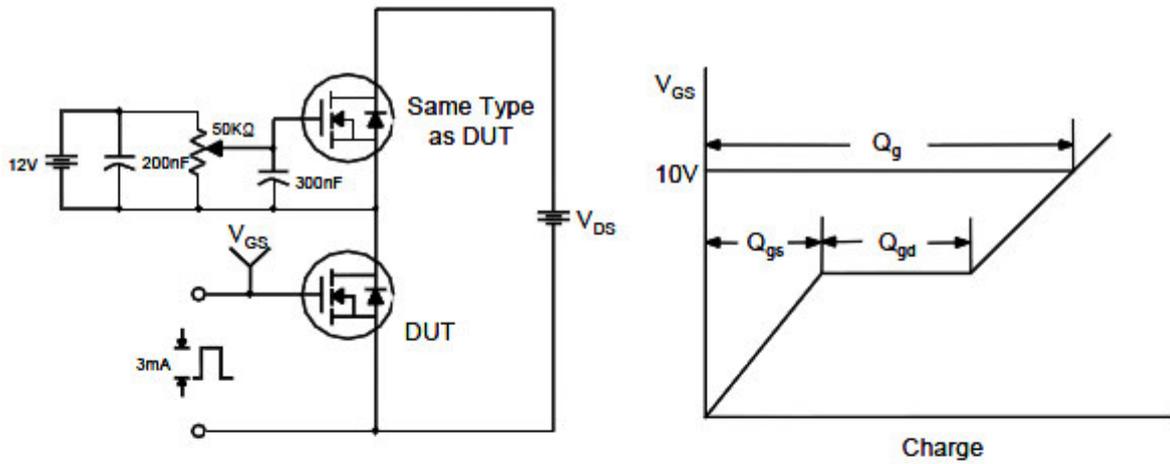


### Normalized Thermal Transient Impedance, Junction-to-Ambient

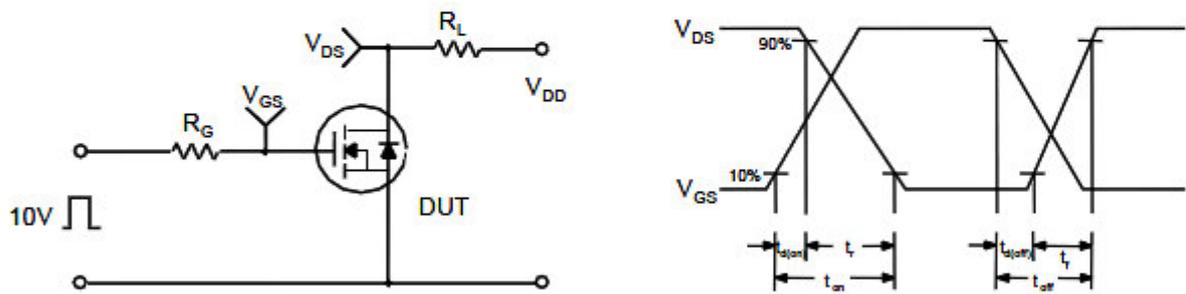


## Typical Performance Characteristics (continue)

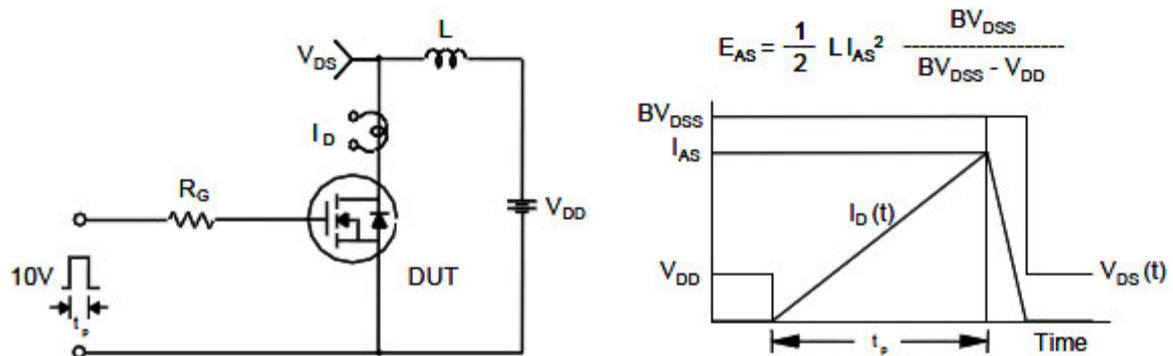
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms

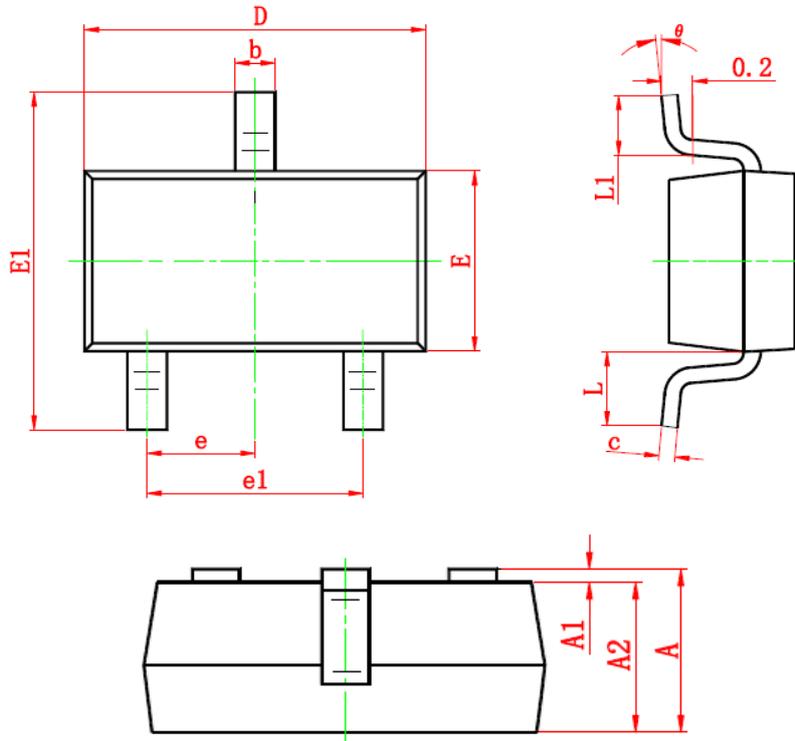


### Unclamped Inductive Switching Test Circuit & Waveforms



## Package Dimension

# SOT-23



### Dimensions

Symbol	Millimeters		Inches	
	Min	Max	Min	Max
<b>A</b>	0.900	1.200	0.035	0.043
<b>A1</b>	0.000	0.100	0.000	0.004
<b>A2</b>	0.900	1.100	0.035	0.039
<b>b</b>	0.300	0.500	0.012	0.020
<b>c</b>	0.080	0.150	0.003	0.006
<b>D</b>	2.800	3.000	0.110	0.118
<b>E</b>	1.200	1.400	0.047	0.055
<b>E1</b>	2.250	2.550	0.089	0.100
<b>e</b>	0.950 TYP		0.037 TYP	
<b>e1</b>	1.800	2.000	0.071	0.079
<b>L</b>	0.550 REF		0.022 REF	
<b>L1</b>	0.300	0.500	0.012	0.020
<b>θ</b>	0°	8°	0°	6°

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