

GSM2309S

20V P-Channel Enhancement Mode MOSFET

Product Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

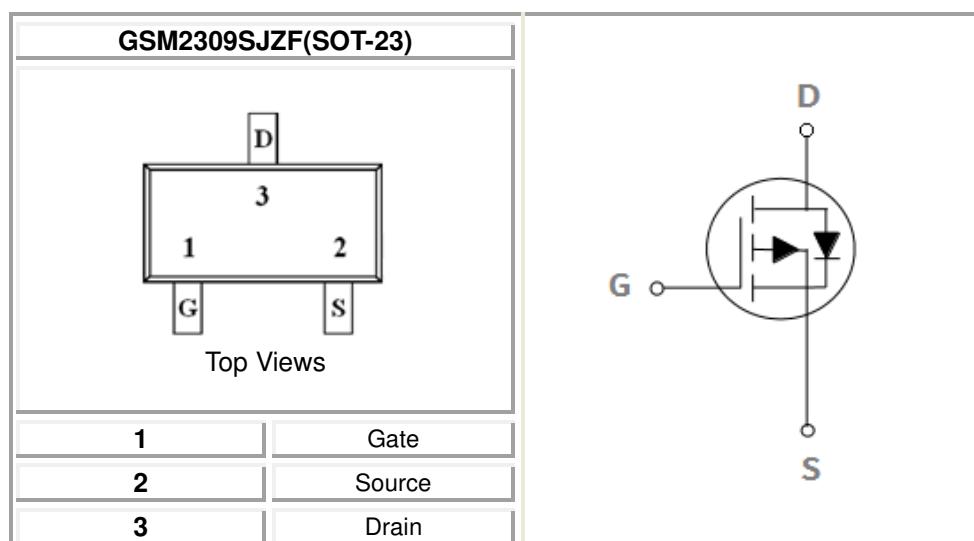
Features

- -20V/-5.8A, $R_{DS(ON)}=33m\Omega$ @ $V_{GS}=-4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for -1.8V Gate Drive Applications

Applications

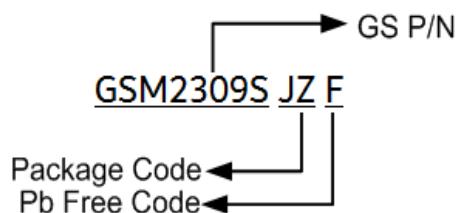
- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

Packages & Pin Assignments



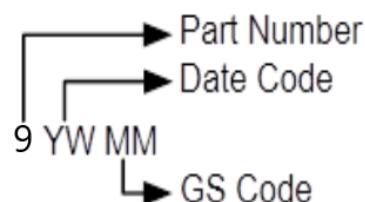
GSM2309S

Ordering Information



Part Number	Package	Quantity Reel
GSM2309SJZF	SOT-23	3000 PCS

Marking Information



Part Number	Package	Part Marking
GSM2309SJZF	SOT-23	9YWMM

Absolute Maximum Ratings

(T_C=25°C unless otherwise noted)

Symbol	Parameter	Typical	Unit	
V _{DSS}	Drain-Source Voltage	-20	V	
V _{GSS}	Gate -Source Voltage	±10	V	
I _D	Continuous Drain Current(T _J =150°C)	T _C =25°C T _C =100°C	-5.8 -3.7	A
I _{DM}	Pulsed Drain Current ¹	-23.2	A	
P _D	Power Dissipation	T _C =25°C	1.56	W
	Power Dissipation	Derate above T _C =25°C	0.012	W/°C
T _J	Operating Junction Temperature	-55 to 150	°C	
T _{STG}	Storage Temperature Range	-55 to 150	°C	
Symbol	Parameter	Max	Unit	
R _{θJA}	Thermal Resistance-Junction to Ambient	80	°C/W	

Electrical Characteristics

($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=-250\mu\text{A}$	-20			V
$\Delta BV_{\text{DSS}}/\Delta T_J$	BV _{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$		-0.02		V°C
I_{GSS}	Gate-Source Leakage Current	$V_{DS}=0\text{V}, V_{GS}=\pm 10\text{V}$			± 100	nA
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-20\text{V}, V_{GS}=0\text{V}$ $T_J=25^\circ\text{C}$			-1	uA
		$V_{DS}=-16\text{V}, V_{GS}=0\text{V}$ $T_J=125^\circ\text{C}$			-10	
$R_{DS(\text{on})}$	Drain-Source On-Resistance	$V_{GS} = -4.5\text{V}, I_D = -4\text{A}$		28	33	$\text{m}\Omega$
		$V_{GS} = -2.5\text{V}, I_D = -3\text{A}$		37	45	
		$V_{GS} = -1.8\text{V}, I_D = -2\text{A}$		49	65	
g_{FS}	Forward Transconductance	$V_{DS}=-10\text{V}, I_D=-3\text{A}$		8.4		S
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu\text{A}$	-0.3	-0.6	-1	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient			2		mV°C
Dynamic						
Q_g	Total Gate Charge ^{2,3}			16.1	25	nC
Q_{gs}	Gate-Source Charge ^{2,3}	$V_{DS}=-10\text{V}, V_{GS}=-4.5\text{V}, I_D=-4.0\text{A}$		1.8	3	
Q_{gd}	Gate-Drain Charge ^{2,3}			3.8	7	
C_{iss}	Input Capacitance			1440	2100	pF
C_{oss}	Output Capacitance	$V_{DS}=-15\text{V}, V_{GS}=-0\text{V}, f=1\text{MHz}$		155	230	
C_{rss}	Reverse Transfer Capacitance			115	170	
$t_{d(on)}$	Turn-On Delay Time ^{2,3}			8.2	16	ns
t_r	Rise Time ^{2,3}	$V_{DD}=-10\text{V}, V_{GS}=-4.5\text{V}, R_G=25\Omega, I_D=-1\text{A}$		30	57	
$t_{d(off)}$	Turn-Off Delay Time ^{2,3}			71.1	135	
t_f	Fall Time ^{2,3}			19.8	38	
I_s	Continuous Source Current	$V_G=V_D=0\text{V}, \text{Force Current}$			-5.8	A
I_{SM}	Pulsed Source Current				-23.2	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}, I_S=-1\text{A}, T_J=25^\circ\text{C}$			-1	V

Note :

- 1.Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- 3.Essentially independent of operating temperature.

Typical Performance Characteristics

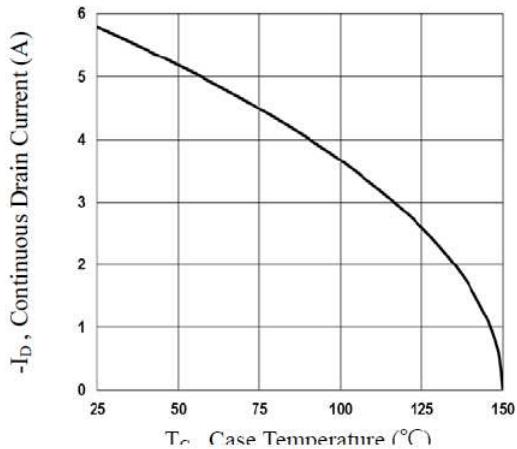


Fig.1 Continuous Drain Current vs. T_c

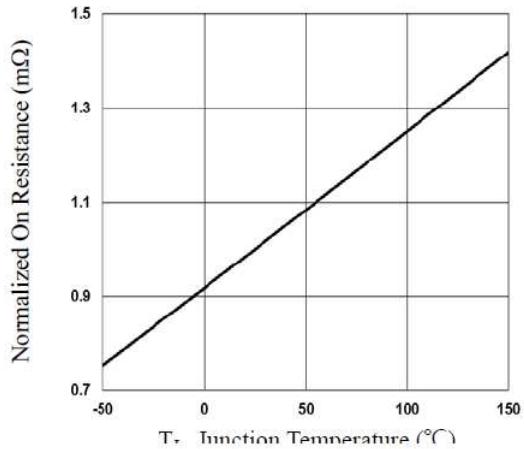


Fig.2 Normalized RD_{SON} vs. T_j

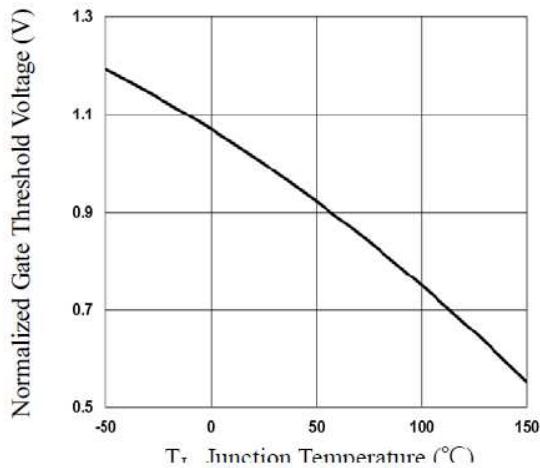


Fig.3 Normalized V_{th} vs. T_j

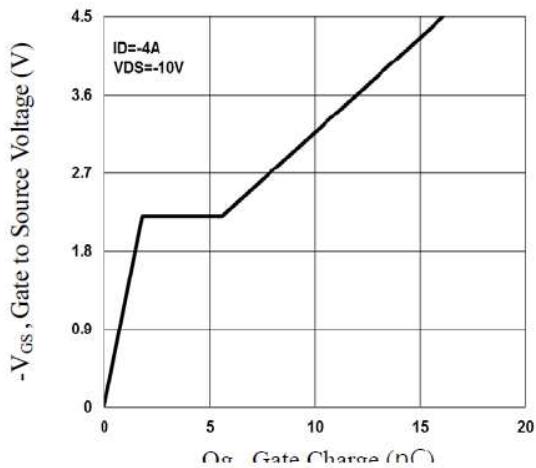


Fig.4 Gate Charge Waveform

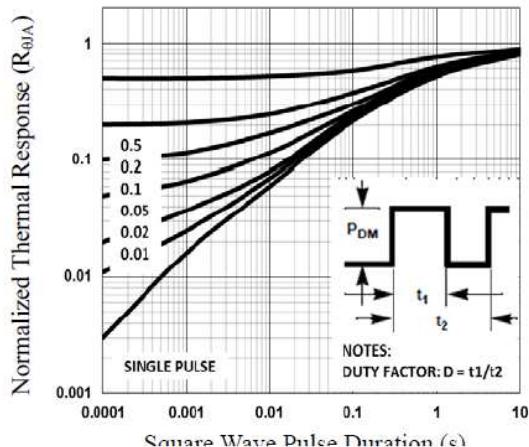


Fig.5 Normalized Transient Impedance

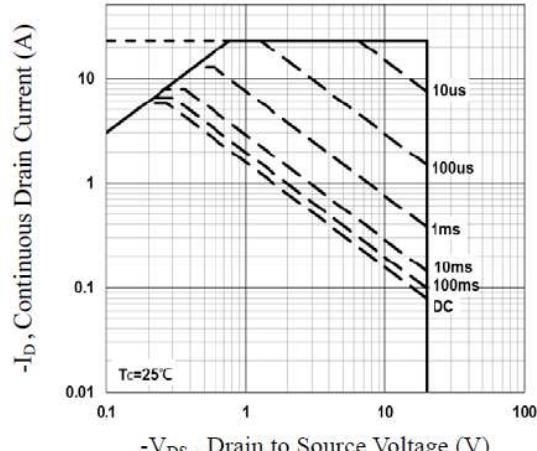


Fig.6 Maximum Safe Operation Area

Typical Performance Characteristics (continue)

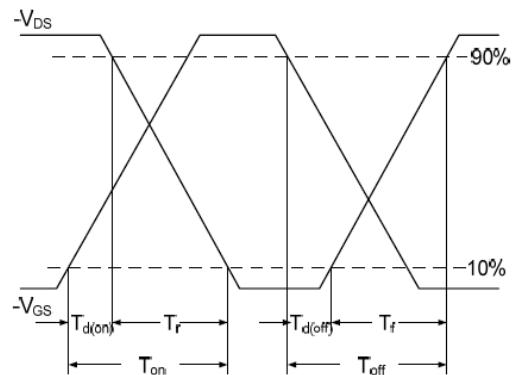


Fig.7 Switching Time Waveform

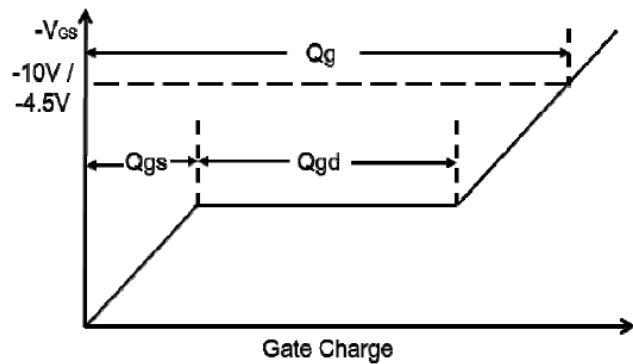
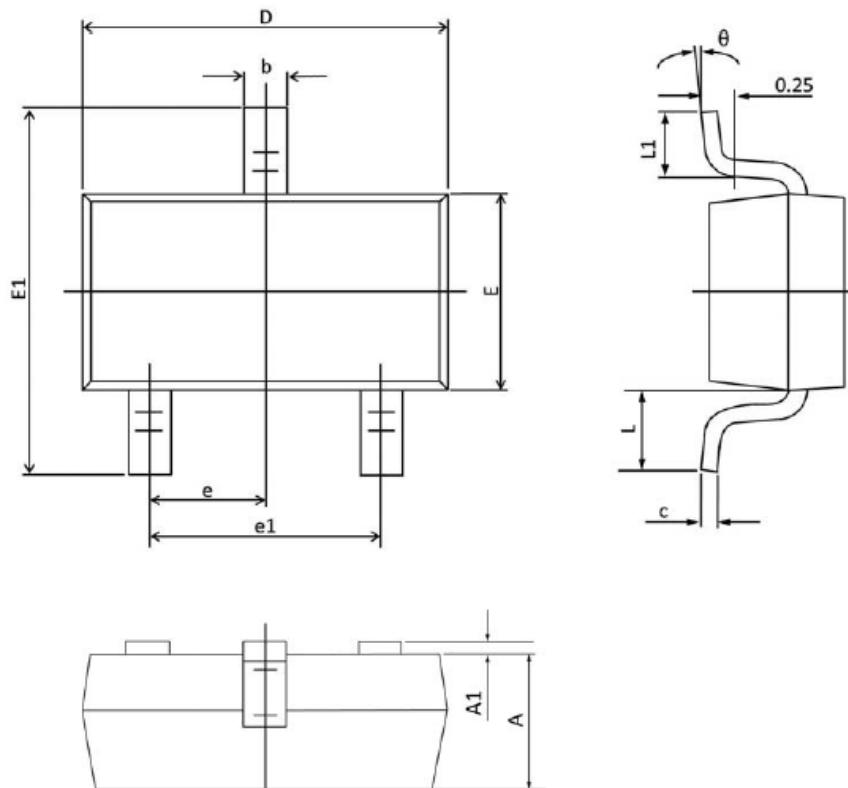


Fig.8 Gate Charge Waveform

GSM2309S

Package Dimension

SOT-23



Dimensions

SYMBOL	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	0.9	1.0	0.035	0.039
A1	0	0.1	0	0.004
b	0.3	0.5	0.012	0.02
c	0.09	0.11	0.003	0.004
D	2.8	3.0	0.11	0.118
E	1.2	1.4	0.047	0.055
E1	2.25	2.55	0.089	0.1
e	0.95 TYP.		0.037 TYP.	
e1	1.8	2.0	0.071	0.079
L	0.55 REF.		0.022 REF.	
L1	0.3	0.5	0.012	0.02
θ	1°	7°	1°	7°

GSM2309S

NOTICE

Information furnished is believed to be accurate and reliable. However Globaltech Semiconductor assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties, which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Globaltech Semiconductor. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information without express written approval of Globaltech Semiconductor.

CONTACT US

GS Headquarter



4F.,No.43-1,Lane11,Sec.6,Minquan E.Rd Neihu District Taipei City 114, Taiwan (R.O.C)



886-2-2657-9980



886-2-2657-3630



sales_twn@gs-power.com

RD Division



824 Bolton Drive Milpitas. CA. 95035



1-408-457-0587