

GSM3106ZF

30V N-Channel MOSFETs

Product Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

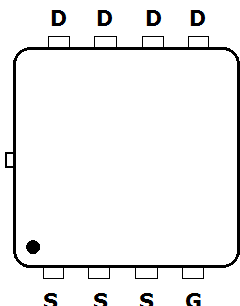
Features

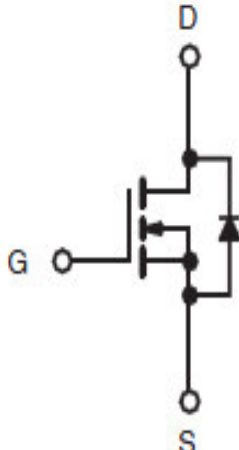
- 30V, 54A, $R_{DS(ON)}=6m\Omega@V_{GS}=10V$
- High Power and current handling capability
- Lead Free and Green Devices Available
- DFN3x3-8L package design

Applications

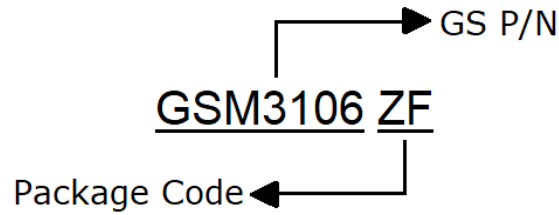
- MB / VGA / Vcore
- POL Applications
- SMPS 2nd SR

Packages & Pin Assignments

GSM3106ZF (DFN3x3-8L)	
 <p>Top View</p>	
Pin	Description
1	Source
2	Source
3	Source
4	Gate
5	Drain
6	Drain
7	Drain
8	Drain

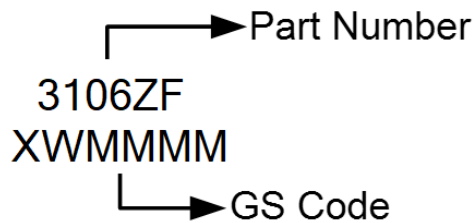


Ordering Information



Part Number	Package	Quantity
GSM3106ZF	DFN3x3-8L	5000pcs

Marking Information



Absolute Maximum Ratings

T_A=25°C Unless otherwise noted

Symbol	Parameter	Typical	Unit
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Continuous Drain Current	T _C =25°C ¹	54
		T _C =70°C	43
I _{DM}	Pulsed Drain Current ²	80	A
E _{AS}	Single Pulse Avalanche Energy ³	25	mJ
P _D	Power Dissipation T _C =25°C	26.6	W
	Power Dissipation T _C =70°C	17.1	W
T _J	Operating Junction Temperature Range	-55 to +150	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C
R _{θJC}	Thermal Resistance-Junction to Case	4.7	°C/W

Note :

- 1.The maximum current rating is package limited..
- 2.Repetitive Rating: Pulse width limited by maximum junctiontemperature.
- 3.E_{AS} condition: T_J=25°C, V_{DS}=30V, V_{GS}=10V, R_G=25Ω, L=0.5mH, I_{peak}=24A.

Electrical Characteristics

T_A=25°C Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	1.0		2.5	V
I _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±20V			±100	nA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =30V, V _{GS} =0V			1	uA
V _{SD}	Diode Forward Voltage ³	V _{GS} =0V, I _S =2A			1	V
R _{DS(on)}	Drain-Source On-Resistance ³	V _{GS} =10V, I _D =20A		4.8	6	mΩ
		V _{GS} =4.5V, I _D =10A		6.9	9	
Gate charge characteristics						
Q _g	Total Gate Charge ^{3,4}	V _{DD} =15V, I _D =9A		16.7		nC
Q _{gs}	Gate-Source Charge ^{3,4}			2.2		
Q _{gd}	Gate-Drain Charge ^{3,4}			3.5		
Dynamic characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1.0MHz		1155		pF
C _{oss}	Output Capacitance			456		
C _{rss}	Reverse Transfer Capacitance			72		
t _{d(on)}	Turn-On Time	V _{DD} =15V, V _{GS} =10V, R _g =3Ω, I _D =9A		3.5		ns
t _r	Rise Time			5.5		
t _{d(off)}	Turn-Off Time			13.5		
t _f	Fall Time			4.6		

Typical Performance Characteristics

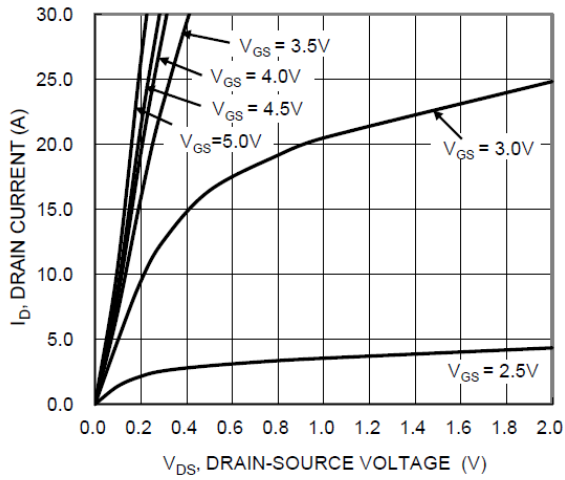


Figure 1. Output Characteristics

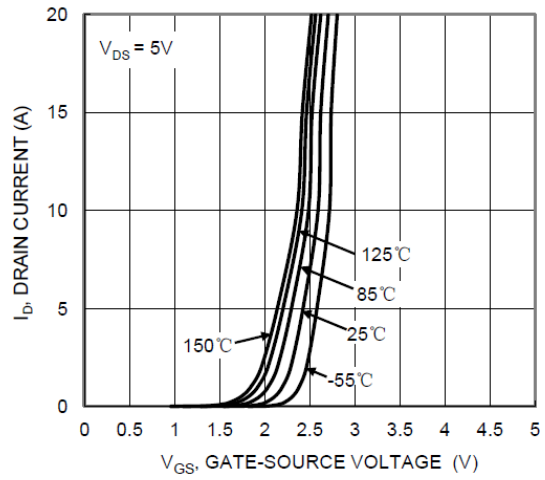


Figure 2. Transfer Characteristics

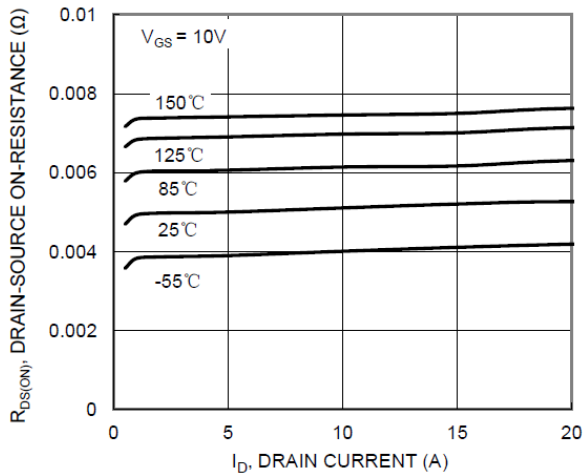


Figure 3. On-Resistance vs. Drain Current

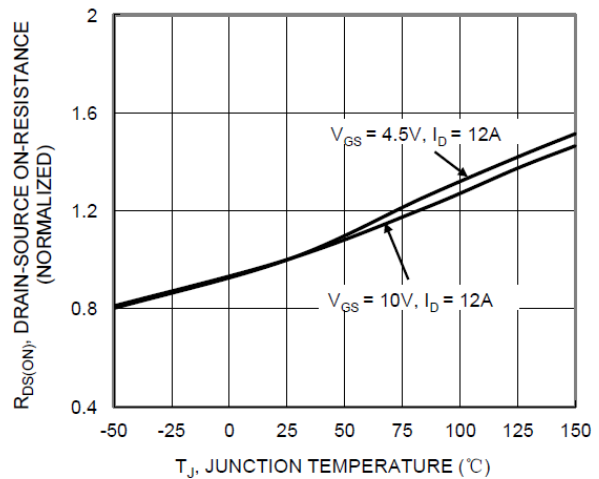


Figure 4. Normalized $R_{DS(on)}$ vs. T_J

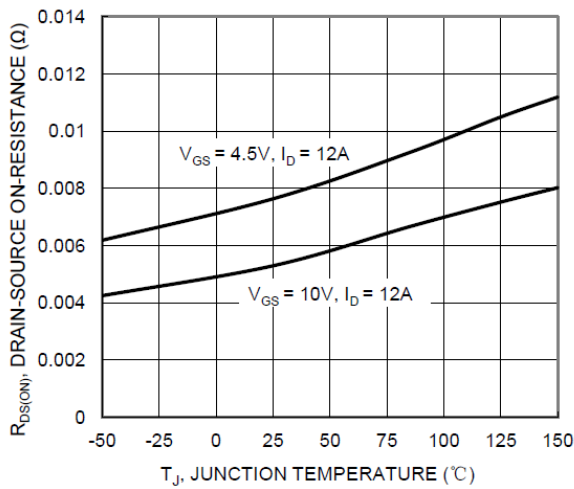


Figure 5. On-Resistance Variation with Temperature

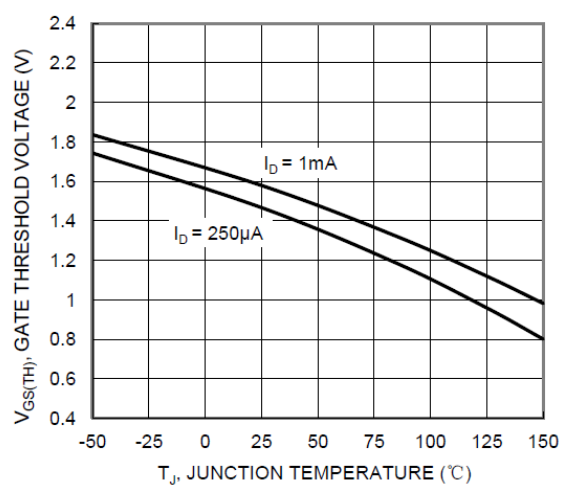


Figure 6. Gate Threshold Variation vs. T_J

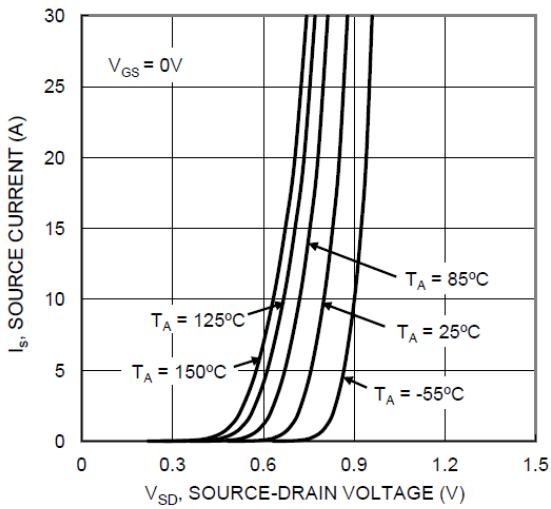


Figure 7. Diode Forward Voltage vs. Current

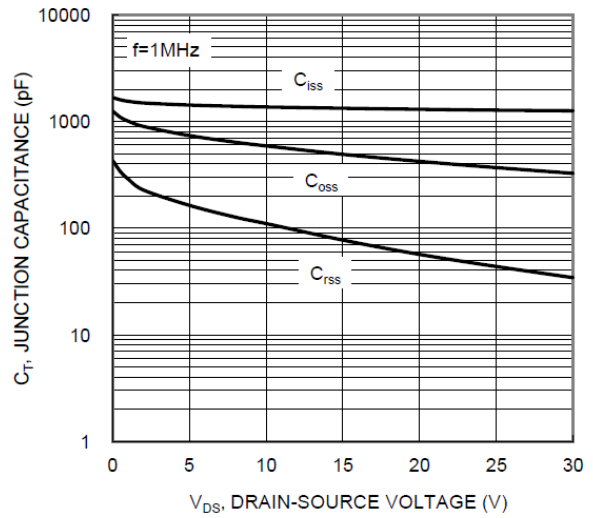


Figure 8. Capacitance

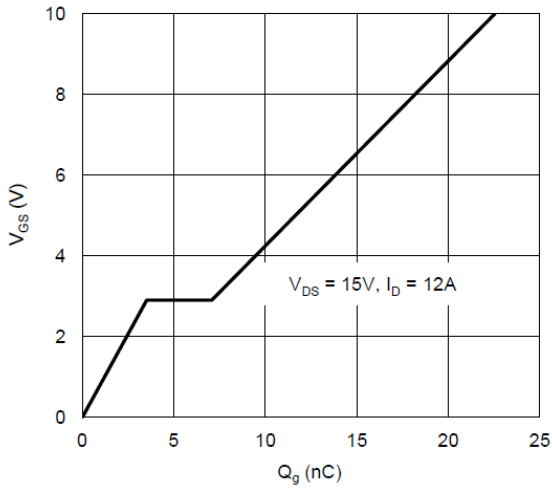


Figure 9. Gate Charge Waveform

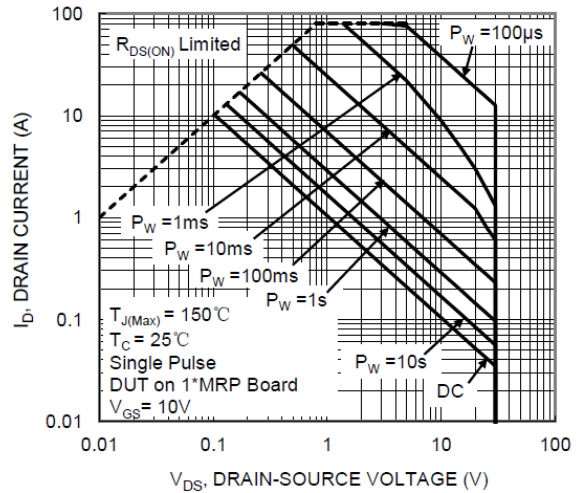


Figure 10. Maximum Safe Operating Area

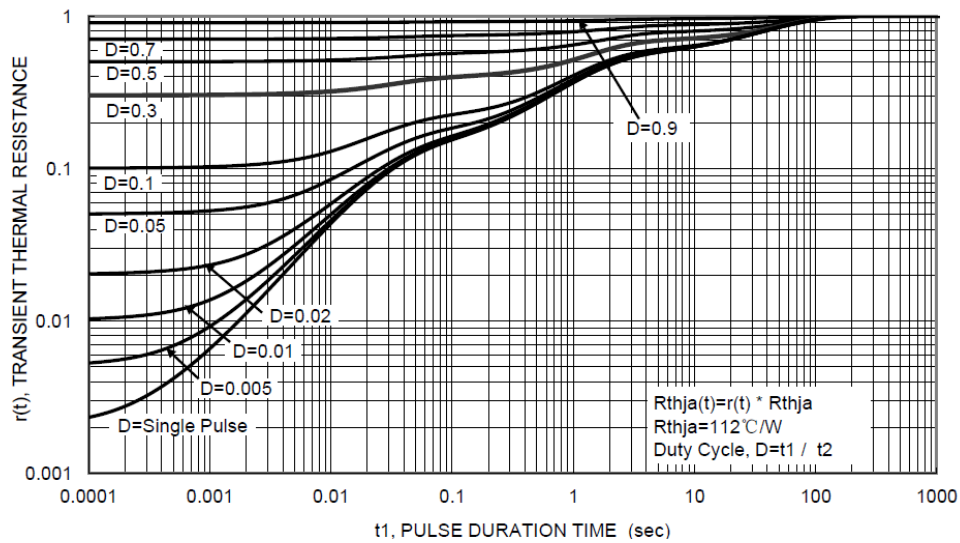
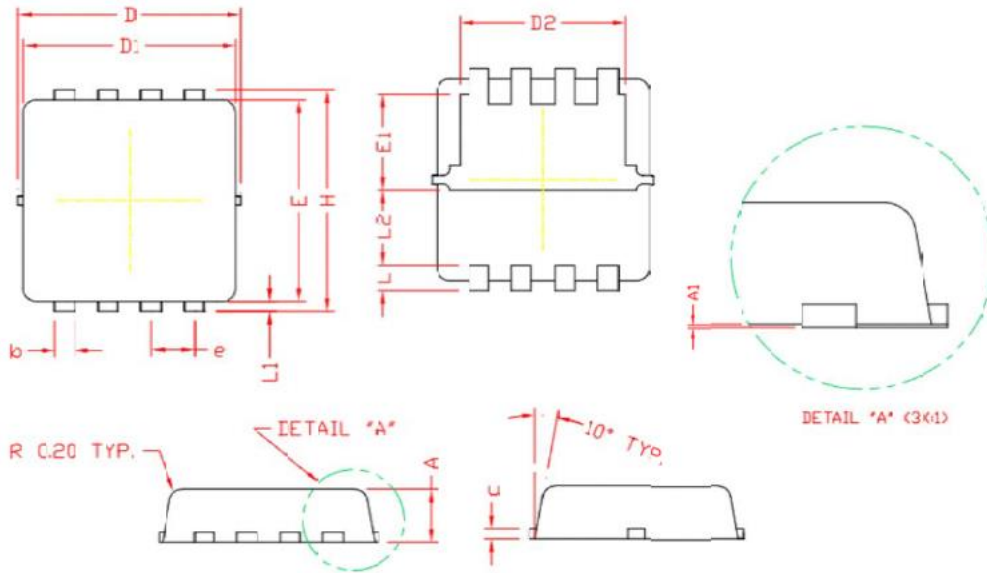


Figure 11. Normalized Transient Thermal Resistance

Package Dimension

DFN3x3-8L









Dimensions				
SYMBOL	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	0.700	0.900	0.028	0.035
A1	0.000	0.050	0.000	0.002
b	0.240	0.350	0.010	0.014
c	0.100	0.200	0.004	0.008
D	3.250	3.400	0.128	0.134
D1	3.050	3.250	0.120	0.120
D2	2.400	2.600	0.095	0.102
E	3.000	3.200	0.118	0.126
E1	1.350	1.550	0.053	0.061
e	0.650 BSC		0.026 BSC	
H	3.200	3.400	0.126	0.134
L	0.300	0.500	0.012	0.020
L1	0.100	0.200	0.004	0.008
L2	1.130 REF		0.045 REF	

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