

# GSM6332

## N & P Pair Enhancement Mode MOSFET

### Product Description

The GSM6332 is the N and P Pair enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge.

These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

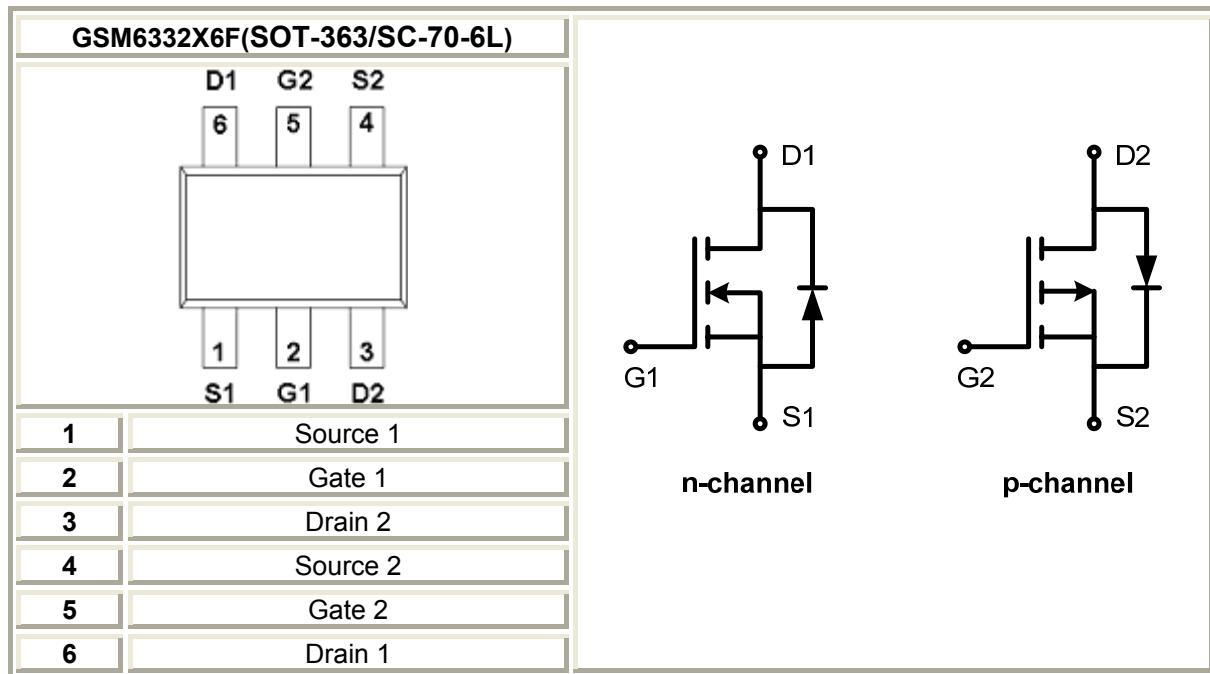
### Features

- N-Channel  
20V/1.0A, $R_{DS(ON)}=280m\Omega @ V_{GS}=4.5V$   
20V/0.8A, $R_{DS(ON)}=340m\Omega @ V_{GS}=2.5V$   
20V/0.7A, $R_{DS(ON)}=580m\Omega @ V_{GS}=1.8V$
- P-Channel  
-20V/-1.0A, $R_{DS(ON)}=600m\Omega @ V_{GS}=-4.5V$   
-20V/-0.8A, $R_{DS(ON)}=800m\Omega @ V_{GS}=-2.5V$   
-20V/-0.7A, $R_{DS(ON)}=1440m\Omega @ V_{GS}=-1.8V$
- Low-Voltage Operation
- High-Speed Circuits
- Low Battery Voltage Operation
- SOT-363 package design

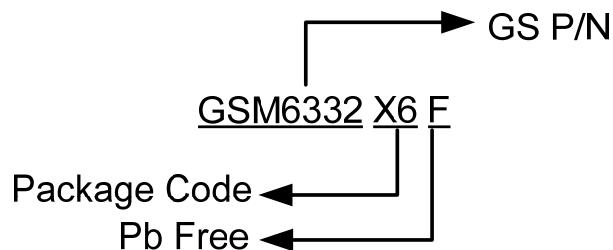
### Applications

- Load Switch for Portable Devices, Smart Phones, Pagers.

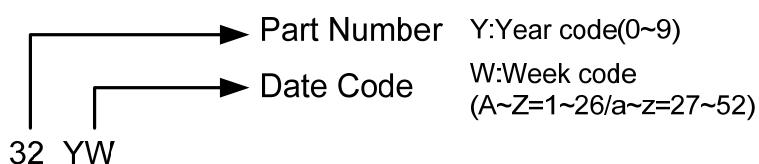
### Packages & Pin Assignments



## Ordering Information



## Marking Information



Part Number	Package	Part Marking
GSM6332X6F	SOT-363	32YW

## Absolute Maximum Ratings

TA=25°C unless otherwise noted

Symbol	Parameter	Typical		Unit
		N-Channel	P-Channel	
V <sub>DSS</sub>	Drain-Source Voltage	20	-20	V
V <sub>GSS</sub>	Gate-Source Voltage	±12	±12	V
I <sub>D</sub>	Continuous Drain Current(T <sub>J</sub> =150°C)	T <sub>A</sub> =25°C T <sub>A</sub> =70°C	1.0 0.7	A
I <sub>DM</sub>	Pulsed Drain Current	6	-6	A
I <sub>S</sub>	Continuous Source Current(Diode Conduction)	1	-1	A
PD	Power Dissipation	T <sub>A</sub> =25°C T <sub>A</sub> =70°C	0.3 0.2	W
T <sub>J</sub>	Operating Junction Temperature		-55/150	°C
T <sub>STG</sub>	Storage Temperature Range		-55/150	°C

## Electrical Characteristics

TA=25°C unless otherwise noted

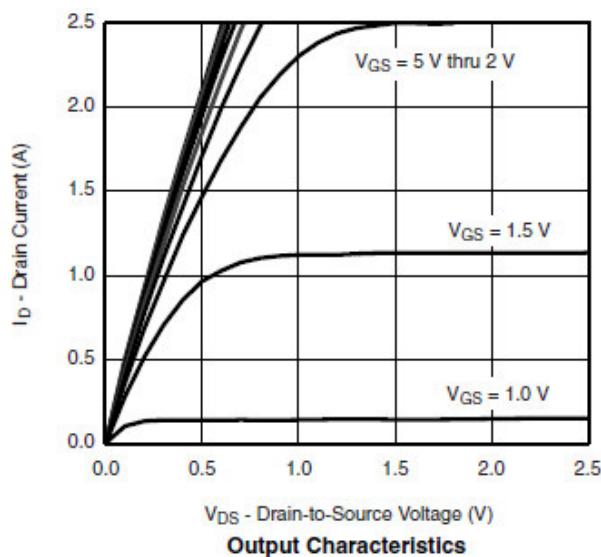
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	N-Ch	20		
		$V_{GS}=0V, I_D=-250\mu A$	P-Ch	-20		
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	N-Ch	0.4	1.0	V
		$V_{DS}=V_{GS}, I_D=-250\mu A$	P-Ch	-0.4	-1.0	
$I_{GSS}$	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 12V$	N-Ch		$\pm 100$	nA
		$V_{DS}=0V, V_{GS}=\pm 12V$	P-Ch		$\pm 100$	
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=20V, V_{GS}=0V$	N-Ch		1	
		$V_{DS}=-20V, V_{GS}=0V$	P-Ch		-1	
		$V_{DS}=20V, V_{GS}=0V T_J=85^\circ C$	N-Ch		5	
		$V_{DS}=-20V, V_{GS}=0V T_J=85^\circ C$	P-Ch		-5	
$I_{D(on)}$	On-State Drain Current	$V_{DS}\geq 5V, V_{GS}=4.5V$	N-Ch	1.2		
		$V_{DS}\leq -5V, V_{GS}=4.5V$	P-Ch	0.7		
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=4.5V, I_D=1.0A$	N-Ch		280	
		$V_{GS}=-4.5V, I_D=-1.0A$	P-Ch		600	
		$V_{GS}=2.5V, I_D=0.8A$	N-Ch		340	mΩ
		$V_{GS}=-2.5V, I_D=-0.8A$	P-Ch		800	
		$V_{GS}=1.8V, I_D=0.7A$	N-Ch		580	
		$V_{GS}=-1.8V, I_D=-0.7A$	P-Ch		1440	
$g_{fs}$	Forward Transconductance	$V_{DS}=10V, I_D=1.0A$	N-Ch		1	
		$V_{DS}=-10V, I_D=-0.4A$	P-Ch		1	
$V_{SD}$	Diode Forward Voltage	$I_S=1.0A, V_{GS}=0V$	N-Ch		0.65	A
		$I_S=-0.15A, V_{GS}=0V$	P-Ch		-0.65	
<b>Dynamic</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1MHz$	N-Ch		70	
		$V_{DS}=-10V, V_{GS}=0V, f=1MHz$	P-Ch		70	
$C_{oss}$	Output Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1MHz$	N-Ch		20	pF
		$V_{DS}=-10V, V_{GS}=0V, f=1MHz$	P-Ch		20	
$C_{rss}$	Reverse Transfer Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1MHz$	N-Ch		8	
		$V_{DS}=-10V, V_{GS}=0V, f=1MHz$	P-Ch		10	
$Q_g$	Total Gate Charge	$N\text{-Channel}$ $V_{DS}=10V, V_{GS}=4.5V, I_D=1.2A$  $P\text{-Channel}$ $V_{DS}=-10V, V_{GS}=-4.5V, I_D=-0.25A$	N-Ch		1.06	1.38
$Q_{gs}$	Gate-Source Charge		P-Ch		1.0	1.3
$Q_{gd}$	Gate-Drain Charge		N-Ch		0.18	
			P-Ch		0.1	
			N-Ch		0.32	
			P-Ch		0.3	

## Electrical Characteristics (Continue)

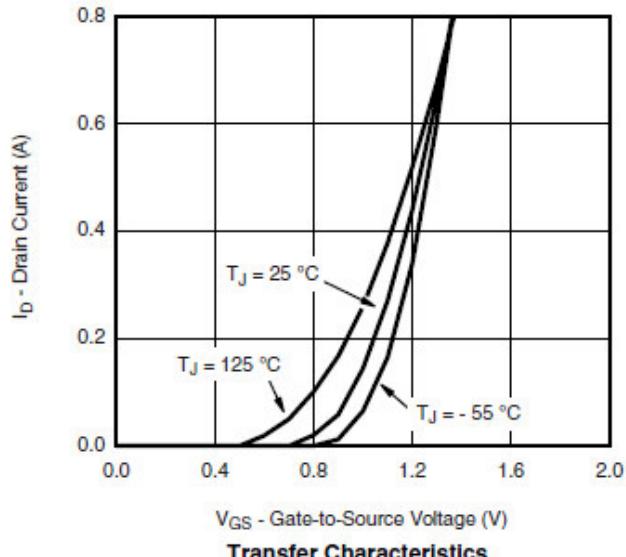
TA=25°C unless otherwise noted

td(on)	Turn-On Time	N-Channel $V_{DD}=10V, R_L=20\Omega, I_D=1.2A$ $V_{GEN}=4.5V, R_G=1\Omega$	18	26	ns
tr			10	15	
td(off)	Turn-Off Time	P-Channel $V_{DD}=-10V, R_L=30\Omega, I_D=-0.2A$ $V_{GEN}=-4.5V, R_G=10\Omega$	20	28	
tf			10	15	
		N-Ch	70	110	
		P-Ch	40	60	
		N-Ch	25	40	
		P-Ch	30	50	

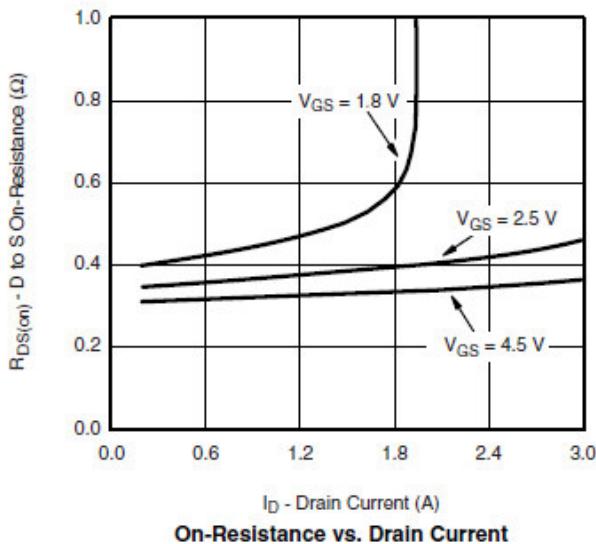
## Typical Performance Characteristics (N-Channel)



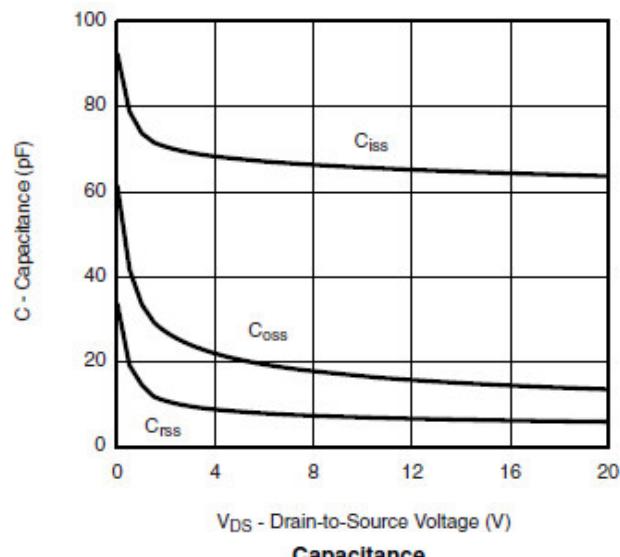
Output Characteristics



Transfer Characteristics

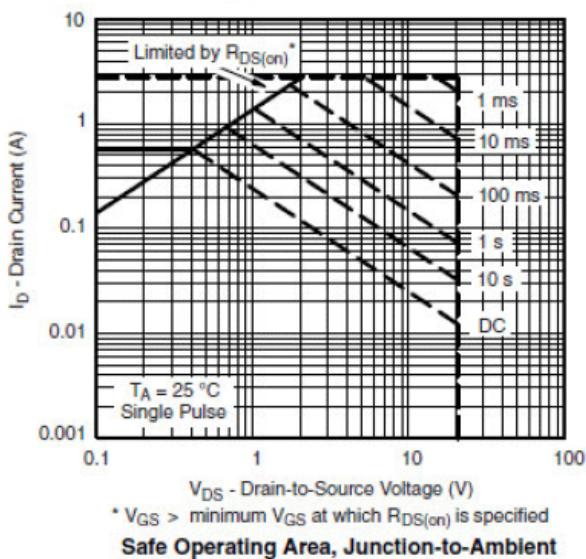
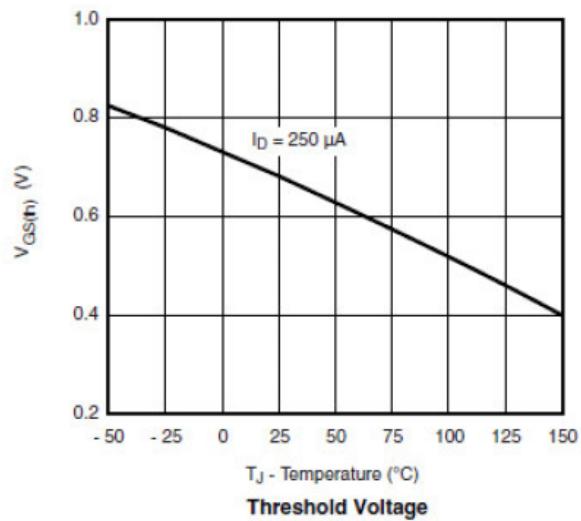
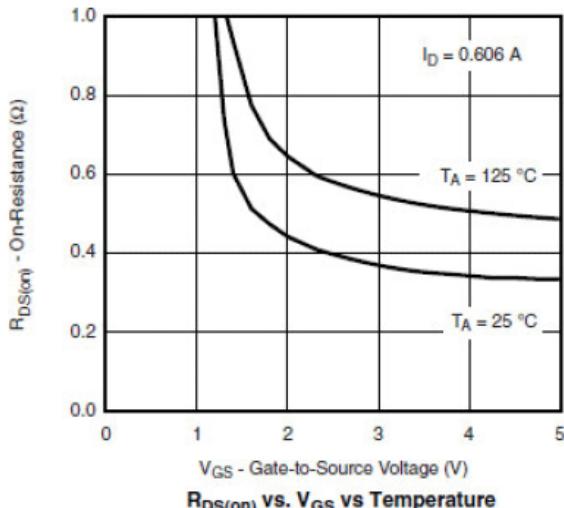
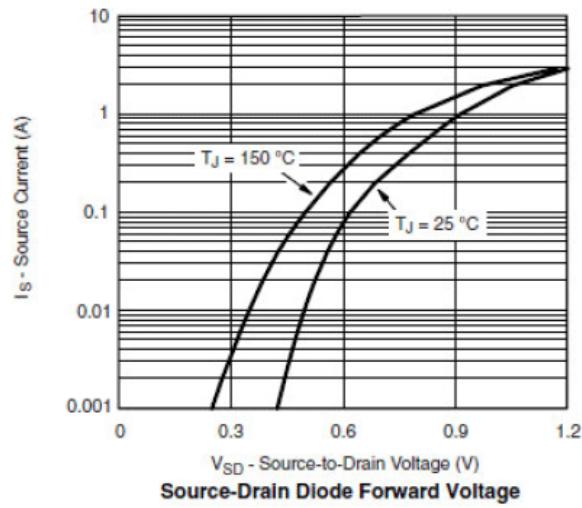
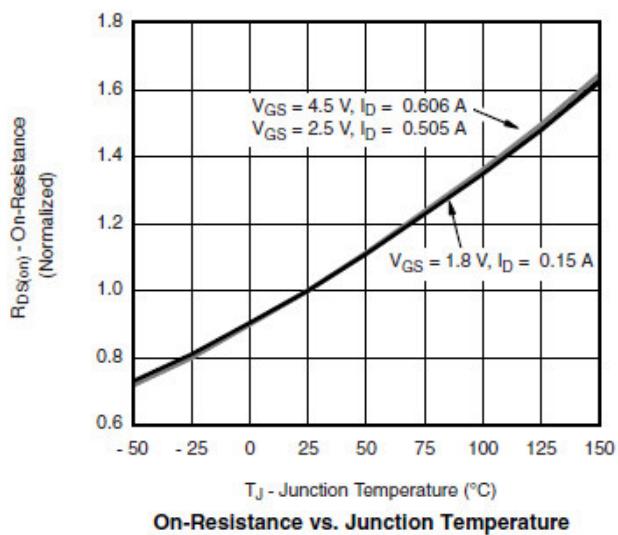
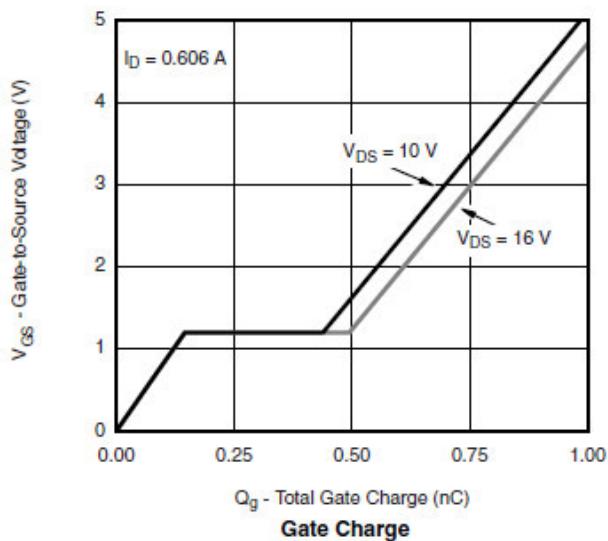


On-Resistance vs. Drain Current

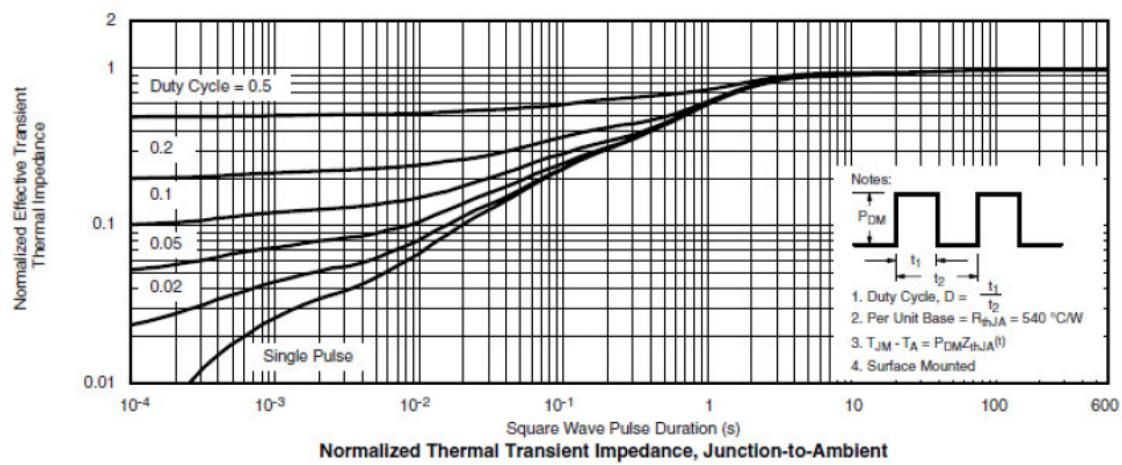


Capacitance

## Typical Performance Characteristics (N-Channel)



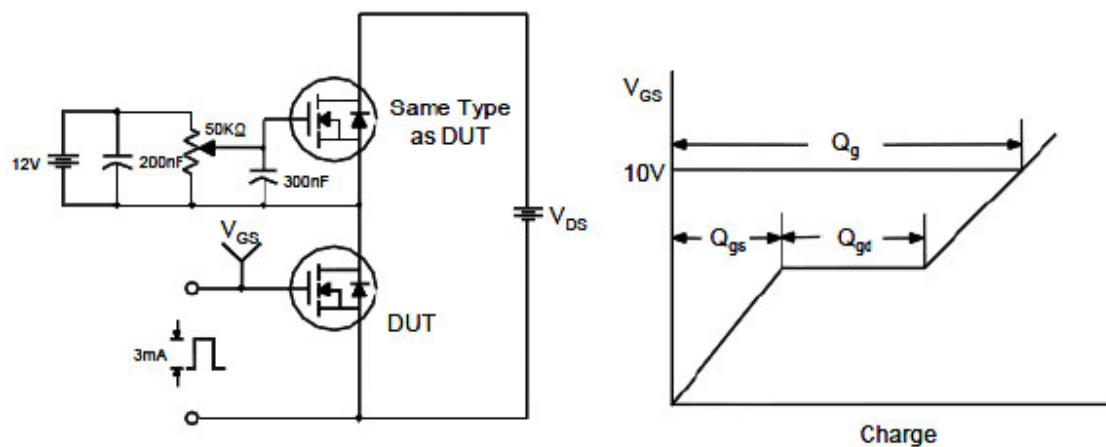
## Typical Performance Characteristics (N-Channel)



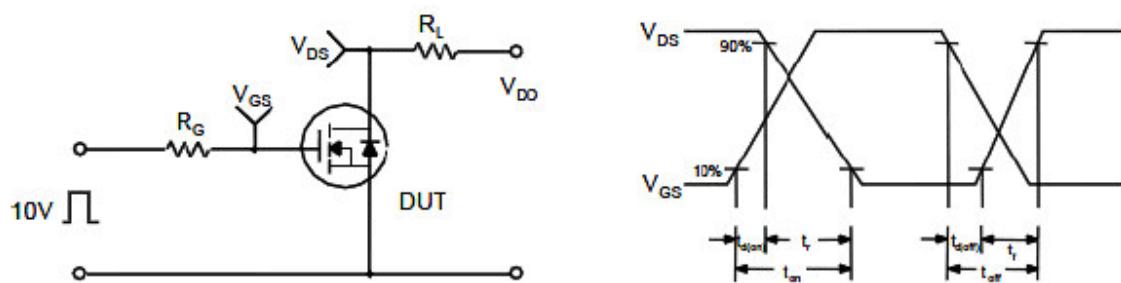
GSM6332

## Typical Performance Characteristics (N-Channel)

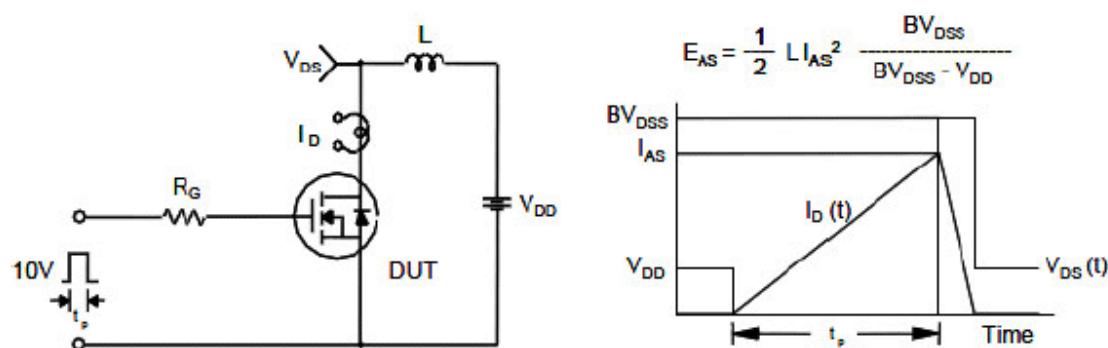
Gate Charge Test Circuit & Waveform



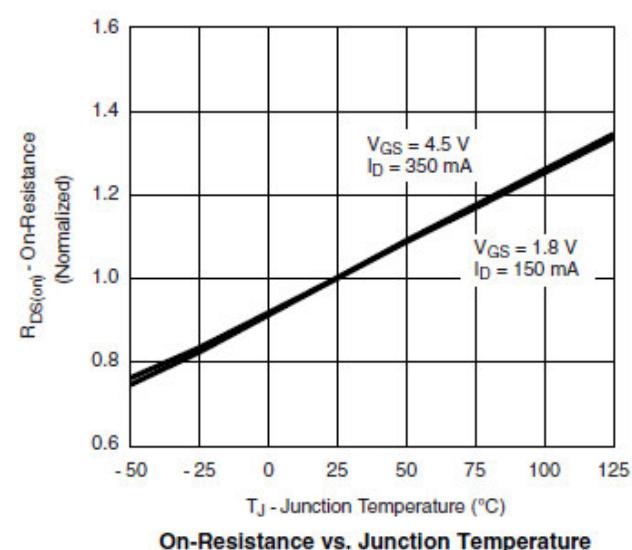
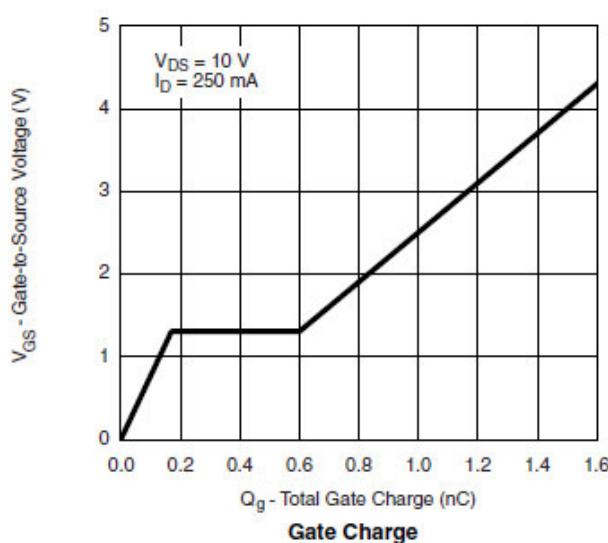
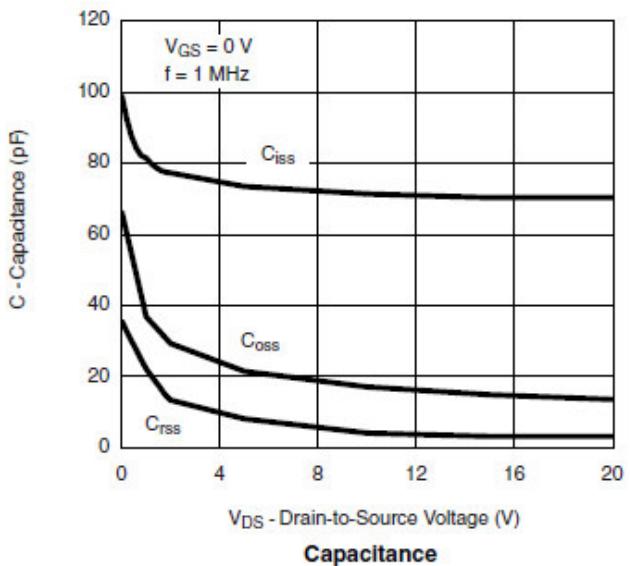
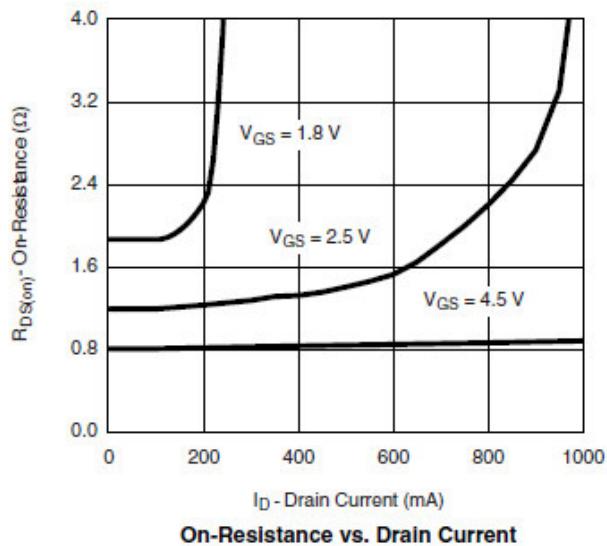
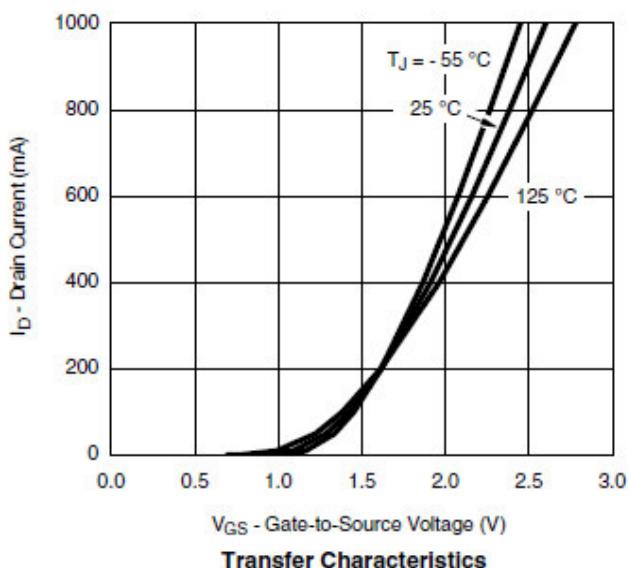
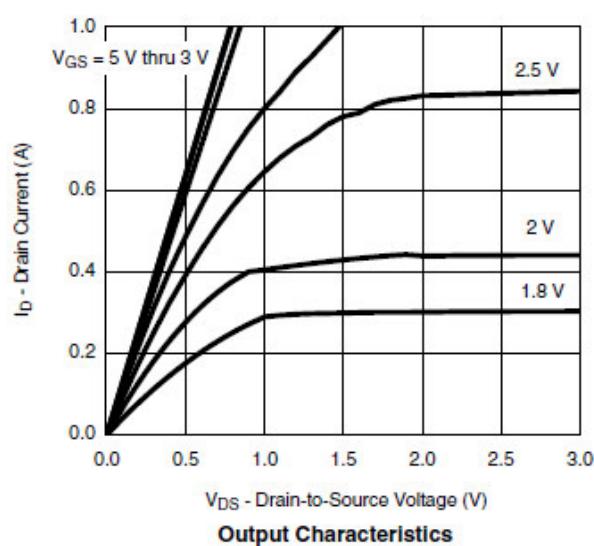
Resistive Switching Test Circuit & Waveforms



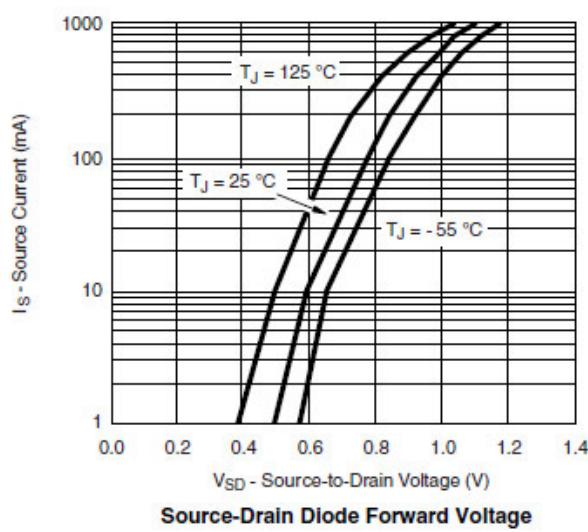
Unclamped Inductive Switching Test Circuit & Waveforms



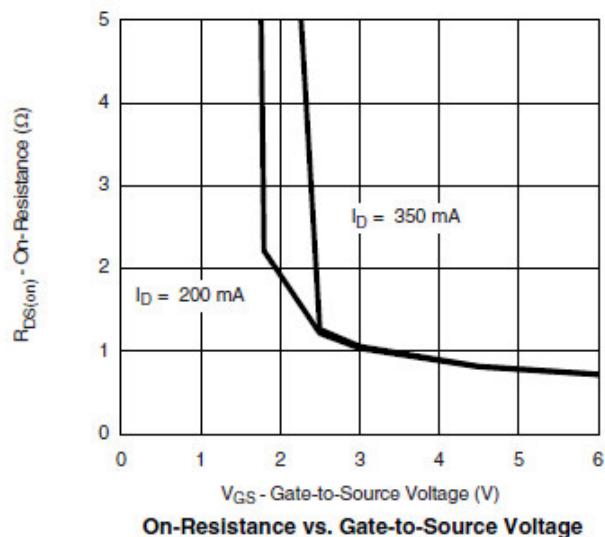
## Typical Performance Characteristics (P-Channel)



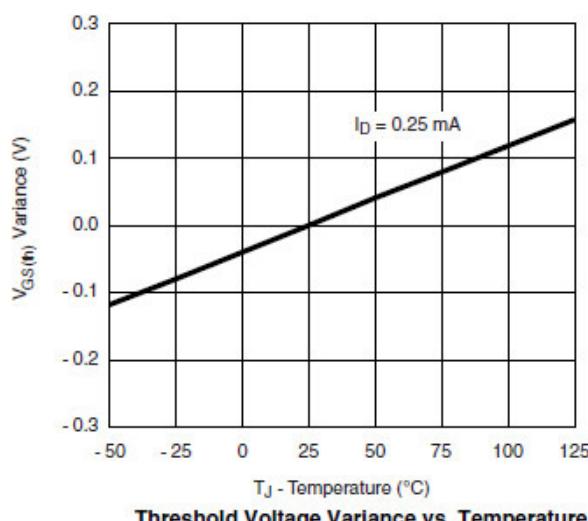
## Typical Performance Characteristics (P-Channel)



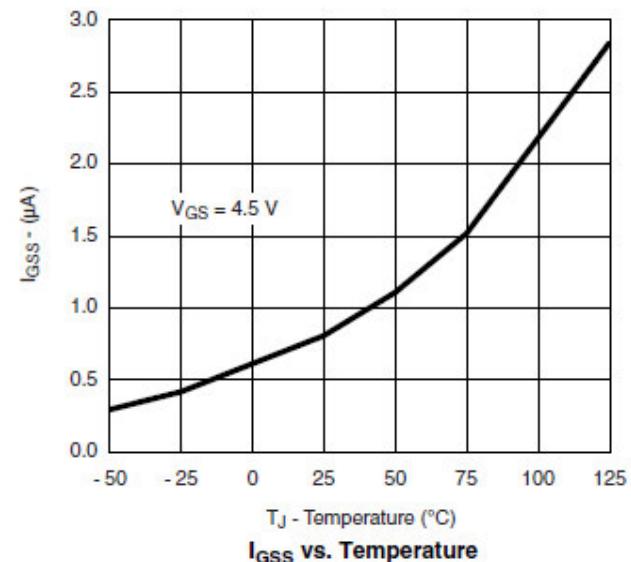
Source-Drain Diode Forward Voltage



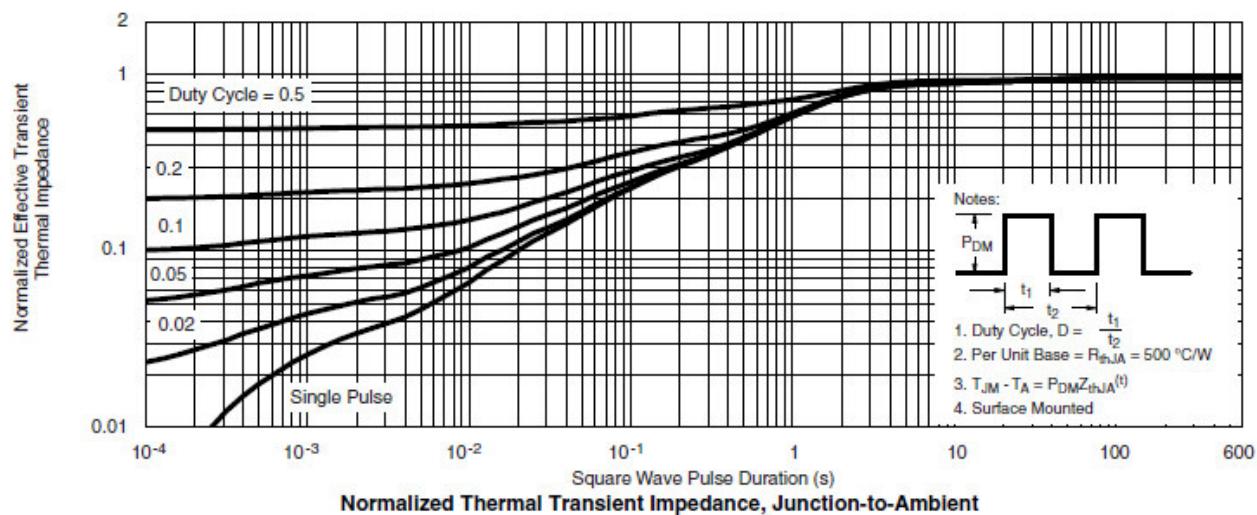
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage Variance vs. Temperature

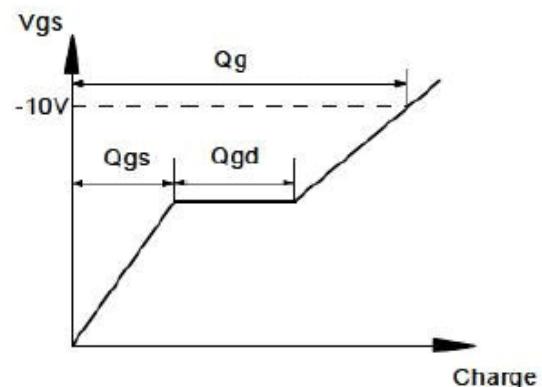
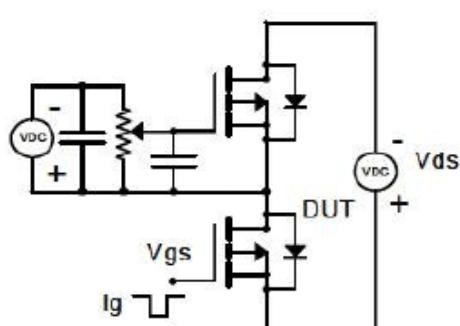


$I_{DSS}$  vs. Temperature

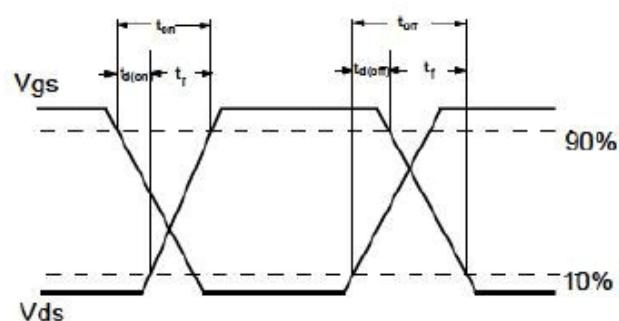
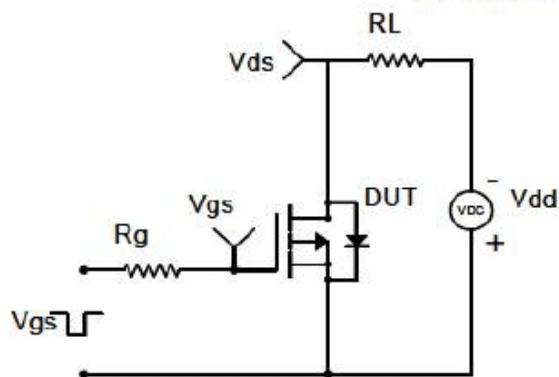


## Typical Performance Characteristics (P-Channel)

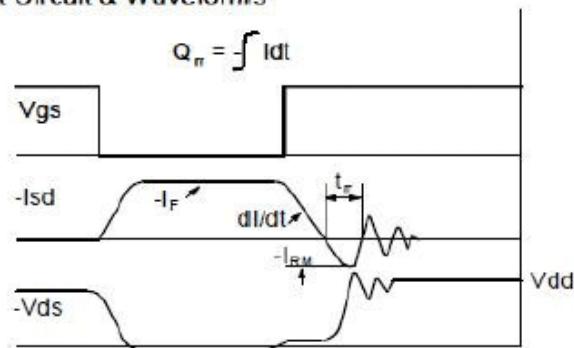
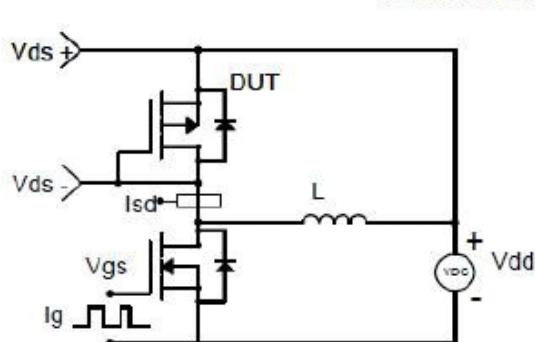
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

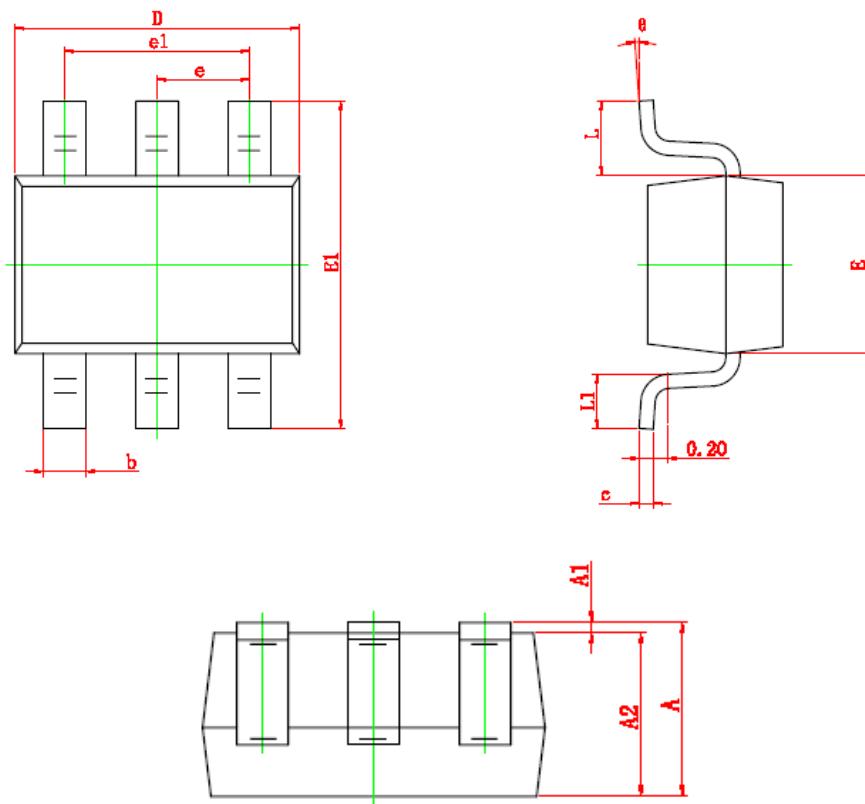


Diode Recovery Test Circuit & Waveforms



## Package Dimension

### SOT-363



### Dimensions

Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 (TYP)		0.026 (TYP)	
e1	1.200	1.400	0.047	0.055
L	0.525 (REF)		0.021 (REF)	
L1	0.260	0.460	0.010	0.018
θ	0°	8 °	0°	8 °

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