

GSMBSS139Y

Dual N-Channel Enhancement MOSFET

Product Description

These Dual N-Channel Enhancement Mode Power Field Effect Transistors are Using Trench DMOS Technology. This Advanced Technology has been Especially Tailored to Minimize on-state Resistance, Provide Superior Switching Performance, and Withstand high Energy Pulse in the Avalanche and Commutation mode.

These Devices are well Suited for High Efficiency Fast Switching Applications.

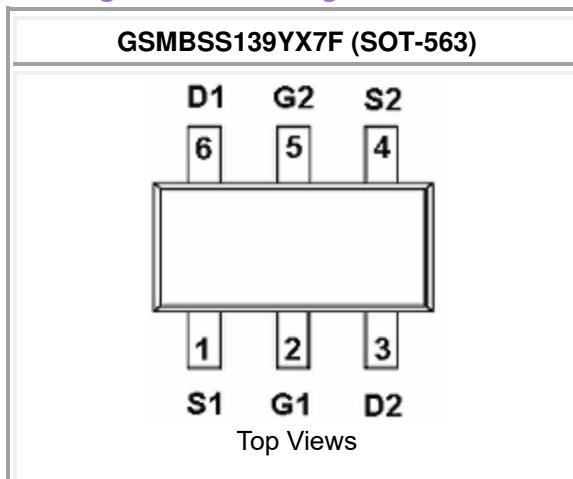
Features

- 60V, 0.2A, $R_{DS(ON)}=2.5\Omega@V_{GS}=4.5V$
- Improved dv/dt Capability
- Fast Switching
- Green Device Available
- SOT-563 Package Design
- ESD Protected : 1500V

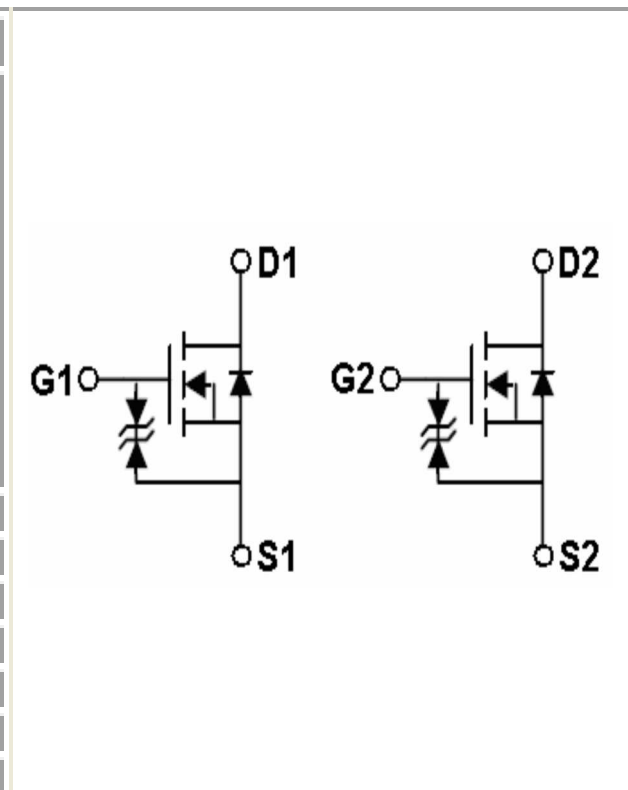
Applications

- Notebook
- Load Switch
- LED Applications

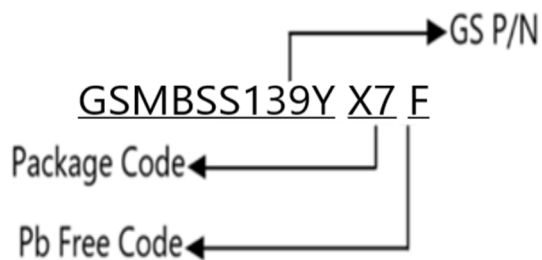
Packages & Pin Assignments



Pin	Description
1	Source 1
2	Gate 1
3	Drain 2
4	Source 2
5	Gate 2
6	Drain 1

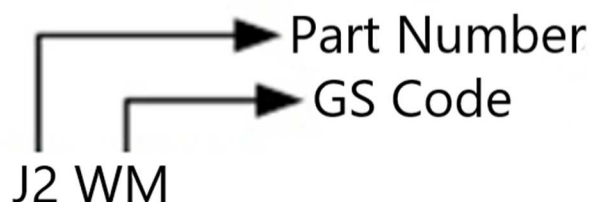


Ordering Information



Part Number	Package	Quantity
GSMBSS139YX7F	SOT-563	3000pcs

Marking Information



Part Number	Package	Part Marking
GSMBSS139YX7F	SOT-563	J2WM

Absolute Maximum Ratings

$T_A=25^{\circ}\text{C}$ Unless otherwise noted

Symbol	Parameter	Limits	Unit
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current $T_A=25^{\circ}\text{C}$	0.2	A
I_{DM}	Pulsed Drain Current	0.8	A
P_D	Power Dissipation ($T_A=25^{\circ}\text{C}$)	0.225	W
	Power Dissipation (Derate above 25°C)	0.0018	W/ $^{\circ}\text{C}$
T_J	Operating Junction Temperature Range	-55 to +150	$^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-55 to +150	$^{\circ}\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	556	$^{\circ}\text{C}/\text{W}$
T_L	Maximum Lead Temperature for Soldering Purpose, for 10 Seconds	260	$^{\circ}\text{C}$

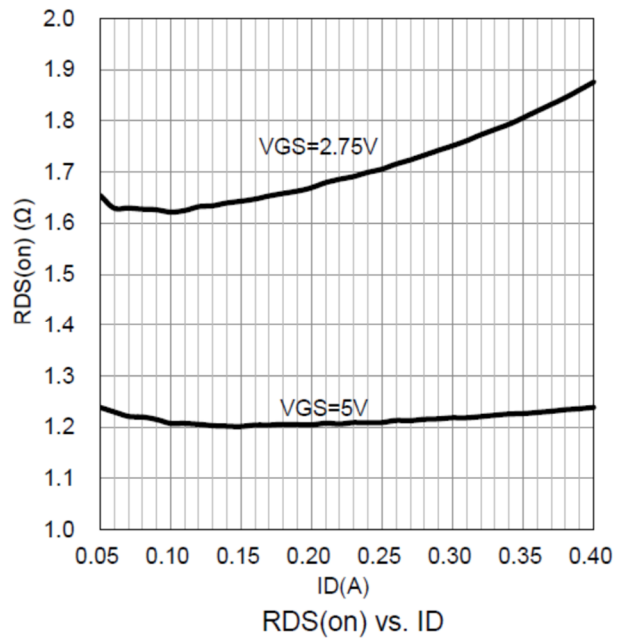
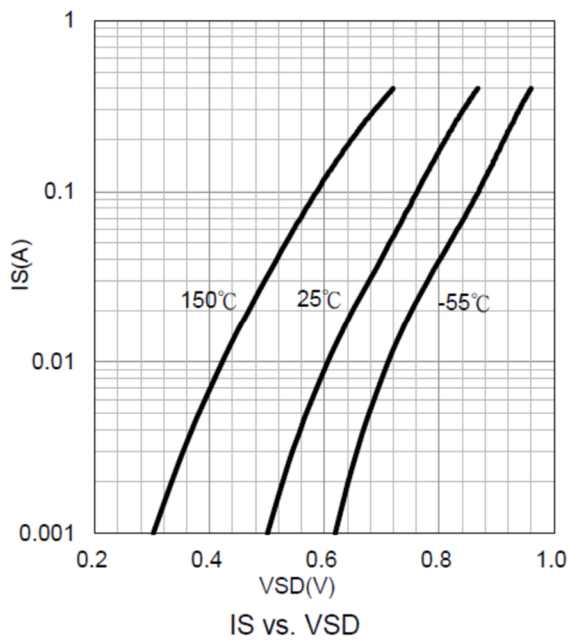
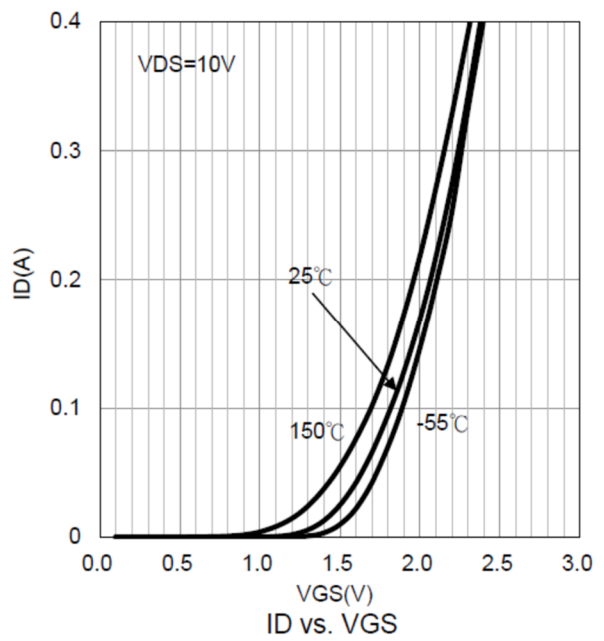
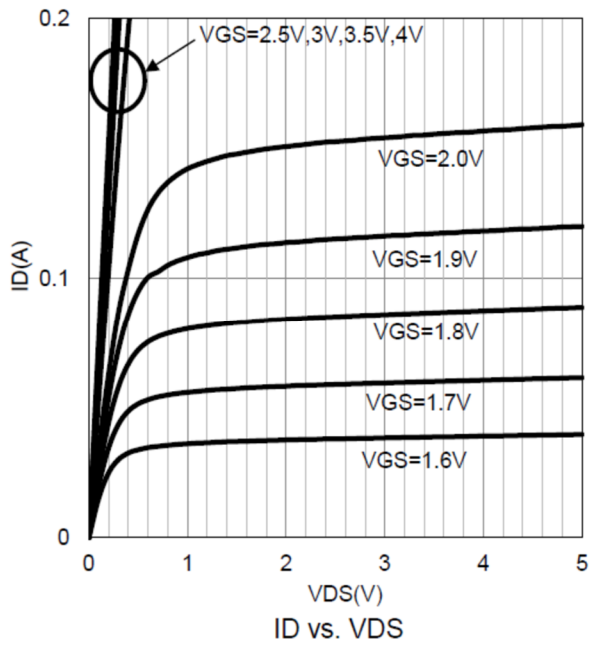
Electrical Characteristics

T_A=25°C Unless otherwise noted

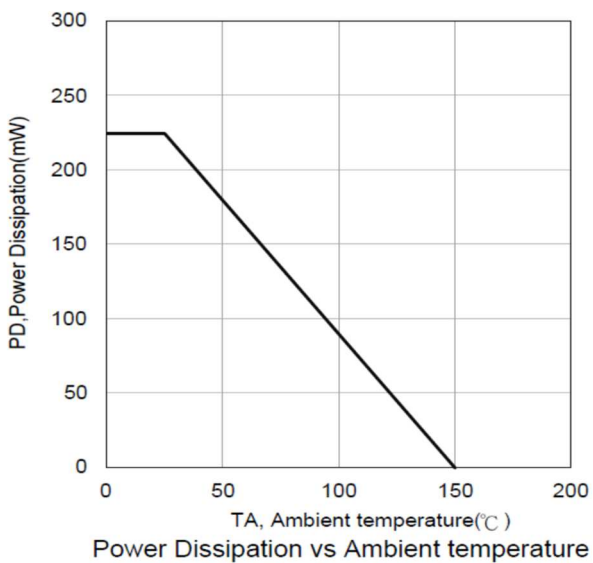
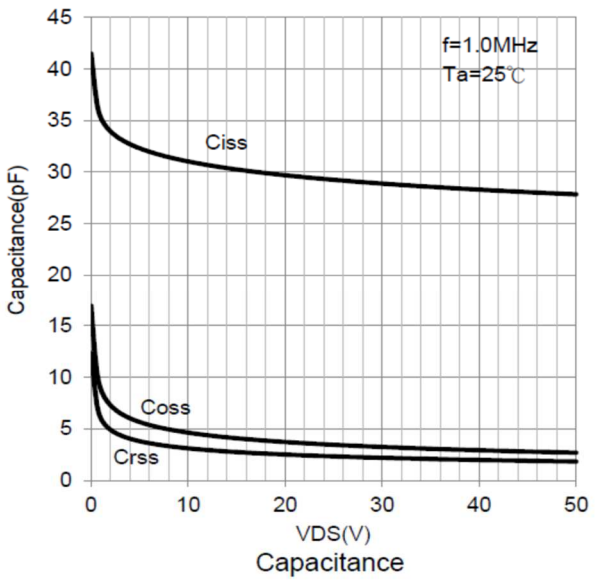
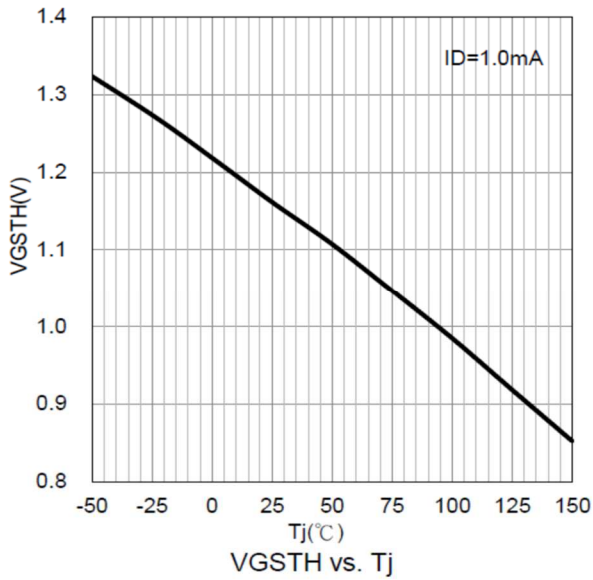
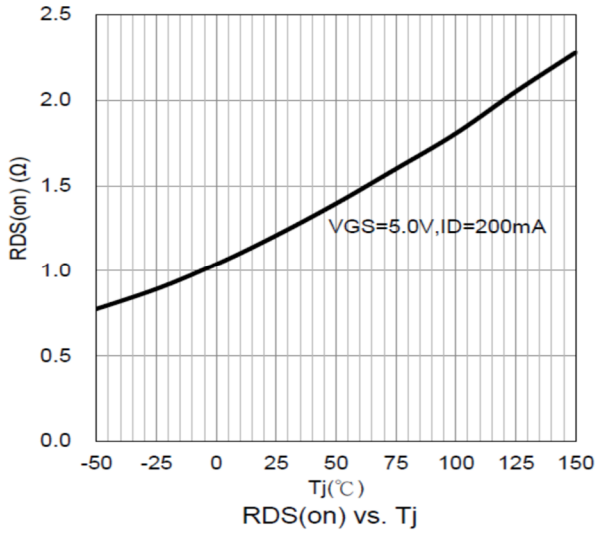
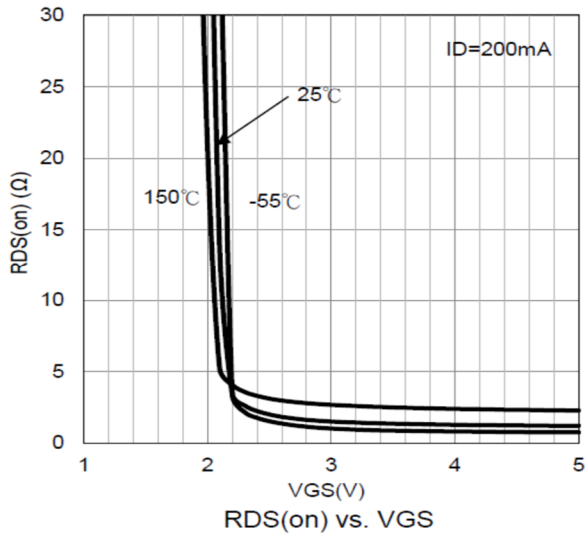
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	60	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.85	-	1.45	V
I _{GSSF}	Gate Leakage Current · Forward	V _{DS} =0V, V _{GS} =20V			10	μA
I _{GSSR}	Gate Leakage Current · Reverse	V _{DS} =0V, V _{GS} =-20V			-10	μA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =25V, V _{GS} =0V			0.1	uA
		V _{DS} =50V, V _{GS} =0V,			0.5	
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =4.5V, I _D =0.2A		-	2.25	Ω
		V _{GS} =2.5V, I _D =0.1A	-	-	4.05	
g _{FS}	Forward Transconductance	V _{DS} =25V, I _D =0.2A	100	-	-	mS

Dynamic						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz		22.8		pF
C _{oss}	Output Capacitance			3.5		
C _{rss}	Reverse Transfer Capacitance			2.9		
t _{d(on)}	Turn-On Time	V _{DD} =30V, I _D =1A, V _{GS} =10V, R _G =25Ω		3.8		ns
t _{d(off)}	Turn-Off Time			19		

Typical Performance Characteristics

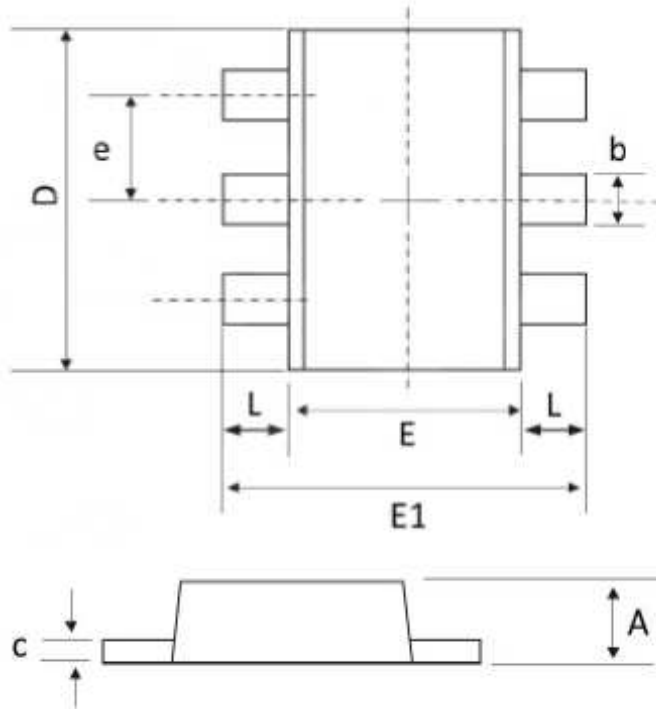


Typical Performance Characteristics (Continue)



Package Dimension

SOT-563









Dimensions				
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.500	0.600	0.020	0.024
b	0.150	0.300	0.006	0.012
c	0.080	0.160	0.003	0.006
D	1.500	1.700	0.059	0.067
E	1.100	1.300	0.043	0.051
E1	1.500	1.700	0.059	0.067
e	0.5(BSC)		0.02(BSC)	
L	0.100	0.300	0.004	0.012

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